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# P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
-40	3 at V <sub>GS</sub> = - 10 V	400	80 nC			
-40	4 at V <sub>GS</sub> = - 4.5 V	-130	60 HC			

#### **FEATURES**

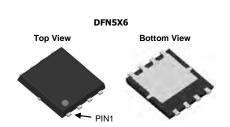
- DT-Trench Power MOSFET
- Low Gate Charge
- $\bullet$  100 % R  $_{\rm g}$  and UIS Tested

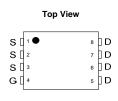


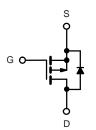
# RoHS

#### **APPLICATIONS**

- PWM Applications
- Load Switch
- Power Management







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	$V_{DS}$	- 40	V		
Gate-Source Voltage	$V_{GS}$	± 20	V		
Continuous Drain Current	T <sub>C</sub> = 25 °C	I-	- 130		
Continuous Drain Current	T <sub>C</sub> = 100 °C	I <sub>D</sub>	- 95		
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	- 125	Α		
Pulsed Drain Current <sup>a</sup>	I <sub>DM</sub>	- 520			
Single Pulse Avalanche Current	ent L = 0.1 mH		- 120		
Single Pulse Avalanche Energy	L=0.1 IIII	E <sub>AS</sub>	150	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	D.	90	W	
iviaximum rower bissipation	T <sub>C</sub> = 100 °C	P <sub>D</sub>	36	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Case (Drain) <sup>C,d</sup>	RthJC	1.39	°C/W		

#### Notes:

- a. BasedonTC= 25°C.
- b. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-40			V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1		-3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Brain Current		$V_{DS} = -40  \text{V},  V_{GS} = 0  \text{V}$			-1	μА	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C		-20			
Davis Occurs Oc Otata Davista and	P	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 20 A		3	3.8	<b></b>	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 20 A		4	5.5	mΩ	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = -10 V,I <sub>D</sub> = - 20 A		40		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			10400		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		818			
Reverse Transfer Capacitance	C <sub>rss</sub>			961			
Total Gate Charge	Qg			80		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$		12			
Gate-Drain Charge	$Q_{gd}$			7.5			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.5		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15			
Rise Time	t <sub>r</sub>	$V_{DS}$ = -20 V, $R_L$ = 4 $\Omega$		90		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -20 \text{A},  V_{GS} = -10 \text{V},   R_g = 6 \Omega$		110			
Fall Time	t <sub>f</sub>			85			
<b>Drain-Source Body Diode Characteristics</b>	;			•			
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>A</sub> = 25 °C			- 130	Α	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 520	А	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = - 1 A			- 1.2	V	
Body Diode Reverse Recovery Time t <sub>rr</sub>		00 A 11/11 400 A/ T 55 50		30		ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$I_F$ = - 20 A, di/dt = 100 A/ $\mu$ s, $T_J$ = 25 °C		20		nC	

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

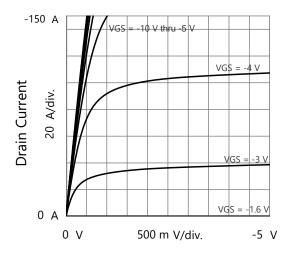
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

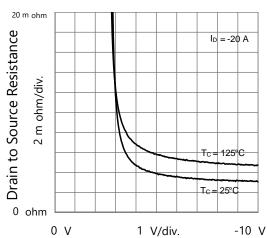


#### TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

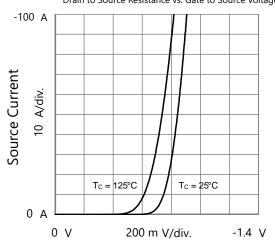
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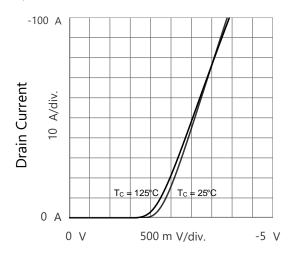
Drain to Source Voltage Output Characteristics



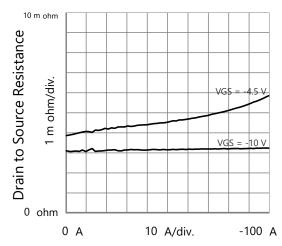
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



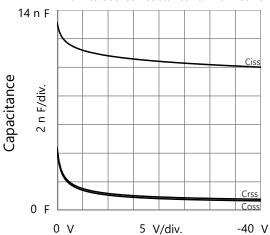
Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



Drain Current
Drain to Source Resistance vs. Drain Current



Drain to Source Voltage Capacitances

TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

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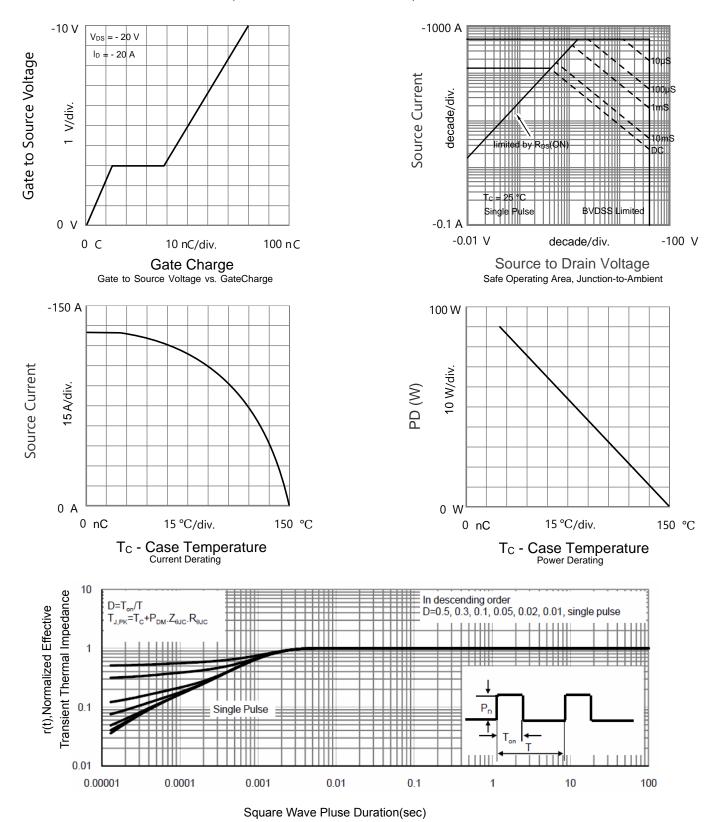
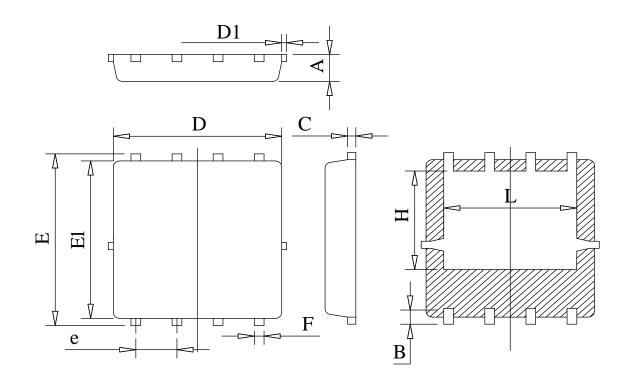


Figure 13 Normalized Maximum Transient Thermal Impedance



### **DFN5X6-8L PACKAGE OUTLINE**



# COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit: mm

Symbol	Min	Тур	Max
A	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25





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