

P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I_D (A) ^a	Q_g (Typ.)
-40	3 at $V_{GS} = -10$ V	-130	80 nC
	4 at $V_{GS} = -4.5$ V		

FEATURES

- DT-Trench Power MOSFET
- Low Gate Charge
- 100 % R_g and UIS Tested
- AEC-Q101 Qualified for Automotive Applications

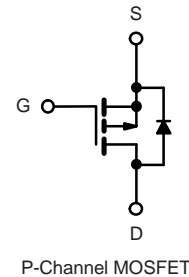
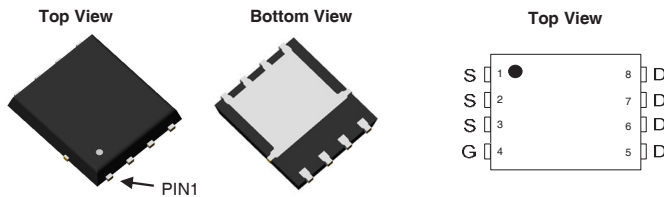


RoHS
COMPLIANT

APPLICATIONS

- PWM Applications
- Load Switch
- Power Management

DFN5X6-8L Pin Configuration



ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	- 40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	$T_C = 25$ °C	- 130
		$T_C = 100$ °C	- 98
Continuous Source Current (Diode Conduction)	I_S	- 125	A
Pulsed Drain Current ^a	I_{DM}	- 520	
Single Pulse Avalanche Current	I_{AS}	- 120	mJ
Single Pulse Avalanche Energy	E_{AS}	150	
Maximum Power Dissipation ^b	P_D	$T_C = 25$ °C	90
		$T_C = 100$ °C	43
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Case (Drain) ^{c,d}	R_{thJC}	1.39	°C/W

Notes:

- Based on $T_C = 25$ °C.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

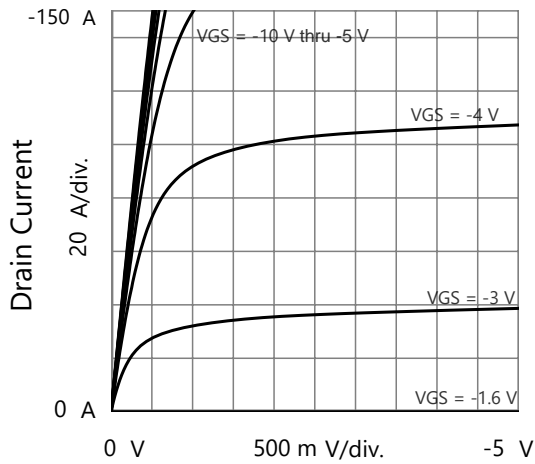
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-40			V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1		-3	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -40\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$		-20		
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -20\text{ A}$		3	3.8	m Ω
		$V_{GS} = -4.5\text{ V}, I_D = -20\text{ A}$		4	5.5	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\text{ V}, I_D = -20\text{ A}$		40		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		10400		μF
Output Capacitance	C_{oss}			818		
Reverse Transfer Capacitance	C_{riss}			961		
Total Gate Charge	Q_g	$V_{DS} = -20\text{ V}, V_{GS} = -10\text{ V}, I_D = -20\text{ A}$		80		nC
Gate-Source Charge	Q_{gs}			12		
Gate-Drain Charge	Q_{gd}			7.5		
Gate Resistance	R_g	$f = 1\text{ MHz}$		1.5		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -20\text{ V}, R_L = 4\text{ }\Omega$ $I_D \cong -20\text{ A}, V_{GS} = -10\text{ V}, R_g = 6\text{ }\Omega$		15		ns
Rise Time	t_r			90		
Turn-Off Delay Time	$t_{d(off)}$			110		
Fall Time	t_f			85		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_A = 25\text{ }^\circ\text{C}$			-130	A
Pulse Diode Forward Current ^a	I_{SM}				-520	
Body Diode Voltage	V_{SD}	$I_S = -1\text{ A}$			-1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		30		ns
Body Diode Reverse Recovery Charge	Q_{rr}				20	

Notes:

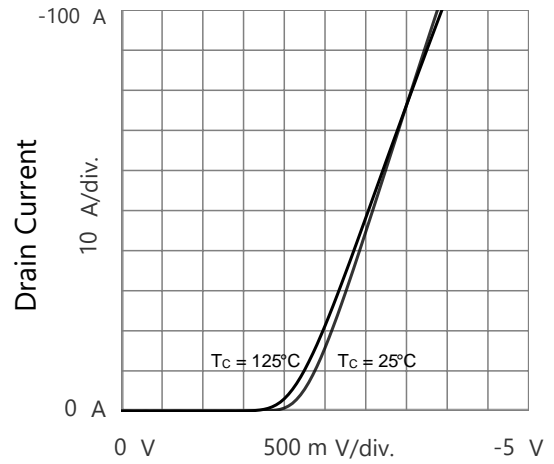
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

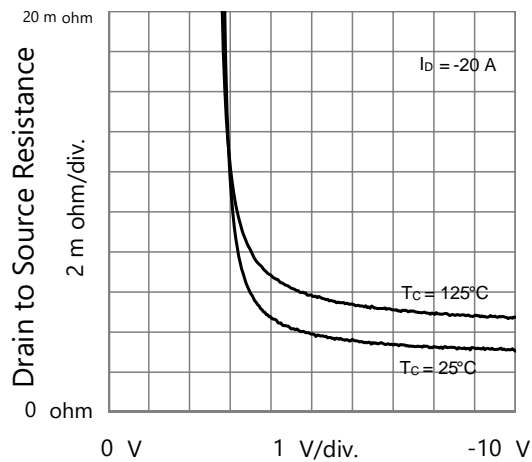
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



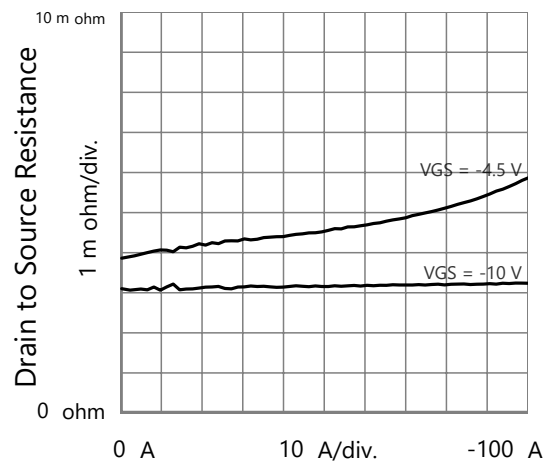
Drain to Source Voltage
Output Characteristics



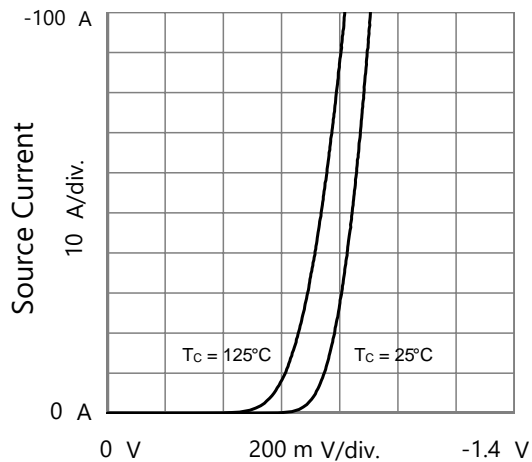
Gate to Source Voltage
Transfer Characteristics



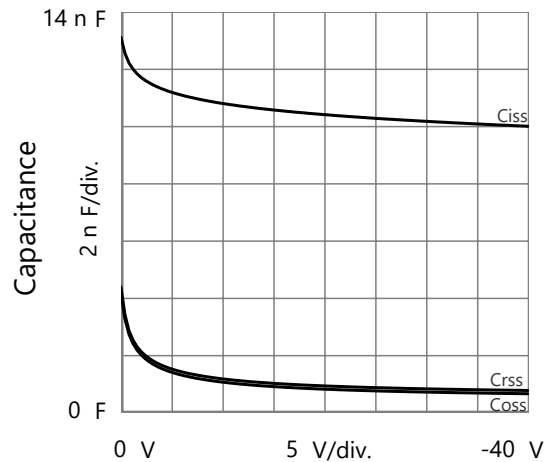
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current



Source to Drain Voltage
Body Diode Forward Characteristics



Drain to Source Voltage
Capacitances

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

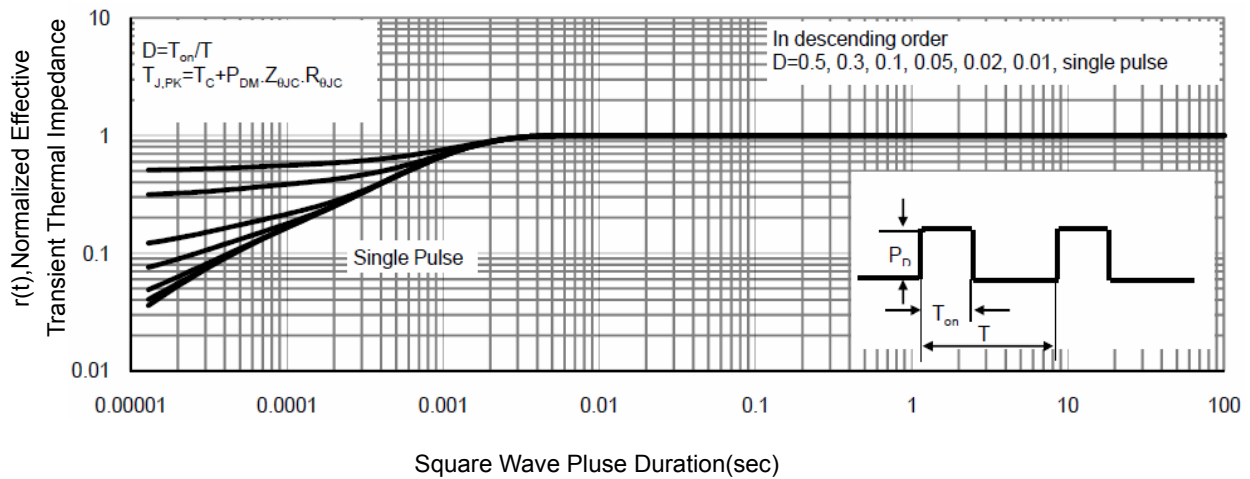
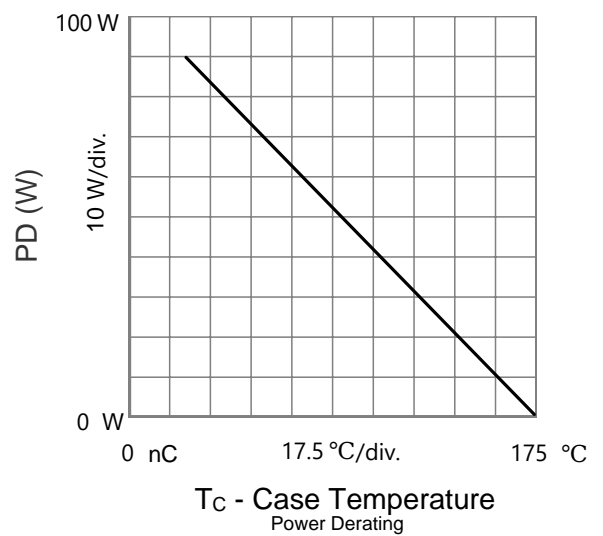
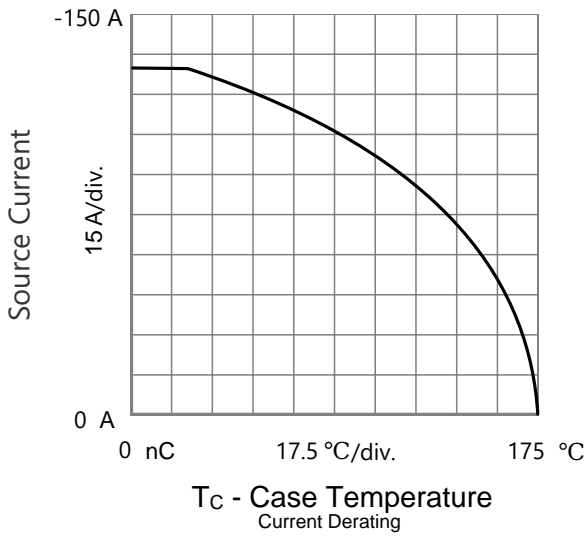
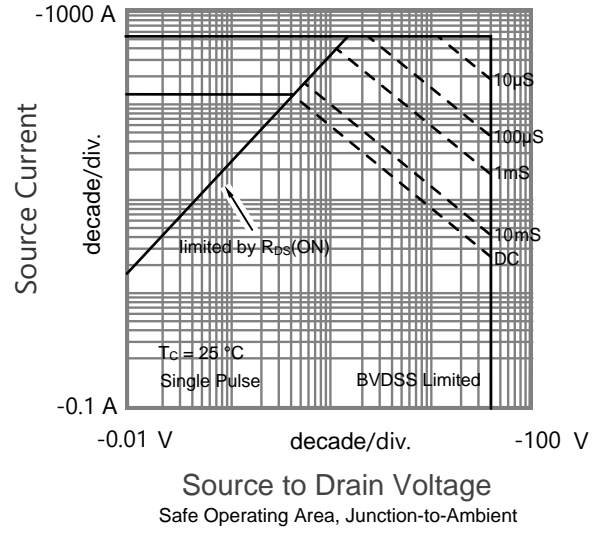
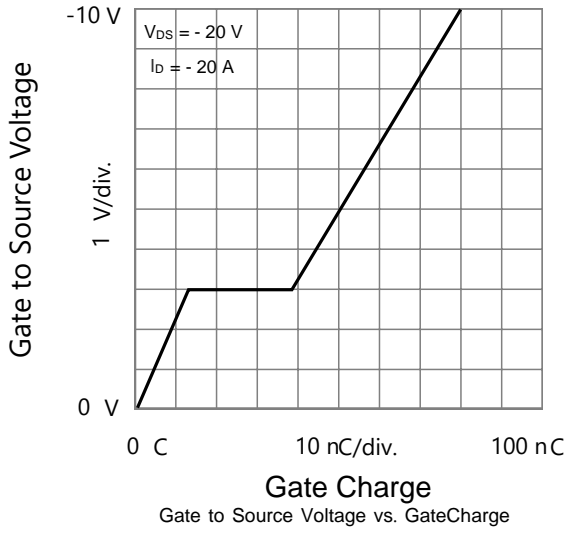
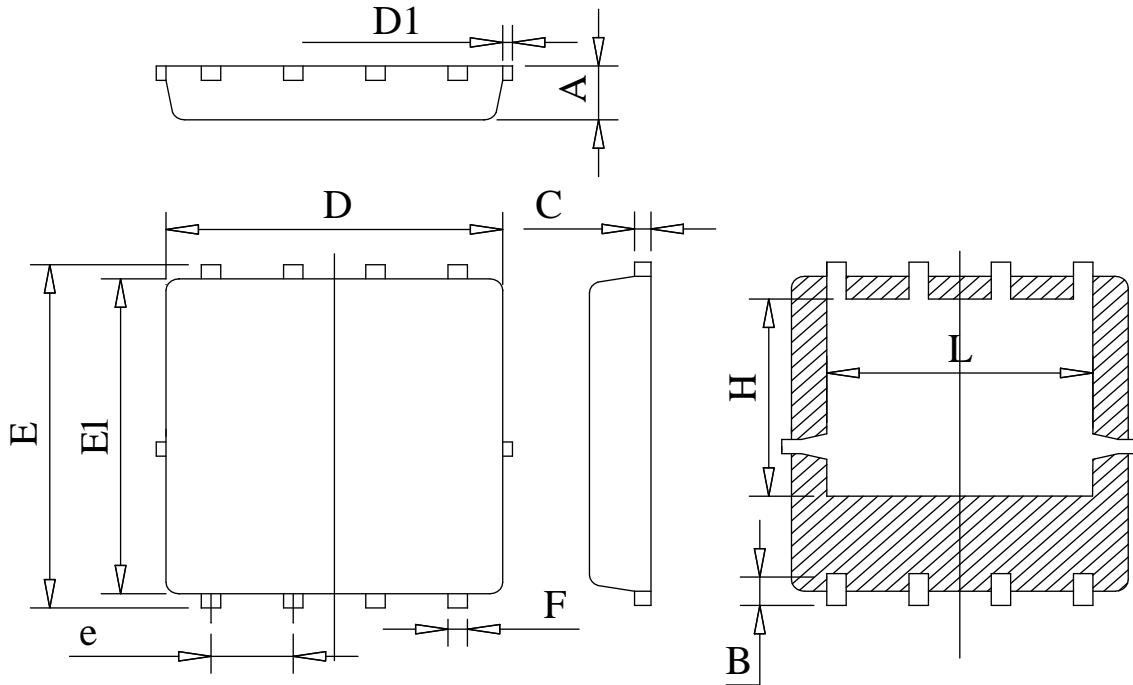


Figure 13 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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