

N-Channel 100 V (D-S) Super Junction MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (m Ω) (Typ.)	I_D (A) ^a	Q_g (Typ.)
100	6.6 at $V_{GS} = 10$ V	100	24 nC
	8.8 at $V_{GS} = 4.5$ V		

FEATURES

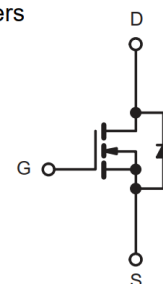
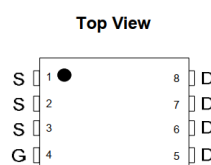
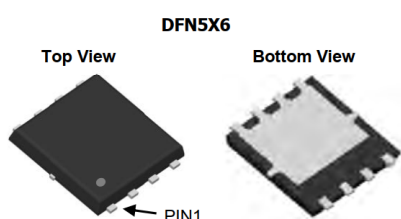
- DT-SJ Power MOSFET
- Very low on-resistance
- 100 % Rg and UIS Tested
- Fast switching



RoHS
COMPLIANT

APPLICATIONS

- Power Management
- Motor Drivers
- DC-DC Converters



N-Channel MOSFET

ORDERING INFORMATION

Part Number	Package Type	Form	# of Pins	MSL	Qty(pcs)
DTQ6082N10SJ	DFN5X6-8L	Reel	8	3	5000

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	$T_C = 25$ °C	I_D	A
	$T_C = 100$ °C	100	
Pulsed Drain Current ($t = 300$ μ s)	I_{DM}	380	mJ
Single Pulse Avalanche Energy	E_{AS}	200	
Maximum Power Dissipation	$T_C = 25$ °C	P_D	W
	$T_C = 100$ °C	120	
	$T_A = 25$ °C	76.8	
	$T_A = 100$ °C	2.1 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature) ^{d, e}		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, f}	R_{thJA}	40	60	°C/W
Maximum Junction-to-Case (Drain)	R_{thJC}	0.8	1.05	

Notes:

a. Based on $T_C = 25$ °C.

b. Surface mounted on 1" x 1" FR4 board.

c. $t = 10$ s.

d. The DFN5X6 is a leadless package. The end of the lead terminal is exposed copper

(not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

f. Maximum under steady state conditions is 70 °C/W.

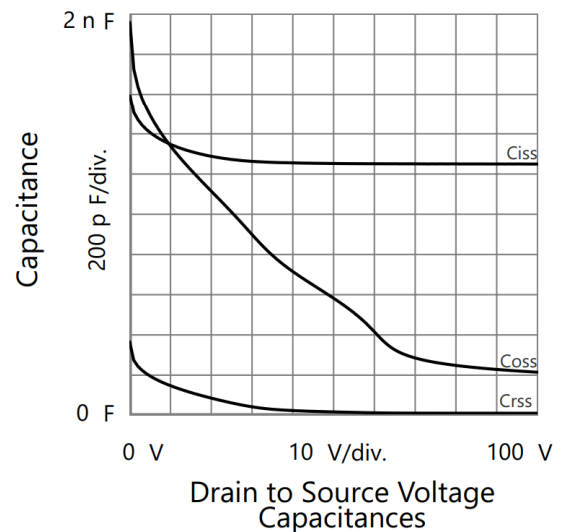
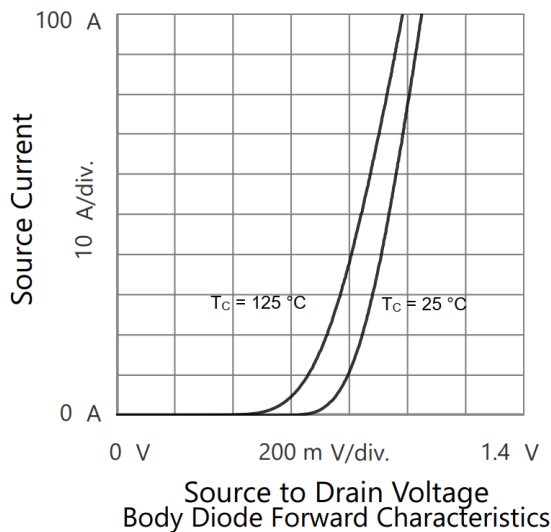
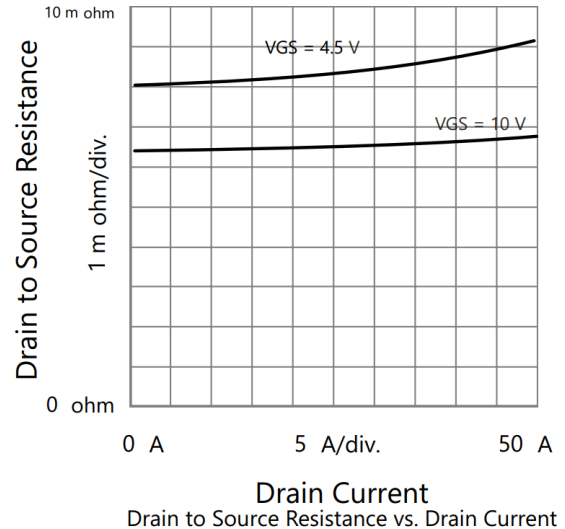
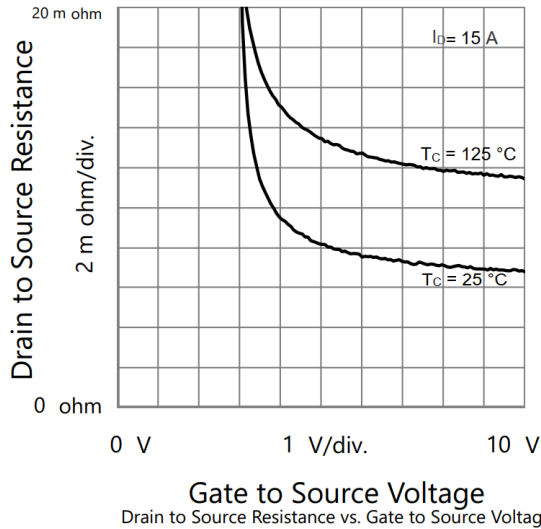
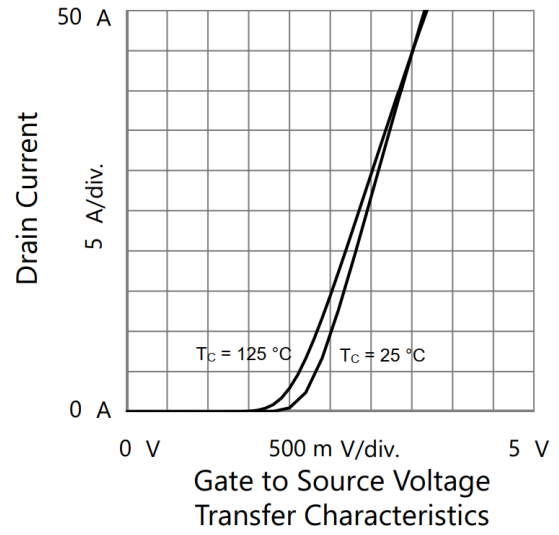
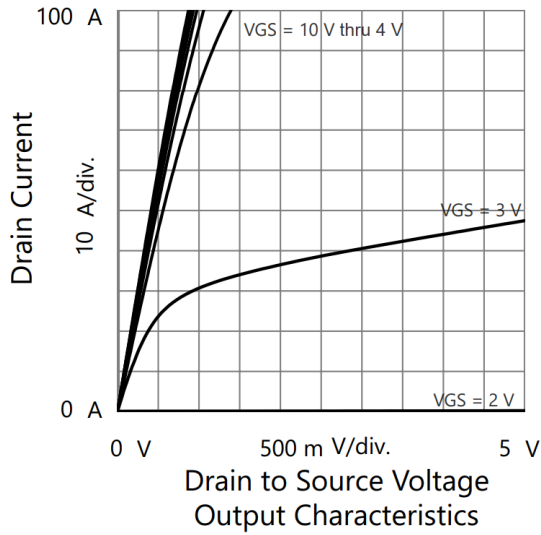
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1	-	3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 80 V, V _{GS} = 0 V, T _J =85°C	-	-	10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	100	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A	-	6.6	8.2	mΩ
		V _{GS} = 4.5 V, I _D = 15 A	-	8.8	11	
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 15 A	-	65	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 50 V, f = 1MHz	-	1250	-	pF
Output Capacitance	C _{oss}		-	582	-	
Reverse Transfer Capacitance	C _{rss}		-	11	-	
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 15 A	-	24	-	nC
Gate-Source Charge ^c	Q _{gs}		-	3.4	-	
Gate-Drain Charge ^c	Q _{gd}		-	5.1	-	
Gate Resistance	R _g	f = 1 MHz	-	2.0	-	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, R _g = 3 Ω I _D = 15 A, V _{GEN} = 10 V,	-	5	-	ns
Rise Time ^c	t _r		-	9	-	
Turn-Off Delay Time ^c	t _{d(off)}		-	20	-	
Fall Time ^c	t _f		-	15	-	
Drain-Source Body Diode Ratings and Characteristics ^b (T _C = 25 °C)						
Continuous Source Current	I _S	T _C = 25 °C	-	-	100	A
Pulsed Source Current	I _{SM}		-	-	380	A
Forward Voltage ^a	V _{SD}	I _F = 1 A, V _{GS} = 0 V	-	-	1	V
Reverse Recovery Time	t _{rr}	I _F = 15 A, di/dt = 100 A/μs	-	42	-	ns
Reverse Recovery Charge	Q _{rr}		-	50	-	nC

Notes

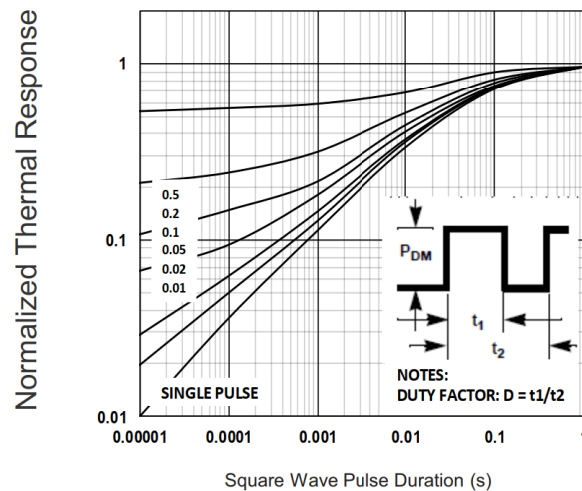
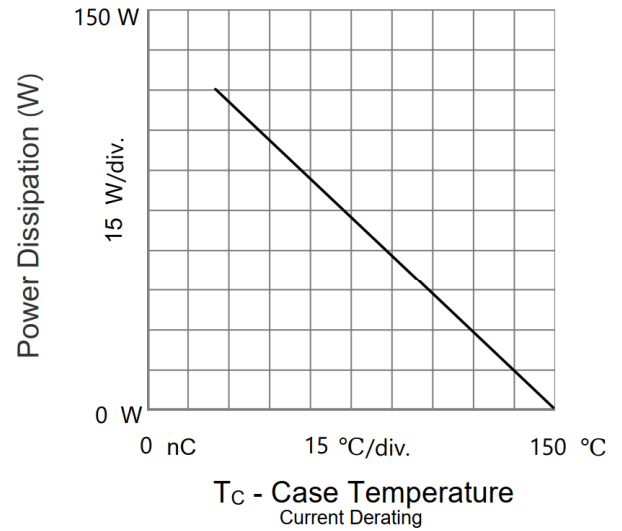
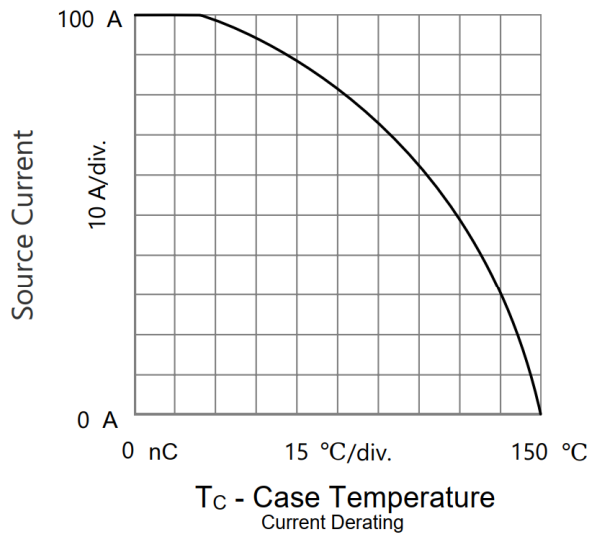
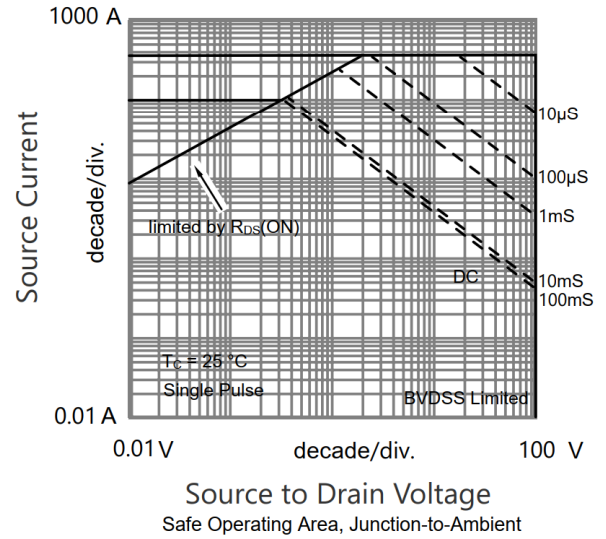
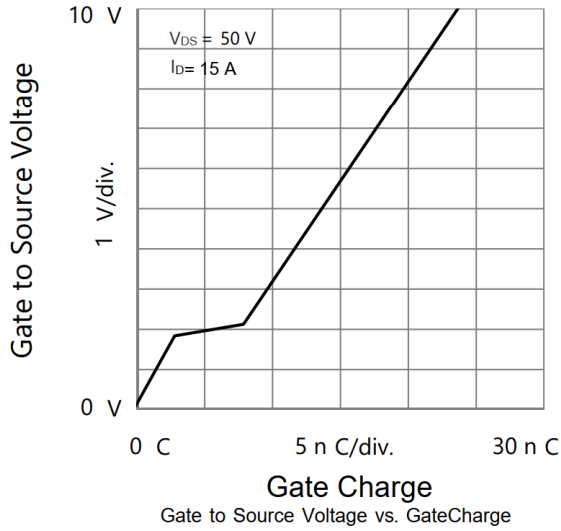
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

Mark:



LOGO = Din_Tek Logo

DTQ&6082N10&SJ = Part Number Code

Y = Year Code

WW = Week Code

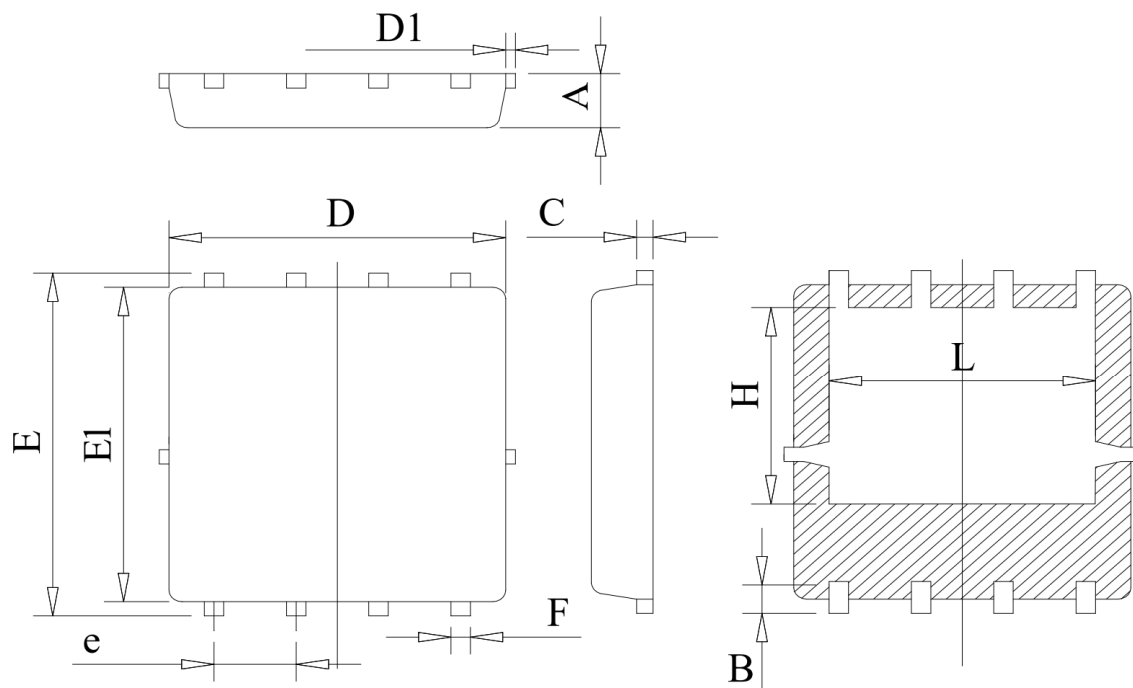
TCD = Tracking Code

NOTES:

1. Year Code : M = 2022, N = 2023, P = 2024, ...
The easily confused letter "O" does not participate in sorting
2. Week Code : Week1 = 01, Week2 = 02, Week3 = 03, ...

The current marking strategy is reflected. Contact your local sales representative for historical marking strategies for these packages.

DFN5*6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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