

## N-Channel 60 V (D-S) Super Junction MOSFET

### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ (m $\Omega$ )(Typ.)	$I_D$ (A) <sup>a</sup>	$Q_g$ (Typ.)
60	0.8at $V_{GS} = 10$ V	305	127nC

### FEATURES

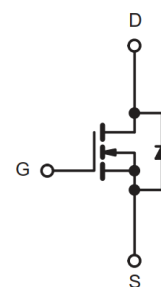
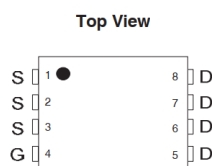
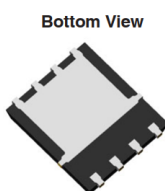
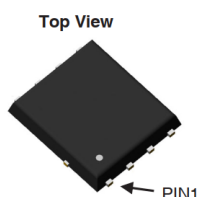
- DT-SJ Power MOSFET
- 100 % Rg and UIS Tested
- Low On-Resistance

### APPLICATIONS

- High-Efficiency DC-DC Converters
- Switching Voltage Regulators
- Motor Drivers


**RoHS**  
 COMPLIANT

### DFN5X6-8L Pin Configuration



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$T_C = 25$ °C	305	A
	$T_C = 100$ °C	200	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	900	A
Single Pulse Avalanche Energy	$E_{AS}$	545	mJ
Maximum Power Dissipation <sup>c</sup>	$T_C = 25$ °C	265	W
	$T_C = 100$ °C	106	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to +150	°C
Soldering Recommendations (Peak Temperature)		260	°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>d</sup>	$R_{thJA}$	-	50	°C/W
Maximum Junction-to-Case (Drain)	$R_{thJC}$	-	0.47	

### Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
- The value of  $R_{thJA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_a = 25$  °C.

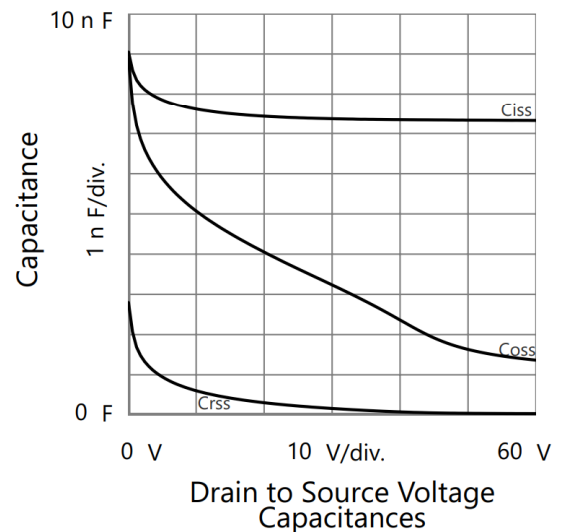
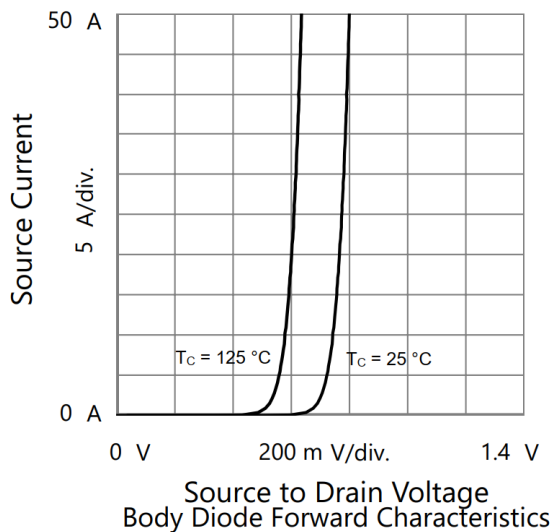
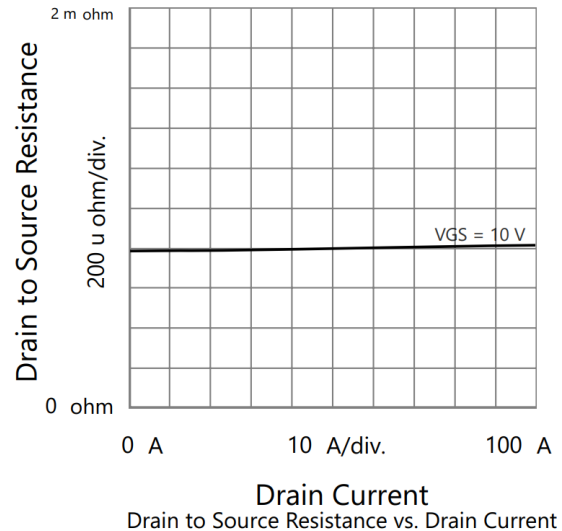
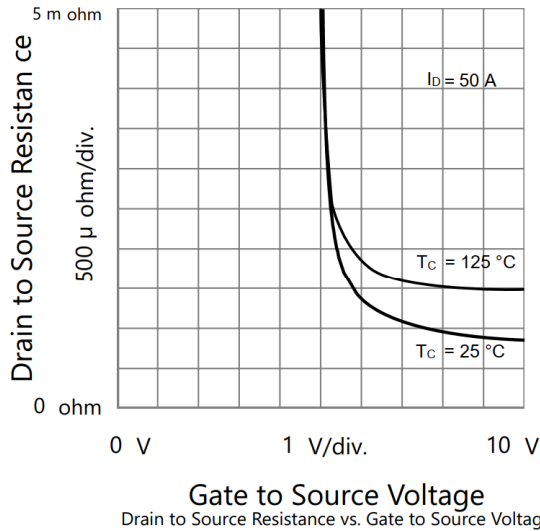
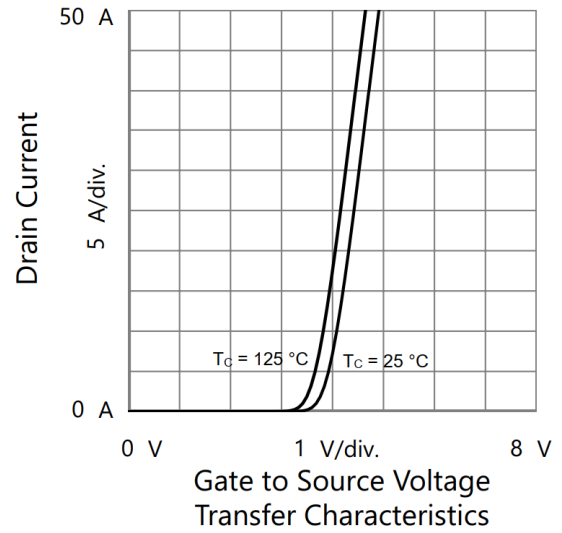
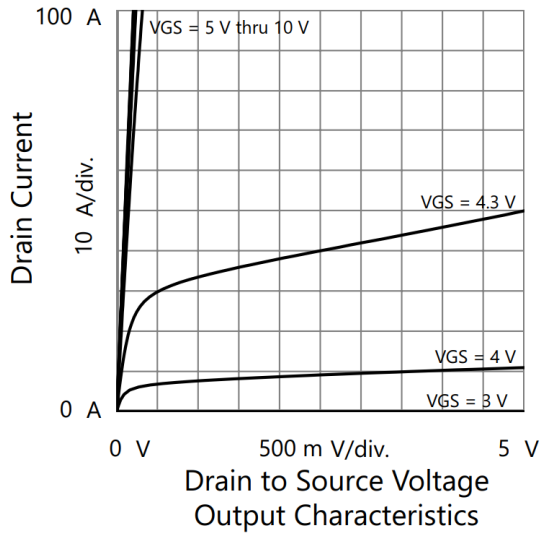
SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2	-	4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	-	-	1	μA
		V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125°C	-	-	100	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 10 V, V <sub>GS</sub> = 10 V	305	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	-	0.8	0.96	mΩ
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 50 A	-	63	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 30 V, f = 1MHz	-	7370	-	pF
Output Capacitance	C <sub>oss</sub>		-	3230	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	160	-	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	-	127	-	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>		-	22.6	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	28	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	2.8	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, R <sub>g</sub> = 3 Ω I <sub>D</sub> = 50 A, V <sub>GEN</sub> = 10 V,	-	23	-	ns
Rise Time <sup>c</sup>	t <sub>r</sub>		-	18	-	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>		-	75	-	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	24	-	
Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> (T <sub>C</sub> = 25 °C)						
Continuous Source Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	305	A
Pulsed Source Current	I <sub>SM</sub>		-	-	300	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0 V	-	-	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 50 A, di/dt = 100 A/μs	-	45	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	180	-	nC

**Notes**

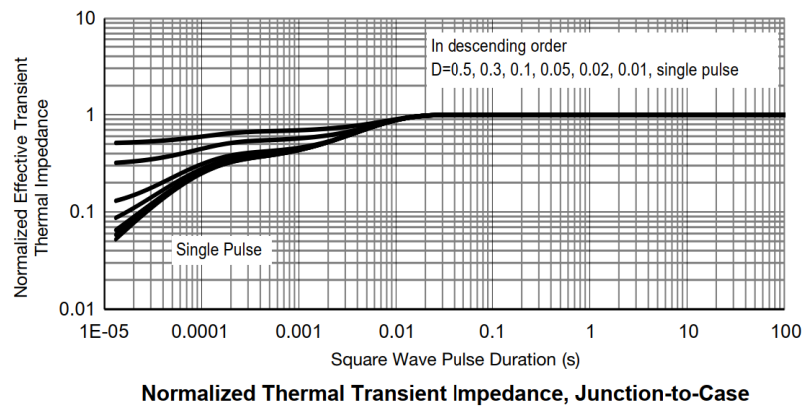
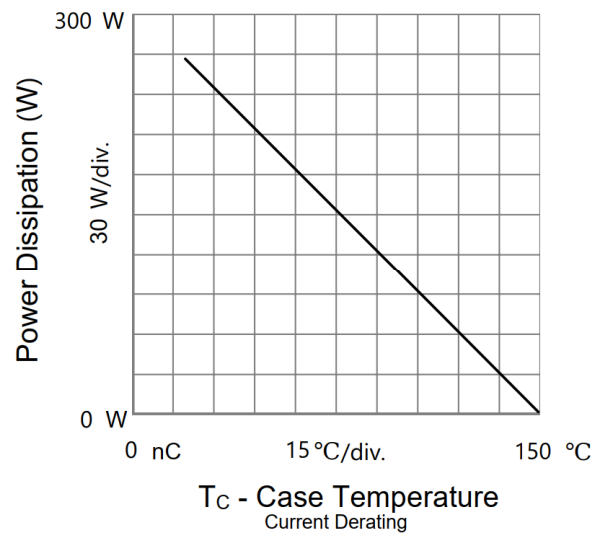
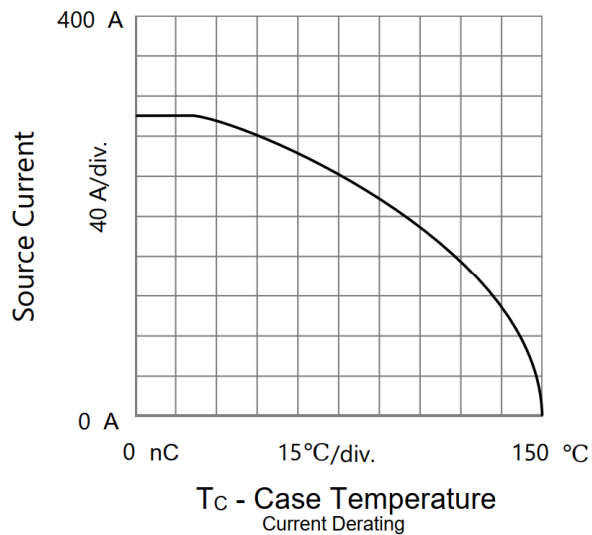
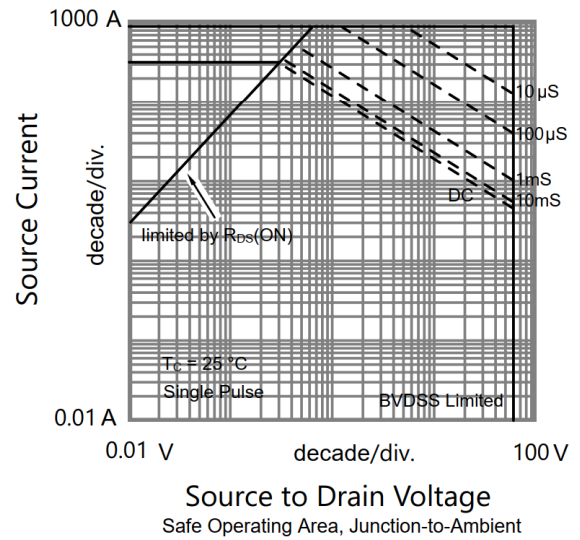
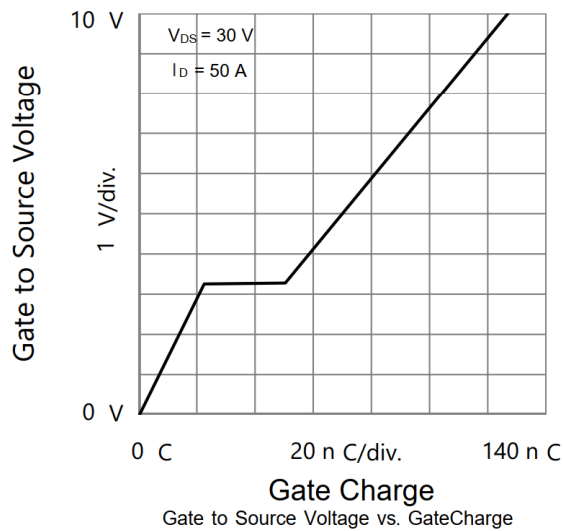
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

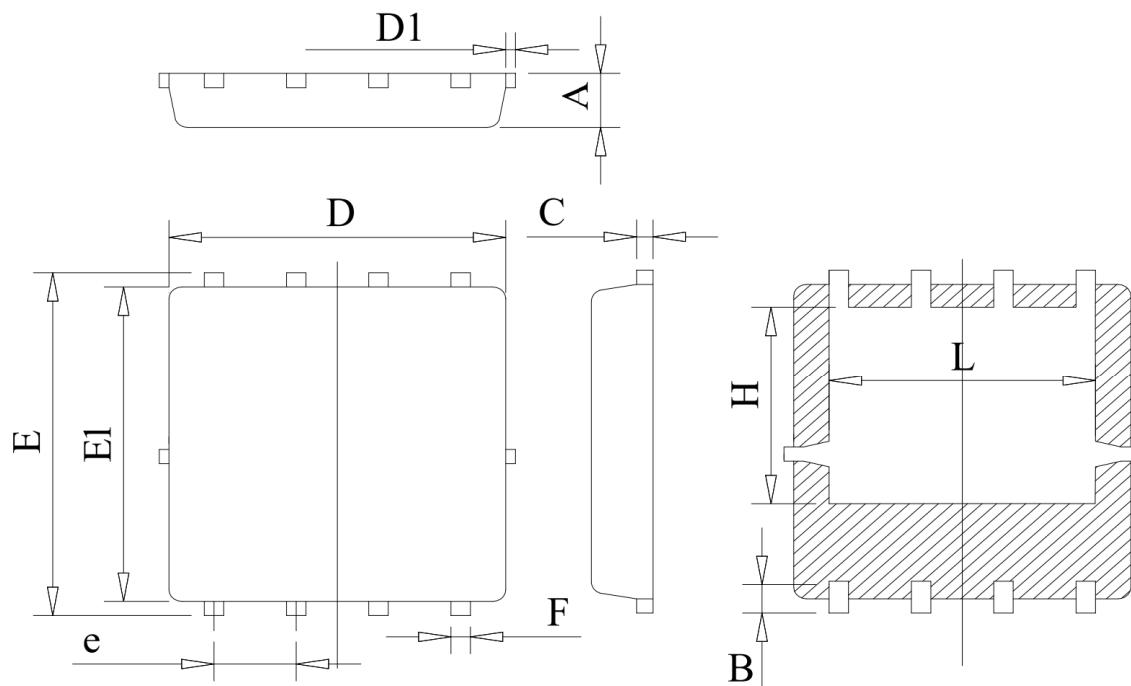
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



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## DFN5\*6-8L PACKAGE OUTLINE



### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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