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P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)	
- 60	7 at V _{GS} = - 10 V	05	76 nC	
	8.5 at V _{GS} = - 4.5 V	- 85	70110	

FEATURES

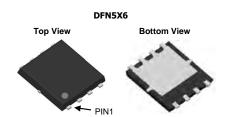
- DT-Trench Power MOSFET
- \bullet 100 % R $_{\rm g}$ and UIS Tested

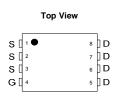
APPLICATIONS

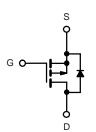
- PWM Applications
- Load Switch
- Power Management



RoHS COMPLIANT







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unles	ss otherwise noted)			
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	- 60	V	
Gate-Source Voltage		V_{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C	1	- 85		
Continuous Drain Current	T _C = 100 °C	l _D	- 54		
Continuous Source Current (Diode Conduction)		I _S	- 85	Α	
Pulsed Drain Current ^a		I _{DM}	- 340		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 80		
Single Pulse Avalanche Energy	L = 0.1 IIIII	E _{AS}	230	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	D	120	W	
iviaximum Fower Dissipation	T _C = 100 °C	P_D	48	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 150	°C	

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Case (Drain) ^{C,d}	RthJC	1.04	°C/W

Notes:

- a. Based on T_C= 25°C.
- b. Pulse test; pulse width 300 $\mu s,\,duty\,\,cycle\,$ 2 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 60			V	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zone Cote Valta de Brain Comment	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current		V _{DS} = -60 V, V _{GS} = 0 V, T _J = 125 °C			- 10	μΑ	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		7	8	mΩ	
		V _{GS} = - 4.5 V, I _D = - 20 A		8.5	10		
Forward Transconductance ^a	9 _{fs}	V _{DS} = -10 V,I _D = - 20 A		60		S	
Dynamic ^b	<u>'</u>				'		
Input Capacitance	C _{iss}			12300		pF	
Output Capacitance	C _{oss}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		485			
Reverse Transfer Capacitance	C _{rss}			604			
Total Gate Charge	Q_g			76		nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -30 \text{ V}, I_{D} = -20 \text{ A}$		10			
Gate-Drain Charge	Q_{gd}			8			
Gate Resistance	R_{g}	f = 1 MHz		1		Ω	
Turn-On Delay Time	t _{d(on)}			58			
Rise Time	t _r	V_{DS} = -30 V, R_L = 4 Ω		23		ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -20 \text{A}, V_{GS} = -10 \text{V}, R_g = 6 \Omega$		105			
Fall Time	t _f	1		21			
Drain-Source Body Diode Characteristics	·						
Continuous Source-Drain Diode Current	I _S	T _A = 25 °C			- 85	Δ	
Pulse Diode Forward Current ^a	I _{SM}				- 340	0 A	
Body Diode Voltage	V_{SD}	I _S = - 1 A			- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	1 00 A di/dt 400 A/v. T 05 00		65		ns	
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = -20 \text{ A, di/dt} = 100 \text{ A/µs, } T_J = 25 \text{ °C}$		74		nC	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

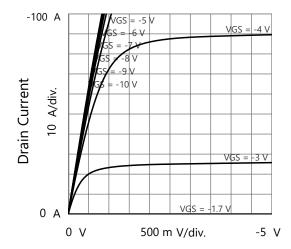
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

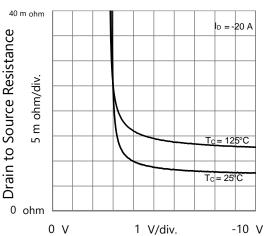


TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

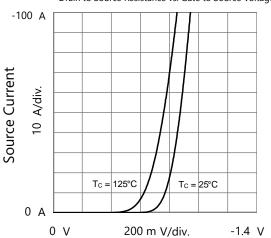
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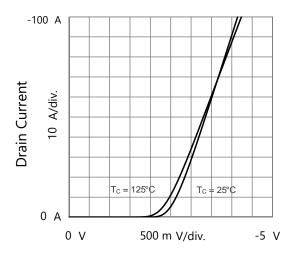
Drain to Source Voltage Output Characteristics



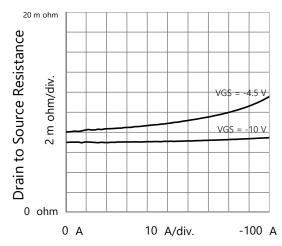
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



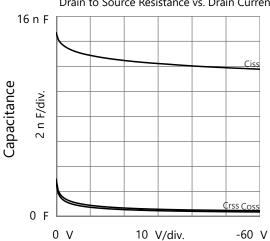
Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



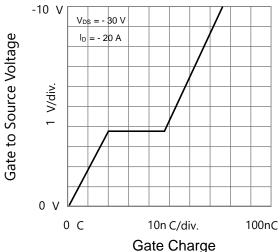
Drain Current
Drain to Source Resistance vs. Drain Current



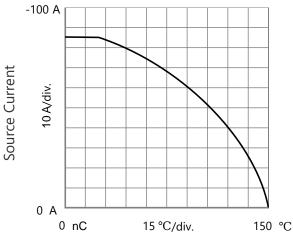
Drain to Source Voltage Capacitances



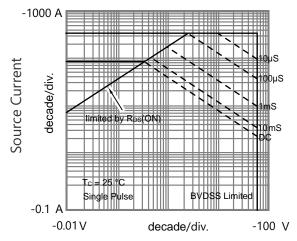
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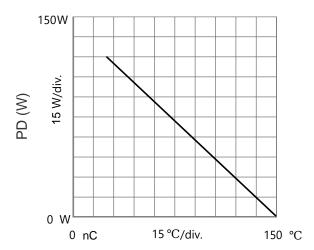
Gate to Source Voltage vs. GateCharge



 $T_{C} \textbf{ - Case Temperature}_{\text{Current Derating}}$

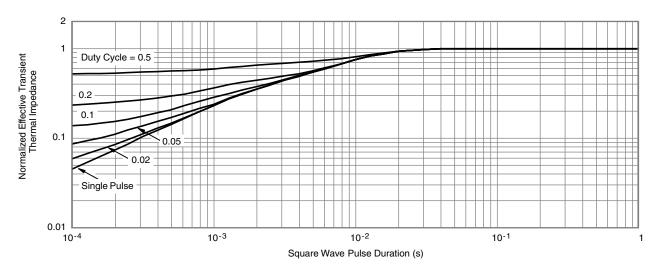


Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature

Power Derating



Normalized Thermal Transient Impedance, Junction-to-Case





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