

Dual N-Channel 100 V (D-S) Super Junction MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
100	9.6 at V _{GS} = 10 V	45	17.6nC
	11.9 at V _{GS} = 4.5 V		

FEATURES

- DT-SJ Power MOSFET
- 100 % R_g and UIS Tested
- 100% Avalanche Test
- High Speed Power Switching

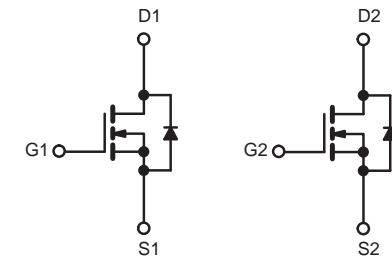
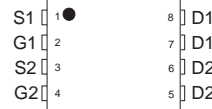
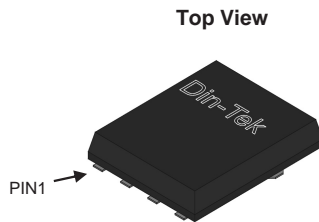


RoHS
COMPLIANT

APPLICATIONS

- DC/DC in Telecoms and Industrial
- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit

DFN5X6-8L Pin Configuration



N1-Channel MOSFET

N2-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C) ^a	I _D	T _C = 25 °C	45
		T _C = 100 °C	29
Pulsed Drain Current ^b	I _{DM}	176	A
Single Avalanche Energy	E _{AS}	130	mJ
Maximum Power Dissipation ^c	P _D	T _C = 25 °C	43
		T _C = 100 °C	17
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to + 150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	50	°C/W
Junction-to-Case (Drain)	R _{thJC}	2.9	

Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P_D is based on max. junction temperature, using junction-case thermal resistance.
- The value of R_{thJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.

SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.2	-	2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 125 °C	-	-	100	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	45	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A	-	9.6	11.6	mΩ
		V _{GS} = 4.5 V, I _D = 15 A	-	11.9	14.9	
Forward Transconductance ^a	g _{fs}	V _{DS} = 5 V, I _D = 20 A	-	31	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz	-	1072	-	pF
Output Capacitance	C _{oss}		-	491	-	
Reverse Transfer Capacitance	C _{rss}		-	12	-	
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 20 A	-	17.6	-	nC
Gate-Source Charge ^c	Q _{gs}		-	3.5	-	
Gate-Drain Charge ^c	Q _{gd}		-	3.6	-	
Gate Resistance	R _g	f = 1 MHz	-	1.2	-	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, I _D = 20 A, R _g = 3 Ω, V _{GS} = 10 V	-	4.1	-	ns
Rise Time ^c	t _r		-	3.6	-	
Turn-Off Delay Time ^c	t _{d(off)}		-	16	-	
Fall Time ^c	t _f		-	9.1	-	
Drain-Source Body Diode Ratings and Characteristics ^b (T_C = 25 °C)						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	45	A
Pulsed Current	I _{SM}		-	-	176	A
Forward Voltage ^a	V _{SD}	I _F = 2 A, V _{GS} = 0 V	-	0.7	1.2	V
Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs	-	43	-	ns
Reverse Recovery Charge	Q _{rr}		-	59	-	nC

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

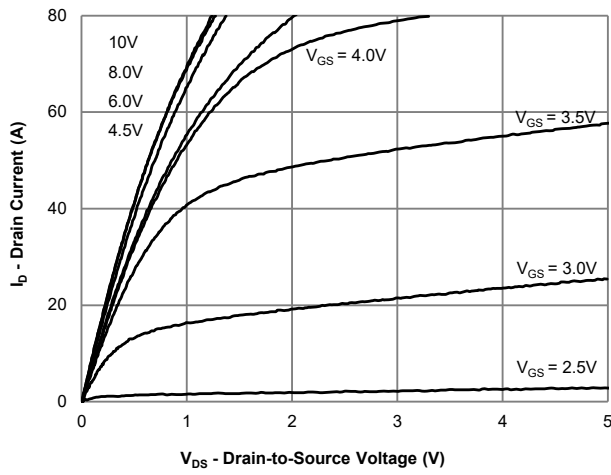


Figure 1: Output Characteristics

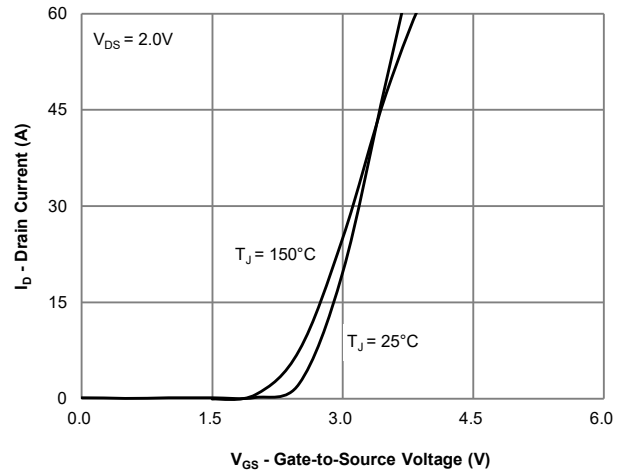


Figure 2: Transfer Characteristics

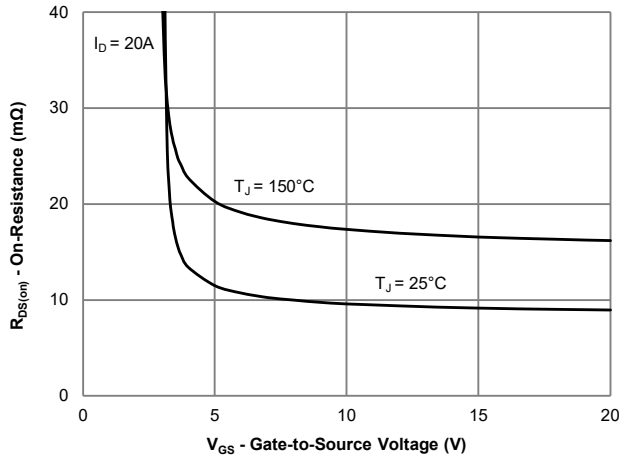


Figure 3: On-Resistance vs. Gate-Source Voltage

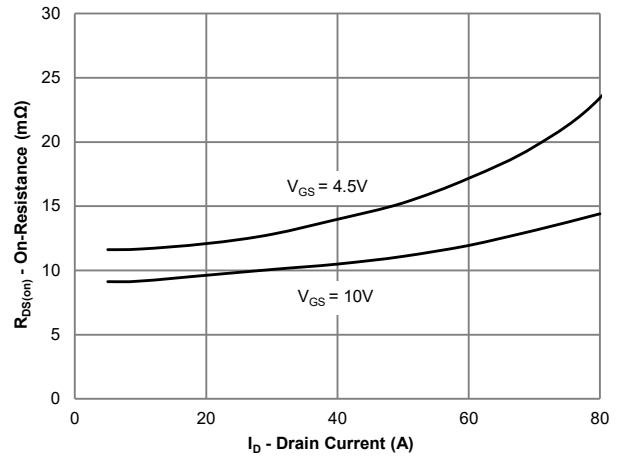


Figure 4: On-Resistance vs. Gate-Source Voltage

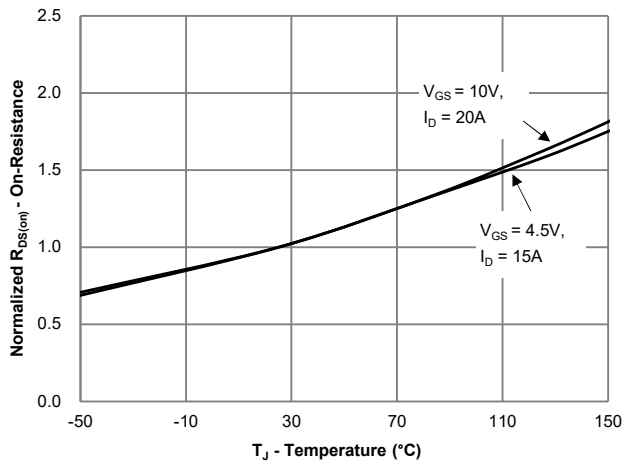


Figure 5: On-Resistance vs. Junction Temperature

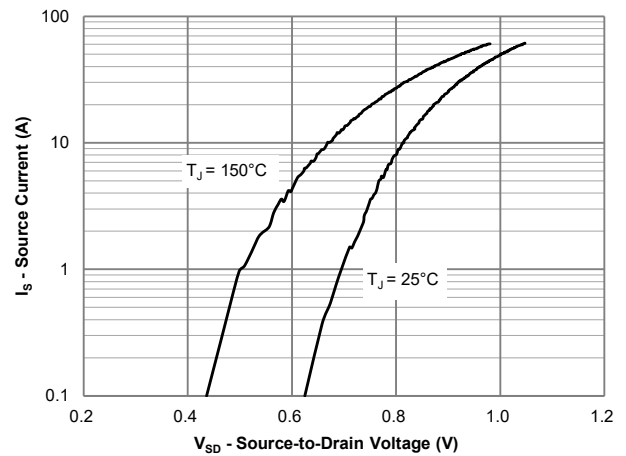


Figure 6: Source-Drain Diode Forward Voltage

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

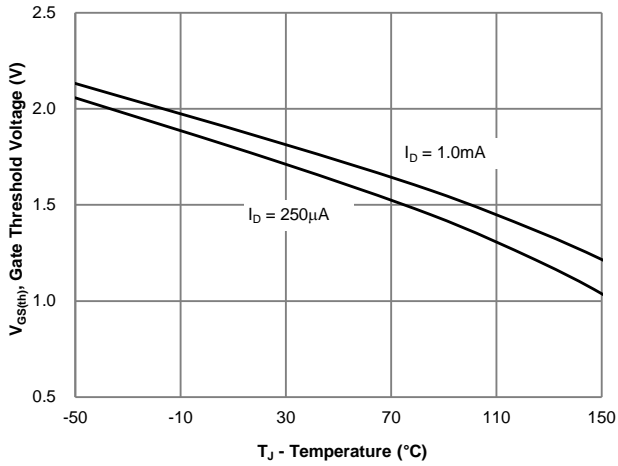


Figure 7: Gate Threshold Variation vs. Junction Temperature

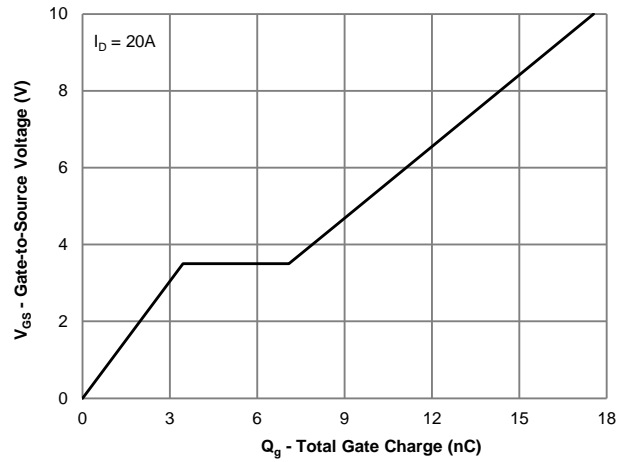


Figure 8: Gate Charge Characteristics

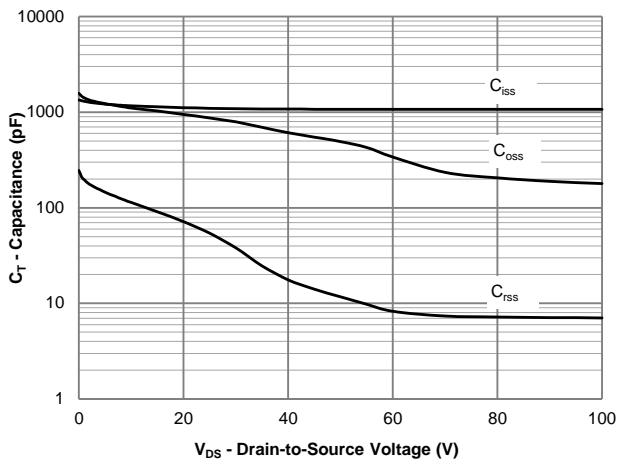


Figure 9: Capacitance Characteristics

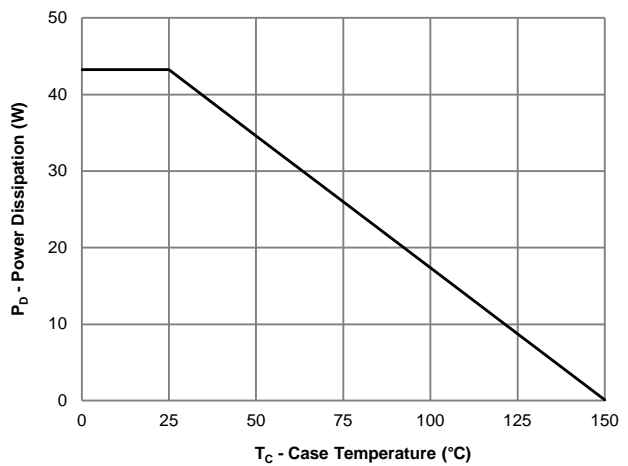


Figure 10: Power Derating

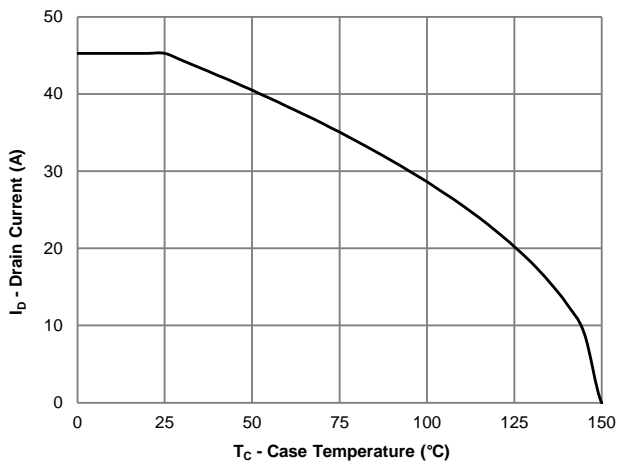


Figure 11: Current Derating

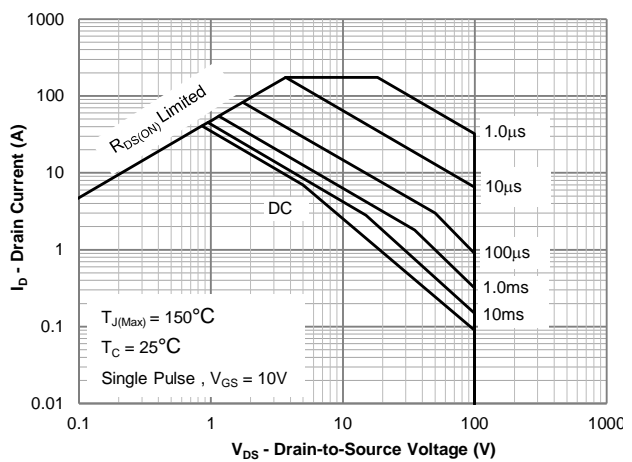


Figure 12: Safe Operating Area

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

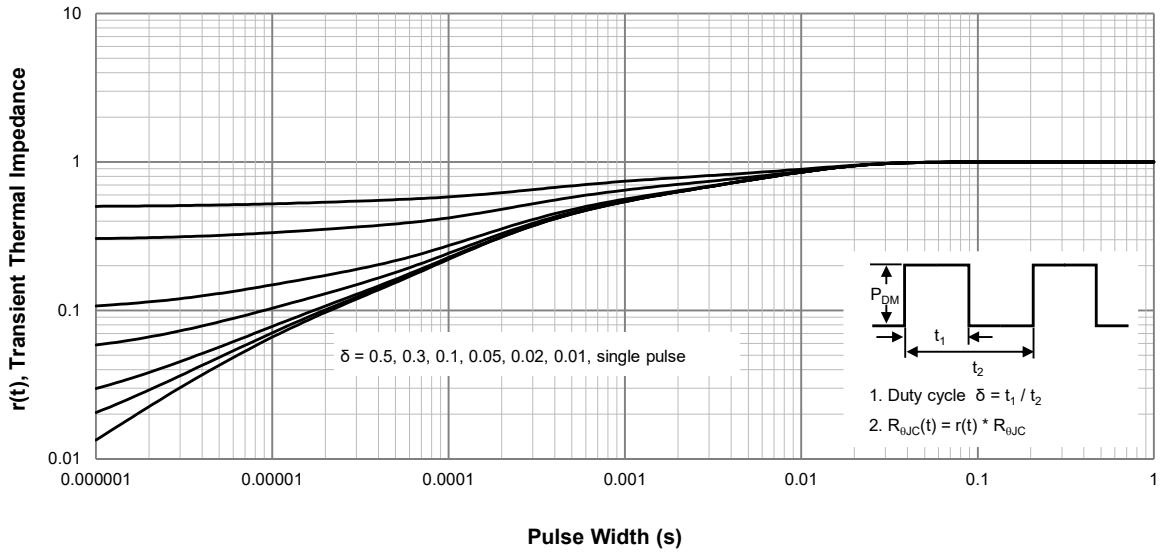
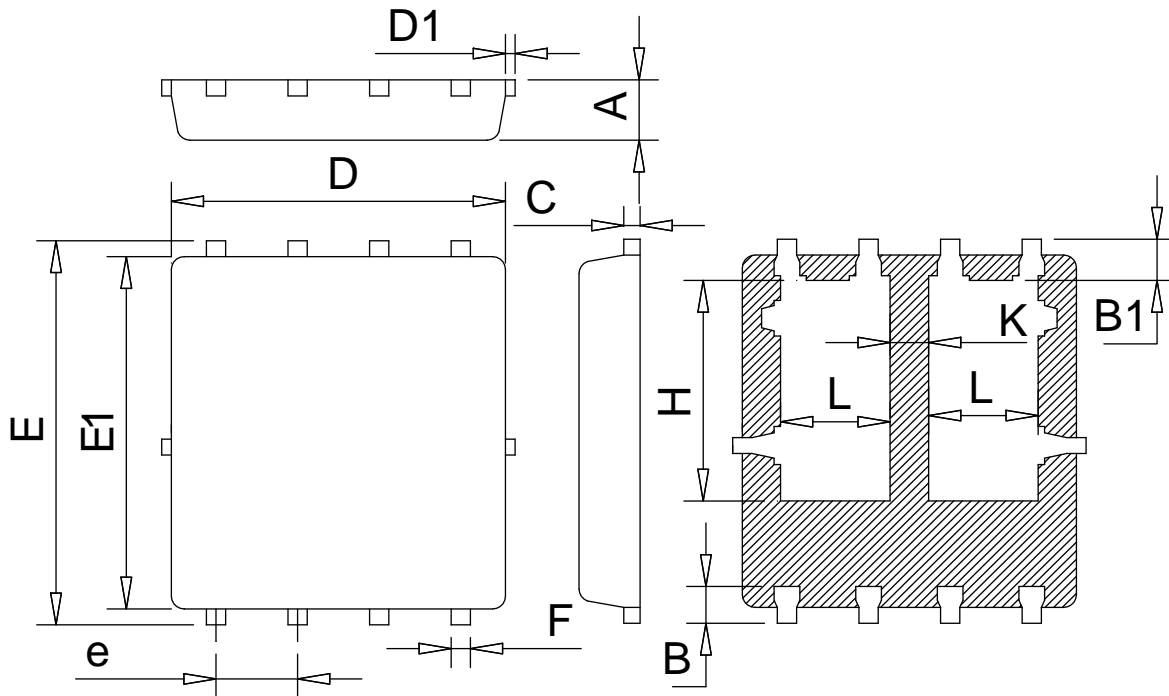


Figure 13: Normalized Maximum Transient Thermal Impedance

DFN5X6-8L-D PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Typ	Max
A	0.85	0.95	1.05
B	0.46	0.58	0.73
B1	0.52	0.65	0.78
C	0.18	0.254	0.32
D	4.70	5.20	5.50
D1	-	-	0.18
E	5.75	6.05	6.35
E1	5.35	5.65	5.85
e	1.15	1.27	1.50
F	0.15	0.30	0.50
H	3.15	3.47	3.80
L	1.35	1.70	2.10
K	0.35	0.60	1.00

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