

# P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
- 100	0.039at V <sub>GS</sub> = - 10 V	- 25	40 50		
- 100	0.046 at V <sub>GS</sub> = - 4.5 V	- 21	40 nC		

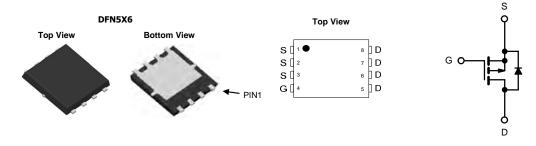
### **FEATURES**

- · DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested



### **APPLICATIONS**

- Notebook
  - Load Switch



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_A = 25 ^{\circ}\text{C}$ , unle	ss otherwise not	ted		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 100	V		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	$T_C = 25 ^{\circ}\text{C}$ $T_C = 70 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$	I <sub>D</sub>	- 25 <sup>a</sup> - 18 <sup>a</sup> - 5 <sup>b, c</sup>		
T <sub>A</sub> = 70 °C Pulsed Drain Current		I <sub>DM</sub>	- 3.3 <sup>b, c</sup> - 75	Α	
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$	I <sub>S</sub>	- 25 <sup>a</sup> - 6 <sup>b, c</sup>		
Single Pulse Avalanche Current  L = 0.1 m		I <sub>AS</sub>	- 30		
Single Pulse Avalanche Energy	L = 0.1 11111	E <sub>AS</sub>	56	mJ	
Maximum Power Dissipation	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	P <sub>D</sub>	61 23 2.6 <sup>b, c</sup> 1.2 <sup>b, c</sup>	W	
Operating Junction and Storage Temperature Ran	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature)		260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 10 s	R <sub>thJA</sub>	16	22	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.5	2.5	C/ V V	

#### Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. The DFN5x6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 54 °C/W.



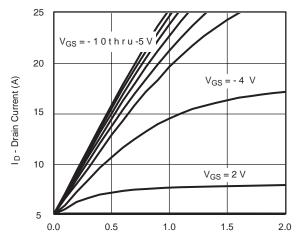
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	<u> </u>					I	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 100			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250A		- 31		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		6.5			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
7 0 1 1/1 5 1 0 1		V <sub>DS</sub> = - 80 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 75			Α	
	5	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 15 A		0.039	0.045	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 10 A		0.046	0.053		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 15 A		52		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			6450		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 80 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1075			
Reverse Transfer Capacitance	C <sub>rss</sub>			415			
	0	V <sub>DS</sub> = -80 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -15 A		40			
Total Gate Charge	$Q_g$			32		1	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = -80 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10 A		9		nC	
Gate-Drain Charge	$Q_{gd}$			12			
Gate Resistance	$R_g$	f = 1 MHz		1.1		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15			
Rise Time	t <sub>r</sub>	$V_{DS} = -80 \text{ V}, R_L = 15 \Omega$		12			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GS} = -10 \text{ V}, R_g = 1 \Omega$		27			
Fall Time	t <sub>f</sub>			10			
Turn-On Delay Time	t <sub>d(on)</sub>			19		ns	
Rise Time	t <sub>r</sub>	$V_{DS} = -80 \text{ V}, R_L = 15 \Omega$		15		-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GS} = -4.5 \text{ V}, R_g = 1 \Omega$		35			
Fall Time	t <sub>f</sub>			10			
<b>Drain-Source Body Diode Characteristic</b>	s	_		•		l	
Continuous Source-Drain Diode Current	I <sub>S</sub>	$T_C = 25  ^{\circ}C$			25	۸	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				75	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = - 10 A		- 0.7	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			30		ns	
Body Diode Reverse Recovery Charge Q <sub>rr</sub>		L = 10 A dl/dt = 100 A/vo T = 25 °C		35		nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		16			
Reverse Recovery Rise Time	t <sub>b</sub>	7		14		ns	

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$  b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

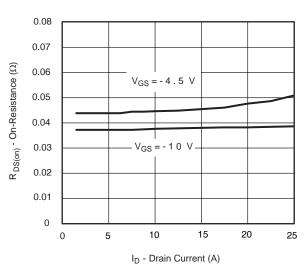


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

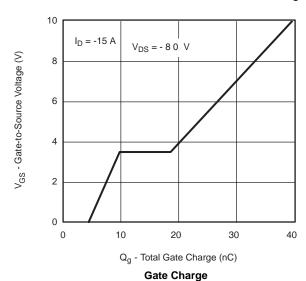


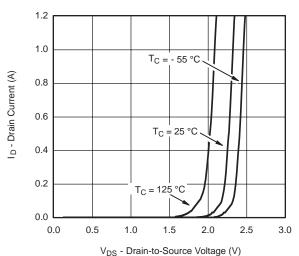
V<sub>DS</sub> - Drain-to-Source Voltage (V)

#### **Output Characteristics**

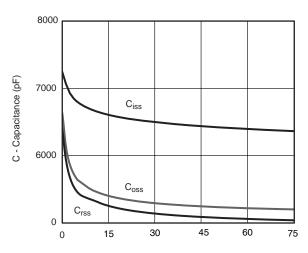


#### On-Resistance vs. Drain Current and Gate Voltage



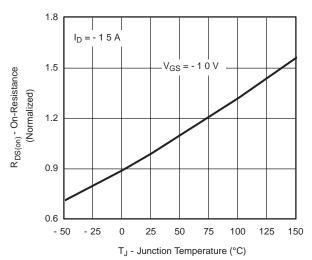


#### Transfer Characteristics



V<sub>DS</sub> - Drain-to-Source Voltage (V)

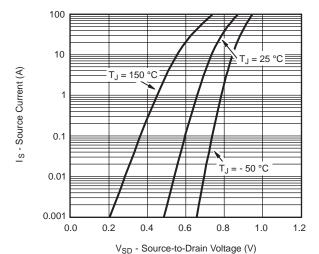
#### Capacitance



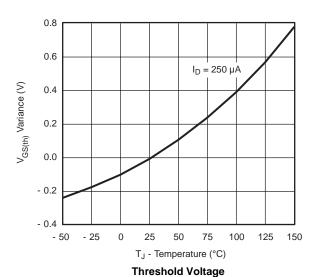
On-Resistance vs. Junction Temperature



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



**Source-Drain Diode Forward Voltage** 

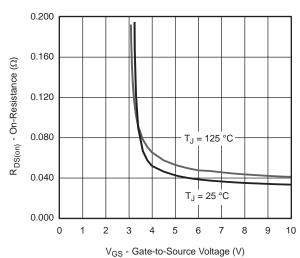


To Limited by R<sub>DS(on)</sub>\*

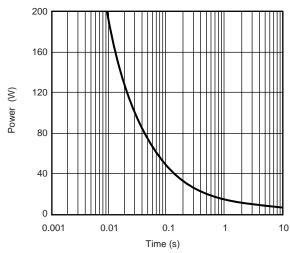
100 µs

100

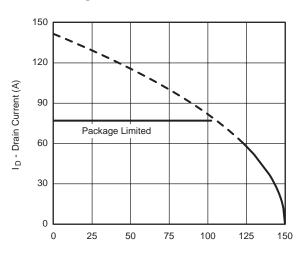
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified Safe Operating Area, Junction-to-Ambient



On-Resistance vs. Gate-to-Source Voltage



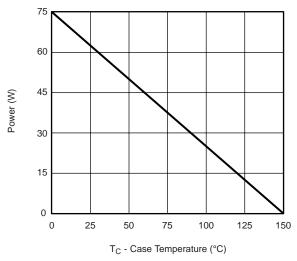
Single Pulse Power, Junction-to-Ambient

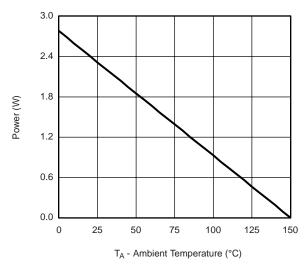


**Current Derating\*** 



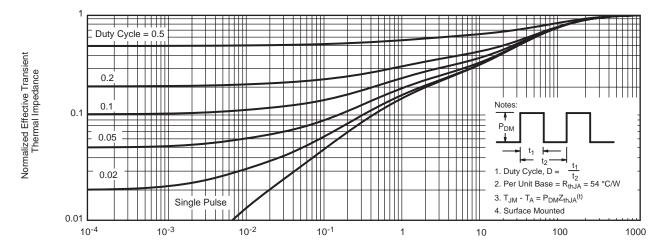
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Power, Junction-to-Case

Power, Junction-to-Ambient

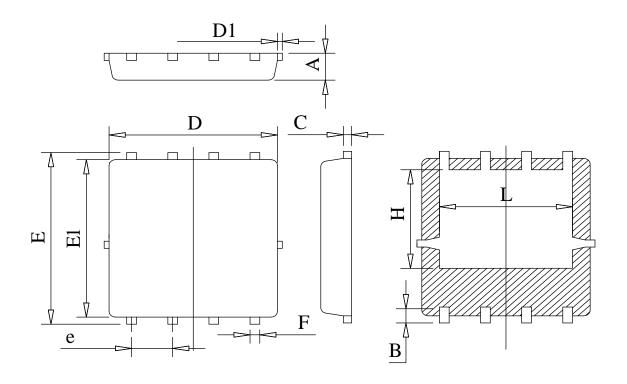


Normalized Thermal Transient Impedance, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



## **DFN5X6-8L PACKAGE OUTLINE**



# **COMMON DIMENSIONS** (UNITS OF MEASURE=MILLIMETER)

Unit: mm

Symbol	Min	Тур	Max
A	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25





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