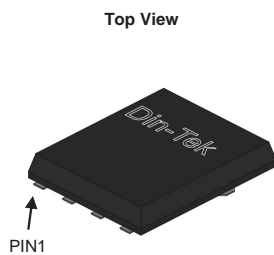


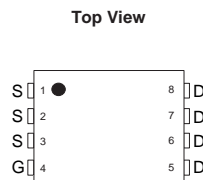
## N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
$V_{DS}$ (V)	$R_{DS(on)}$ (m $\Omega$ ) (Typ.)	$I_D$ (A) <sup>a</sup>	$Q_g$ (Typ.)
100	12 at $V_{GS} = 4.5$ V	48	22.6 nC

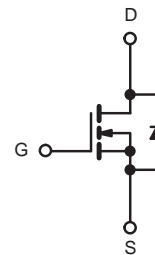
### DFN5X6-8L Pin Configuration



Top View



Top View



N-Channel MOSFET

### FEATURES

- DT-Trench Power MOSFET
- 100 % Rg and UIS tested
- Low on-resistance  $R_{DS(on)}$

### APPLICATIONS

- Power switching application
- Ideal for high-frequency switching and synchronous rectification



**RoHS**  
COMPLIANT

ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$	$T_C = 25$ °C	48
		$T_C = 100$ °C	36
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	180	A
Single Avalanche Energy	$E_{AS}$	178	mJ
Maximum Power Dissipation <sup>c</sup>	$P_D$	$T_C = 25$ °C	85
		$T_C = 100$ °C	34
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) <sup>d</sup>	$R_{thJA}$	50	°C/W
Junction-to-Case (Drain)	$R_{thJC}$	1.47	

#### Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
- The value of  $R_{thJA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_a = 25$  °C.

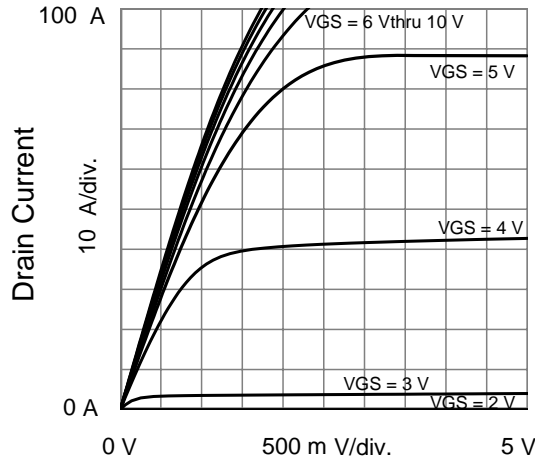
SPECIFICATIONS ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	100			V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 10\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			50	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	48			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$		12	18	m $\Omega$
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 5\text{ V}, I_D = 20\text{ A}$		45		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 50\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1340		pF
Output Capacitance	$C_{oss}$			328		
Reverse Transfer Capacitance	$C_{rss}$			11		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 20\text{ A}$		22.6		nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			3.1		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			4.5		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		1.6		$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 15\text{ V}, I_D = 15\text{ A}, R_g = 3\text{ }\Omega$ $V_{GS} = 10\text{ V}$		13		ns
Rise Time <sup>c</sup>	$t_r$			11.5		
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			22		
Fall Time <sup>c</sup>	$t_f$			8		
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			48	A
Pulse Diode Forward Current	$I_{SM}$				180	
Body Diode Voltage <sup>a</sup>	$V_{SD}$	$I_S = 1\text{ A}$			1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 20\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		28		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$				100	

**Notes:**

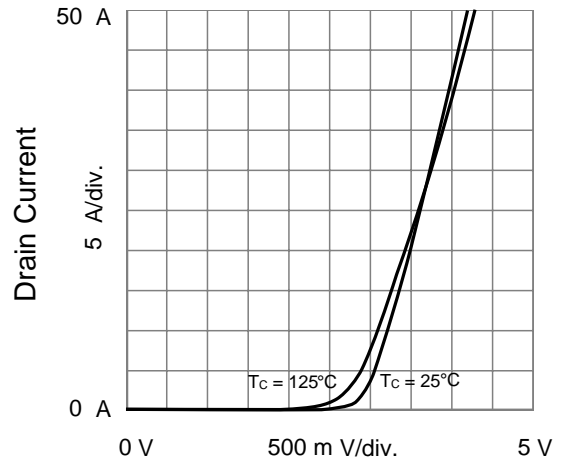
- Pulse test; pulse width 300  $\mu\text{s}$ , duty cycle 2 %.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

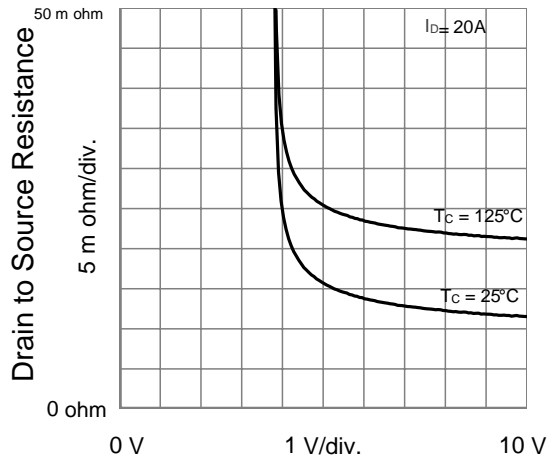
**TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**



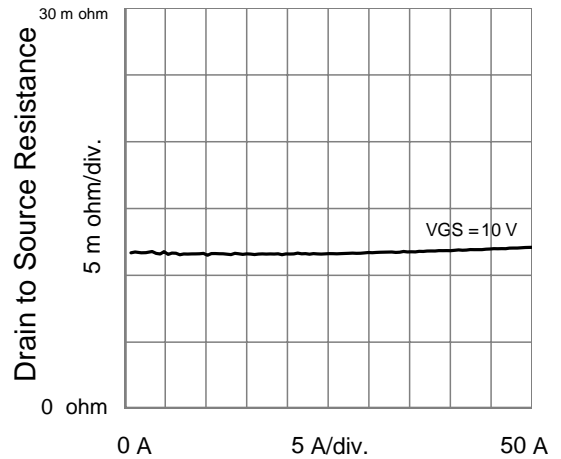
**Drain to Source Voltage Output Characteristics**



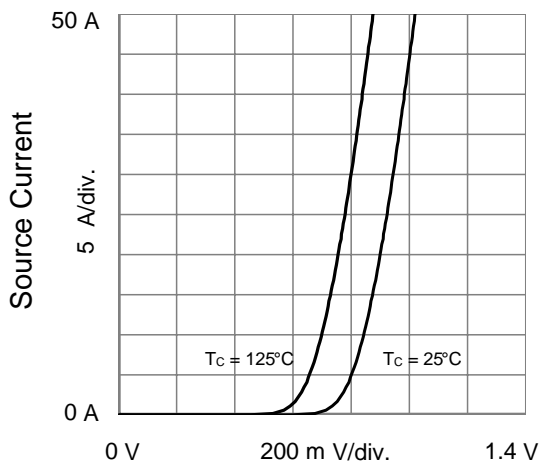
**Gate to Source Voltage Transfer Characteristics**



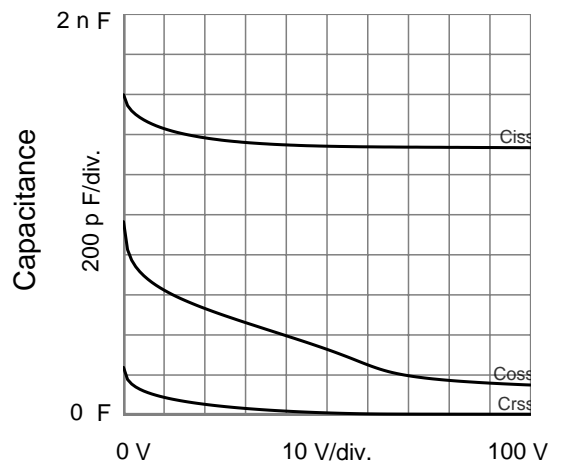
**Gate to Source Voltage Drain to Source Resistance vs. Gate to Source Voltage**



**Drain Current**

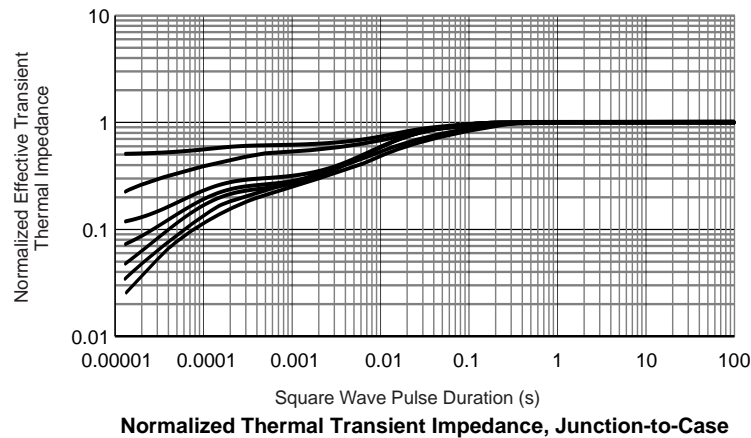
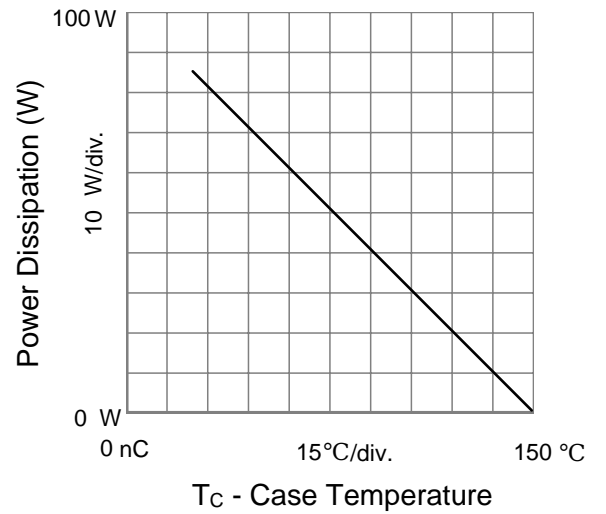
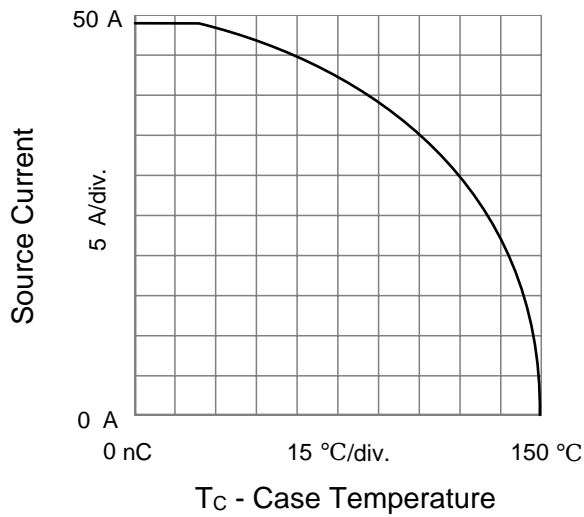
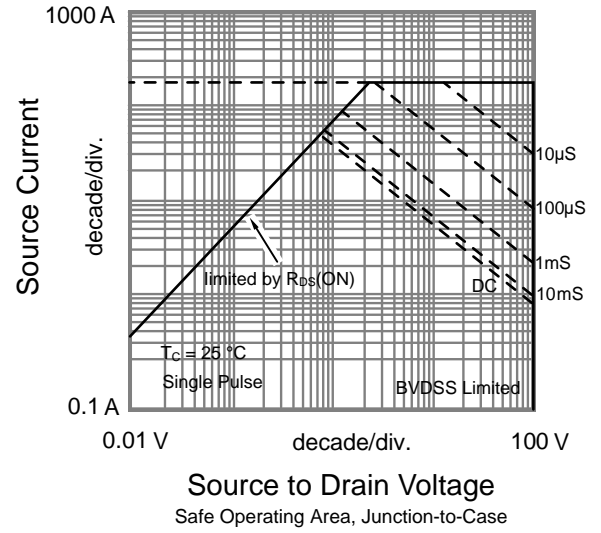
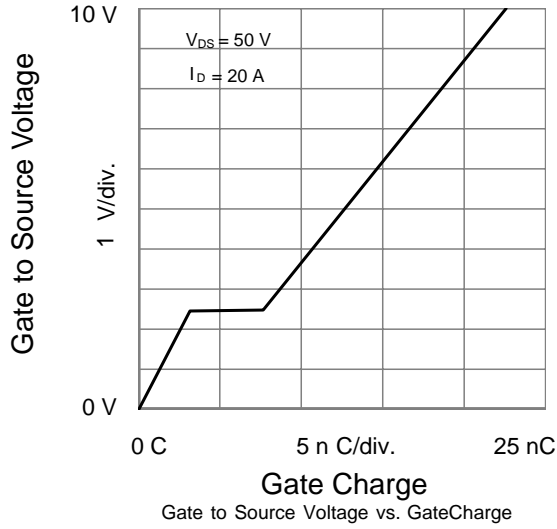


**Source to Drain Voltage Body Diode Forward Characteristics**

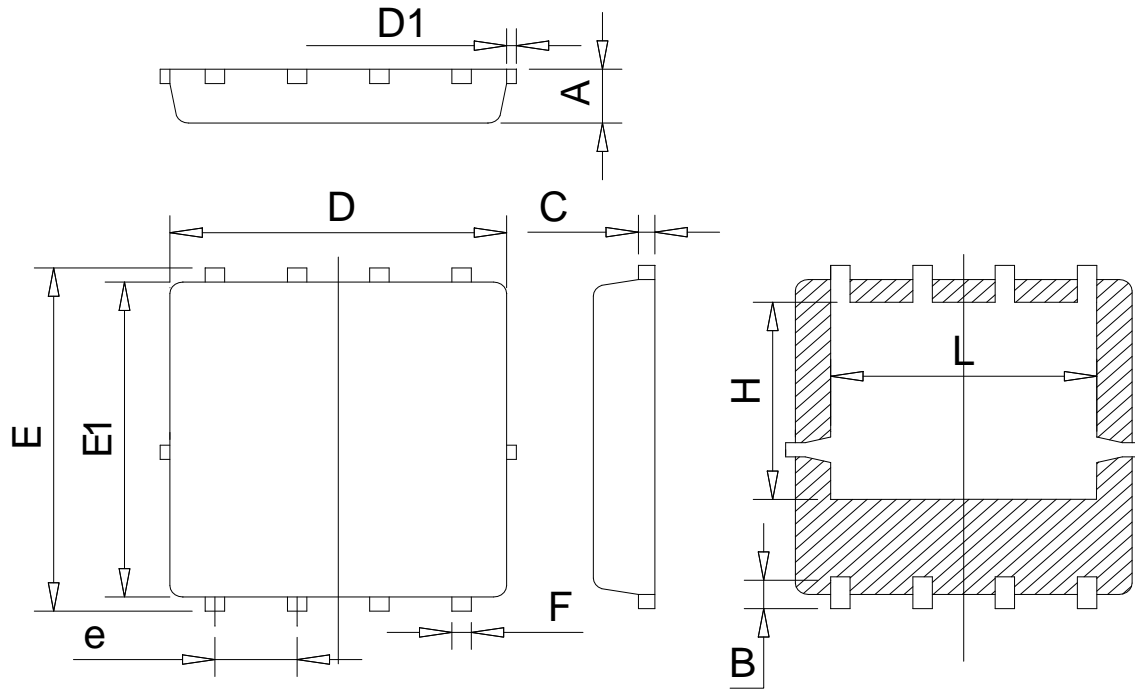


**Drain to Source Voltage Capacitances**

**TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**



**DFN 5X6-8L PACKAGE OUTLINE**



**COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)**

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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