

P-Channel 20-V (D-S) MOSFET

| PRODUCT SUMMARY | | | | | | |
|---------------------|-------------------------------------|---------------------------------|-----------------------|--|--|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) ^a | Q _g (Typ.) | | | |
| - 20 | 0.0019 at V _{GS} = - 10 V | - 120 | | | | |
| | 0.0021 at $V_{GS} = -4.5 \text{ V}$ | - 100 | 120 nC | | | |
| | 0.0029 at V _{GS} = - 2.5 V | - 80 | | | | |

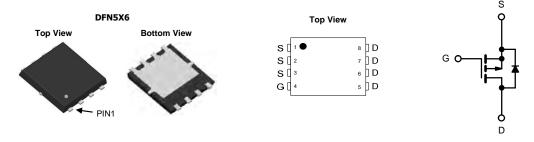
FEATURES

- · DT-Trench Power MOSFET
- 100 % R_g Tested



APPLICATIONS

- Notebook
 - Load Switch



P-Channel MOSFET

| Parameter | | Symbol | Limit | Unit | |
|--|---|-----------------|---|------|--|
| Drain-Source Voltage | | V _{DS} | - 20 | V | |
| Gate-Source Voltage | | V_{GS} | ± 12 | | |
| Continuous Drain Current (T _J = 150 °C) | $T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$ | I _D | - 120 ^a - 100 ^a - 31.6 ^{b, c} - 25.3 ^{b, c} | | |
| Pulsed Drain Current | I _{DM} | - 370 | A | | |
| Continuous Source-Drain Diode Current | $T_C = 25 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$ | I _S | - 70 ^a - 56 ^{b, c} | | |
| Single Pulse Avalanche Current | L = 0.1 mH | I _{AS} | - 60 | | |
| Single Pulse Avalanche Energy | L = 0.111111 | E _{AS} | 250 | mJ | |
| Maximum Power Dissipation | $T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$ | P _D | 170 109 7.95 ^{b, c} 5.1 ^{b, c} | w | |
| Operating Junction and Storage Temperature Ra | T _J , T _{stg} | - 55 to 150 | 00 | | |
| Soldering Recommendations (Peak Temperature | | 260 | °C | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|---|--------------|-------------------|---------|---------|------|--|
| Parameter | | Symbol | Typical | Maximum | Unit | |
| Maximum Junction-to-Ambient ^{b, f} | t ≤ 10 s | R _{thJA} | 15 | 20 | °C/W | |
| Maximum Junction-to-Case (Drain) | Steady State | R_{thJC} | 0.9 | 1.2 | | |

Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. The DFN5x6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 54 °C/W.



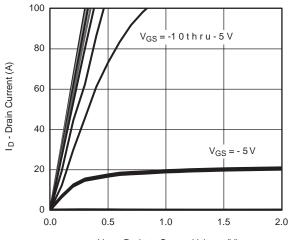
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|-------------------------|---|---|--------|--------|-----------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | $V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$ | - 20 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = - 250 μA | | - 31 | | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | 1 _D = - 230 μΛ | | 6.5 | |] 11107 C | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | - 0.5 | | - 2.0 | V | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$ | | | ± 100 | nA | |
| Zoro Cata Valtaga Drain Current | 1 | V _{DS} = - 16 V, V _{GS} = 0 V | V _{DS} = - 16 V, V _{GS} = 0 V | | - 1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = - 16 V, V _{GS} = 0 V, T _J = 55 °C | | | - 10 | μA | |
| On-State Drain Current ^a | I _{D(on)} | V _{DS} = - 5 V, V _{GS} = - 10 V | - 300 | | | Α | |
| | | V _{GS} = - 10 V, I _D = - 20 A | | 0.0019 | 0.0023 | 1 | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = - 4.5 V, I _D = - 20 A | | 0.0021 | 0.0027 | Ω | |
| | | V _{GS} = - 2.5 V, I _D = - 15 A | | 0.0029 | 0.0039 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 15 V, I _D = - 20 A | | 93 | | S | |
| Dynamic ^b | | | | | | , | |
| Input Capacitance | C _{iss} | | | 10450 | | | |
| Output Capacitance | C _{oss} | V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz | | 1931 | | рF | |
| Reverse Transfer Capacitance | C _{rss} | | | 976 | | <u> </u> | |
| Total Gate Charge | Q _g | V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = - 20 A | | 230 | | | |
| | | | | 78 | | 1 | |
| Gate-Source Charge | Q _{gs} | $V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V}, I_{D} = -20 \text{ A}$ | | 29 | | nC | |
| Gate-Drain Charge | Q_{gd} | | | 37 | | | |
| Gate Resistance | R_g | f = 1 MHz | | 1.9 | | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 25 | | | |
| Rise Time | t _r | V_{DD} = - 15 V, R_L = 15 Ω | | 15 | | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong$ - 1.0 A, V_{GEN} = - 10 V, R_g = 1 Ω | | 110 | | | |
| Fall Time | t _f | | | 30 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 110 | | ns | |
| Rise Time | t _r | $V_{DD} = -15 \text{ V}, R_{L} = 15 \Omega$ | | 100 | | • | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$ | | 100 | | | |
| Fall Time | t _f | | | 50 | | | |
| Drain-Source Body Diode Characteristic | s | | | | | | |
| Continuous Source-Drain Diode Current | I _S | T _C = 25 °C | | | 100 | ^ | |
| Pulse Diode Forward Current ^a | I _{SM} | | | | 370 | Α | |
| Body Diode Voltage | V_{SD} | I _S = - 5 A | | - 0.54 | - 1.1 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | t _{rr} | | 50 | | ns | |
| Body Diode Reverse Recovery Charge | Q _{rr} | | | 65 | | nC | |
| Reverse Recovery Fall Time | t _a | $I_F = 3.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$ | | 26 | | ns | |
| Reverse Recovery Rise Time | t _b | | | 24 | | | |

Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

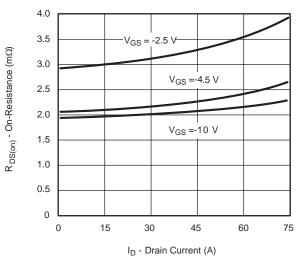
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



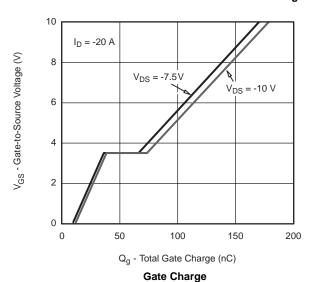


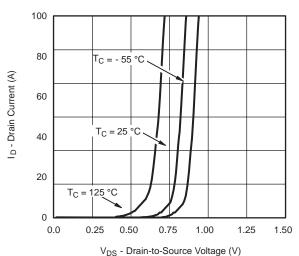
 V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics

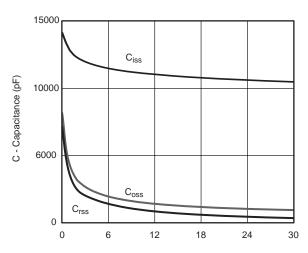


On-Resistance vs. Drain Current and Gate Voltage



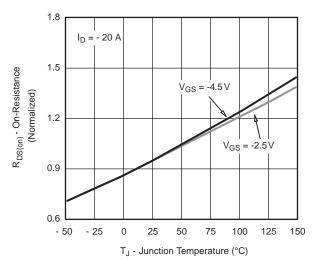






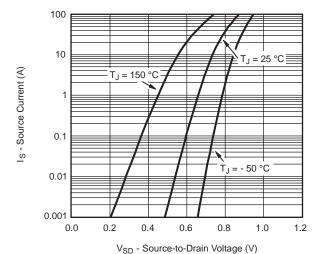
V_{DS} - Drain-to-Source Voltage (V)

Capacitance

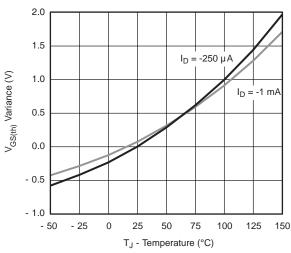


On-Resistance vs. Junction Temperature

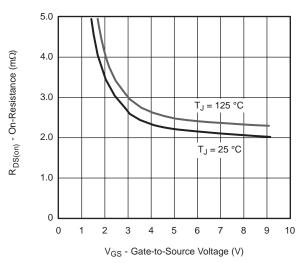




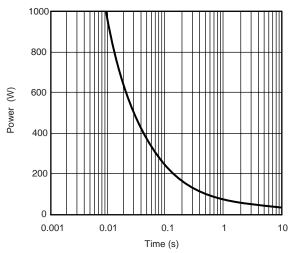
Source-Drain Diode Forward Voltage



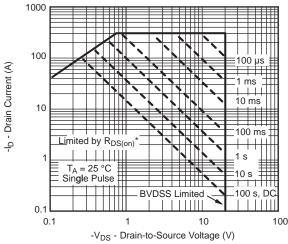
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



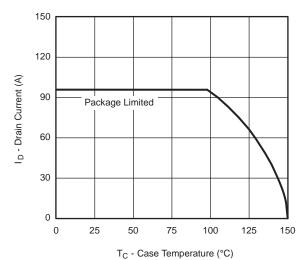
Single Pulse Power, Junction-to-Ambient



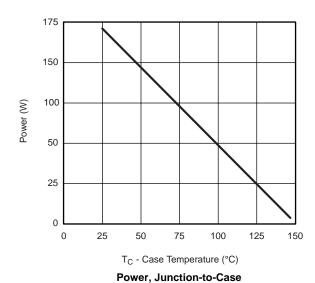
* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

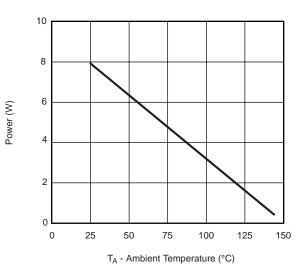
Safe Operating Area, Junction-to-Ambient





Current Derating*

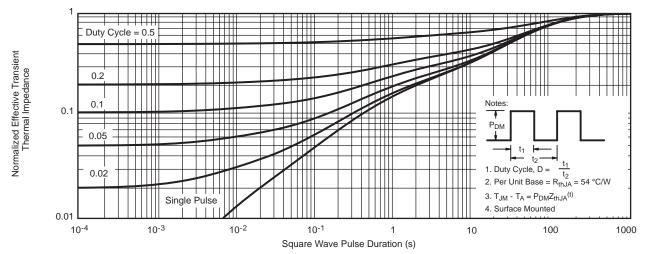




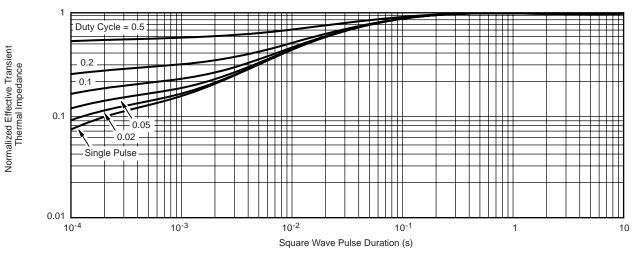
Power, Junction-to-Ambient

 $^{^*}$ The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





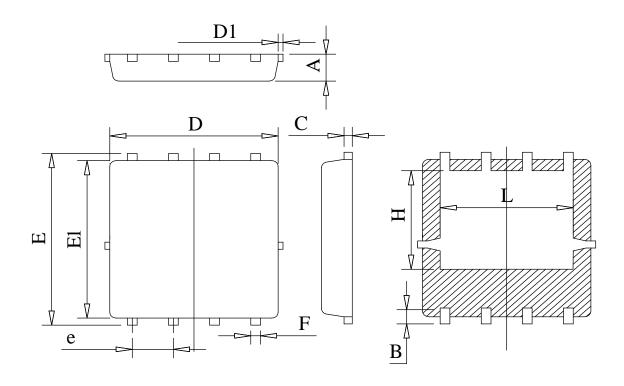
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit: mm

| Symbol | Min | Тур | Max |
|--------|------|-------|------|
| A | 0.78 | 0.95 | 1.12 |
| В | 0.45 | 0.58 | 0.78 |
| C | 0.18 | 0.254 | 0.36 |
| D | 4.70 | 5.20 | 5.45 |
| D1 | | | 0.18 |
| Е | 5.85 | 6.05 | 6.25 |
| E1 | 5.38 | 5.55 | 5.98 |
| e | 1.15 | 1.27 | 1.40 |
| F | 0.18 | 0.30 | 0.52 |
| Н | 3.25 | 3.47 | 3.70 |
| L | 3.75 | 4.00 | 4.25 |





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