P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)		
- 30	8.9 at V _{GS} = - 10 V	- 100	78 nC		
- 50	12.5 at V _{GS} = - 4.5 V	- 100			

FEATURES

• DT-Trench Power MOSFET

GO

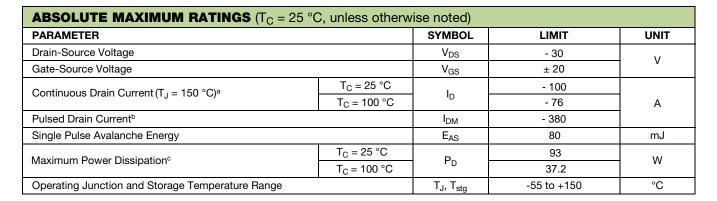
- 100 % Rg and UIS Tested
- · High power and current handing capability

D

P-Channel MOSFET

APPLICATIONS

- Load switch
- PWM application



THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB mount ^d	R _{thJA}	50	°C/W	
Junction-to-Case		R _{thJC}	1.35	0/10	

Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R_{0JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.

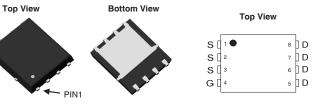




DTQ6301 www.din-tek.jp









Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static		• •				
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 30			V
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	- 1		3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V		- 1		
Zero Gate Voltage Drain Current		V _{DS} = - 24 V, V _{GS} = 0 V, T _J = 55 °C			- 10 µA	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 100			Α
	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		8.9	9.8	_
Drain-Source On-State Resistance ^a		V _{GS} = - 4.5 V, I _D = - 15 A		12.5	13.8	mΩ
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = -20 A		55		S
Dynamic ^b						
Input Capacitance	C _{iss}			3330		pF
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		384		
Reverse Transfer Capacitance	C _{rss}			368		
Total Gate Charge	Qg			78		
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -20 \text{ A}$		15		nC
Gate-Drain Charge	Q _{gd}			9		
Gate Resistance	Rg	f = 1 MHz		6.5		Ω
Turn-On Delay Time	t _{d(on)}			12		
Rise Time	t _r	V _{DD} = - 15 V, I _D = - 10 A,		15		ns
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = -10 \text{ V}, \text{ Rg} = 3 \Omega$		39		
Fall Time	t _f			21		
Drain-Source Body Diode Characteristics	;					
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 100	A
Pulse Diode Forward Currenta	I _{SM}				- 380	A
Body Diode Voltage	V _{SD}	I _S = - 1 A			- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}			48		ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 10 A, di/dt = 100 A/μs, T _J = 25 °C		65		nC

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

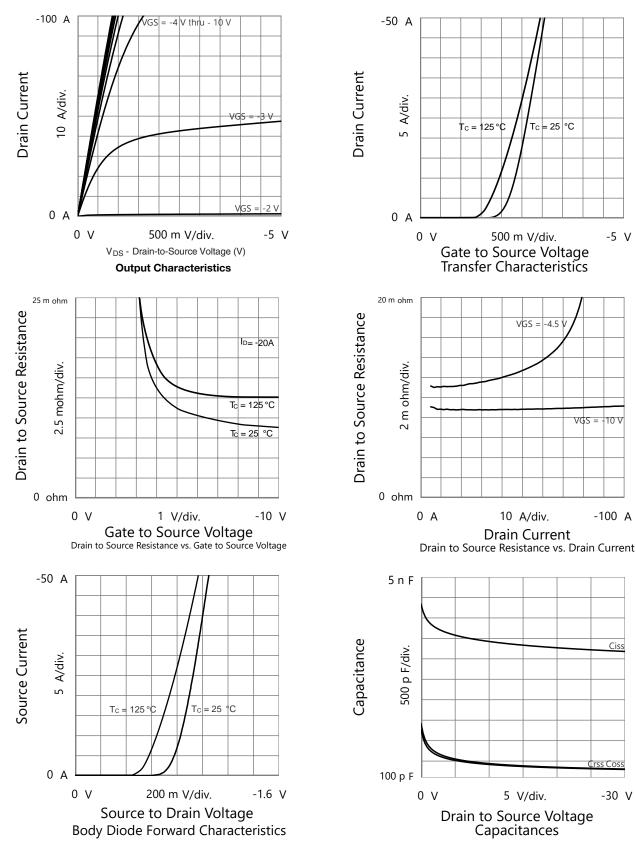


-5 V

10 V

Ciss

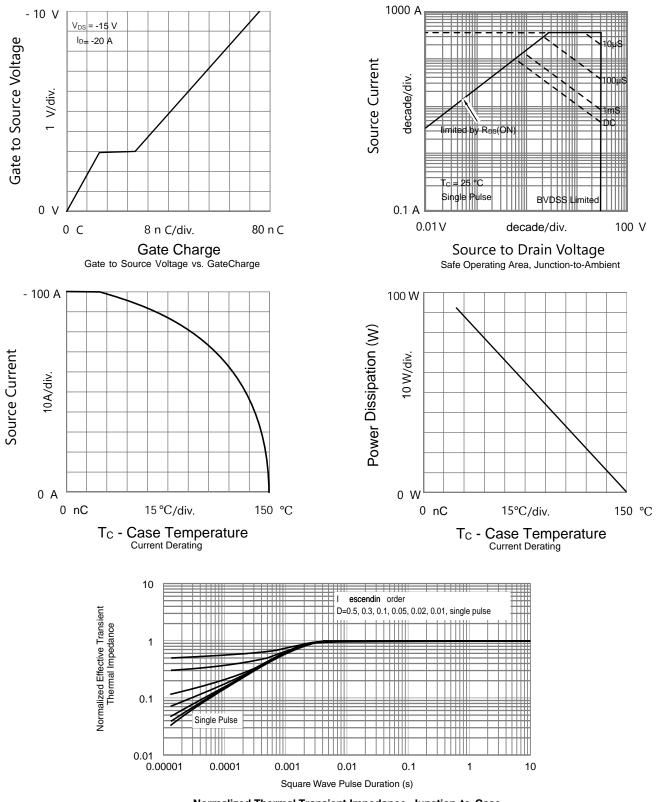
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



-30 V



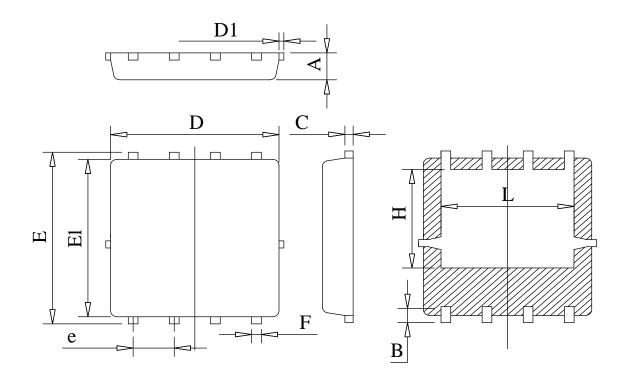
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case



DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm			
Symbol	Min	Тур	Max
А	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25



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