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N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^{a, e}	Q _g (Typ.)			
30	0.0032 at V _{GS} = 10 V	88	49 nC			
30	0.0047 at V _{GS} = 4.5 V	79	43110			

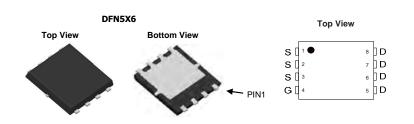
FEATURES

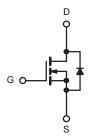
- **DT-Trench Power MOSFET**
- 100 % R_g and UIS Tested



APPLICATIONS

- · Notebook PC Core
- VRM/POL





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V_{DS}	30	V			
Gate-Source Voltage		V _{GS}	± 20			
	T _C = 25 °C		88 ^{a, e}			
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 70 °C	l-	75 ^e			
Continuous Diam Current (1) = 173 C)	T _A = 25 °C	I _D	26 ^{b, c}	А		
	T _A = 70 °C		24 ^{b, c}	Α		
Pulsed Drain Current		I _{DM}	264			
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	33			
Single Pulse Avalanche Energy			85	mJ		
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	88 ^{a, e}	А		
Continuous Source-Diairi Diode Current	T _A = 25 °C	18	3.6 ^{b, c}			
	T _C = 25 °C		220 ^a	W		
Maximum Power Dissipation	T _C = 70 °C	P _D	167			
Maximum Fower Dissipation	T _A = 25 °C	' D	3.8 ^{b, c}	VV		
	T _A = 70 °C		2.6 ^{b, c}			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	30	40	°C/W	
Maximum Junction-to-Case	Steady State	R_{thJC}	0.5	0.9	C/VV	

- a. Based on $T_C = 25$ °C. b. Surface mounted on 1" x 1" FR4 board.

- c. t = 10 s.
 d. Maximum under steady state conditions is 90 °C/W.
 e. Calculated based on maximum junction temperature. Package limitation current is 80 A.



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Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		>//90	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	ι _D = 230 μΑ		- 5.5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta da Busin Comunit	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	264			Α	
	В	V _{GS} = 10 V, I _D = 32 A		0.0032	0.0035		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 29 \text{ A}$		0.0047	0.0052	Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 32 \text{ A}$		149		S	
Dynamic ^b			l.		<u> </u>		
Input Capacitance	C _{iss}			1233		pF	
Output Capacitance	C _{oss}	V_{DS} = 12.5 V, V_{GS} = 0 V, f = 1 MHz		920			
Reverse Transfer Capacitance	C _{rss}			480			
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 32 \text{ A}$		49		nC	
				41			
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 29 \text{ A}$		25			
Gate-Drain Charge	Q _{gd}			23			
Gate Resistance	R _g	f = 1 MHz		1.2	2.0	Ω	
Turn-On Delay Time	t _{d(on)}			17	26		
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.555 Ω		10	16	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 27$ A, $V_{GEN}=10$ V, $R_g=1$ Ω		65	100		
Fall Time	t _f			10	15		
Turn-On Delay Time	t _{d(on)}			50	81		
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		170	250		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 24$ A, $V_{GEN}=4.5$ V, $R_g=1$ Ω		50	75		
Fall Time	t _f			10	16		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	$T_C = 25$ °C			88	۸	
Pulse Diode Forward Current ^a	I _{SM}				264	Α	
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			48	70	ns	
Body Diode Reverse Recovery Charge Q _{rr}		I _F = 20 A, di/dt = 100 A/μs, T _{.1} = 25 °C		65	93	nC	
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}$, $I_J = 25 \text{ °C}$		24			
Reverse Recovery Rise Time	ecovery Rise Time t _b			20		ns	

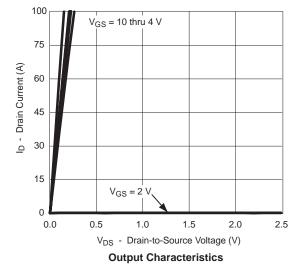
Notes

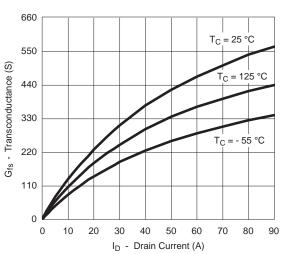
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

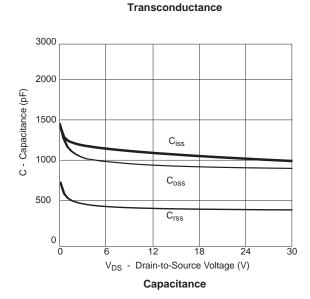
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

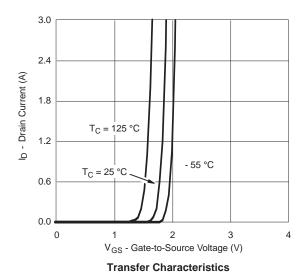


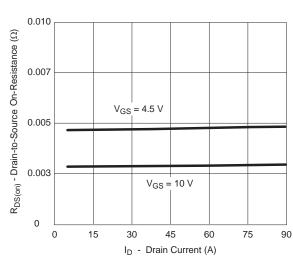
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

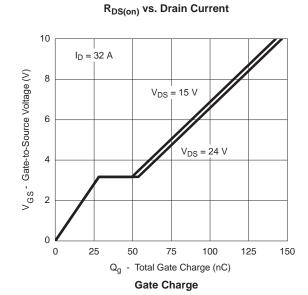






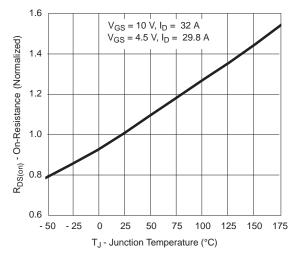




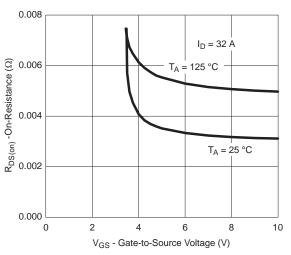




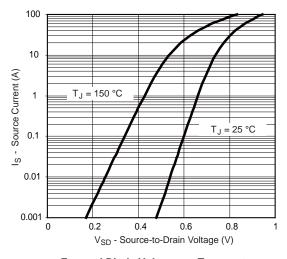
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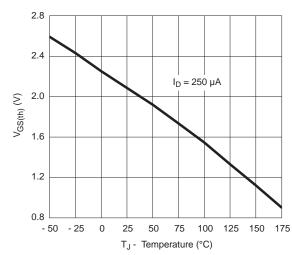
On-Resistance vs. Junction Temperature



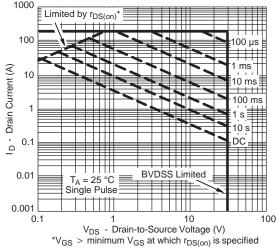
R_{DS(on)} vs. V_{GS} vs. Temperature



Forward Diode Voltage vs. Temperature



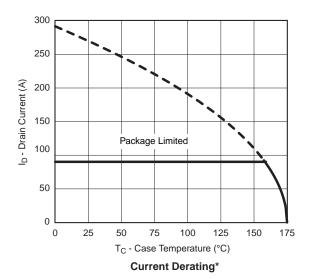
Threshold Voltage

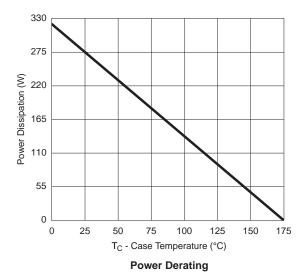


Safe Operating Area, Junction-to-Ambient

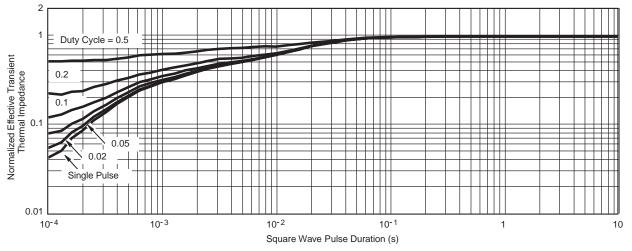


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





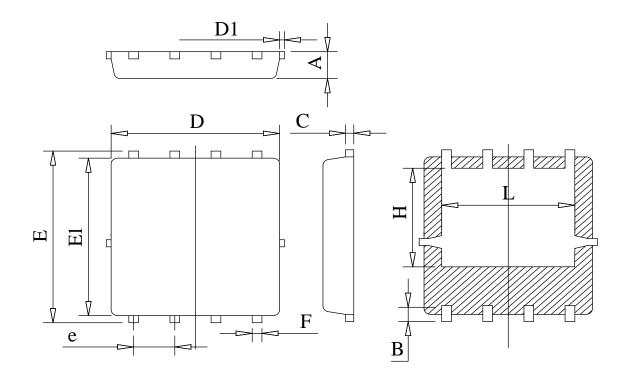
 * The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Case

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DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit: mm

Symbol	Min	Тур	Max
A	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25



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