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RoHS COMPLIANT

N-Channel 40 V (D-S) MOSFET

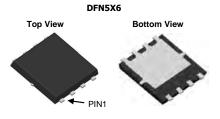
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ.)		
40	0.0012 at V _{GS} = 10 V	170	125 nC		
40	0.0016 at V_{GS} = 4.5 V	150	125110		

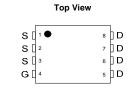
FEATURES

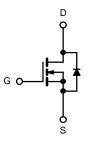
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Notebook PC Core
- VRM/POL ٠







N-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	40	V	
Gate-Source Voltage		V _{GS}	± 20	V	
	T _C = 25 °C		170 ^{a, e}		
Continuous Drain Current (T. -175 °C)	T _C = 70 °C		150 ^e		
Continuous Drain Current (T _J = 175 °C)	T _A = 25 °C	I _D	39 ^{b, c}		
	T _A = 70 °C		28.6 ^{b, c}		
Pulsed Drain Current		I _{DM}	400		
Avalanche Current Pulse	1 0.1 ml l	I _{AS}	41	mJ	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	550		
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	170 ^{a, e}	Α	
Communus Source-Drain Diode Current	T _A = 25 °C	'S	5.36 ^{b, c}		
	T _C = 25 °C		120 ^a	w	
Maximum Bawar Dissinction	T _C = 70 °C	PD	105		
Maximum Power Dissipation	T _A = 25 °C	'D	4.15 ^{b, c}		
	T _A = 70 °C		2.87 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 \text{ s}$	R _{thJA}	14	18	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.45	0.7	0/11	

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 90 °C/W.

e. Calculated based on maximum junction temperature. Package limitation current is 80 A.

Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	40			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 230 \mu A$		- 5.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 32 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	/		1	
Zero Gate Voltage Drain Current		$V_{DS} = 32 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, V_{GS} = 10 V	170			А
	Р	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$		0.0012	0.0016	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0016	0.0020	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 15 \text{ A}$		80		S
Dynamic ^b	I					
Input Capacitance	C _{iss}			4385		pF
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		1975		
Reverse Transfer Capacitance	C _{rss}			110		
Total Cata Charge	Qg	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		125		nC
Total Gate Charge				57.3		
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 20 A		18		
Gate-Drain Charge	Q _{gd}			13		
Gate Resistance	R _g	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t _{d(on)}			14	22	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, \text{ R}_{L} = 0.555 \Omega$		10	16	
Turn-Off Delay Time	t _{d(off)}	$I_{D} \cong$ 30A, V_{GEN} = 10 V, R_{g} = 1 Ω		56	85	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			12	20	ns -
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		150	220	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ 20 A, V_{GEN} = 4.5 V, R_{g} = 1 Ω		55	83	
Fall Time	t _f			12	18	
Drain-Source Body Diode Characteristic	s			•		
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			170	٨
Pulse Diode Forward Current ^a	I _{SM}				400	A
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			35	58	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 20 A, di/dt = 100 A/μs, T _J = 25 °C		90.2	125	nC
Reverse Recovery Fall Time	t _a	$r_F = 20 \text{ A}, \text{ al/al} = 100 \text{ A/}\text{µs}, \text{I}_\text{J} = 25 ^{\circ}\text{C}$		27		ns
Reverse Recovery Rise Time	t _b			25		

Notes:

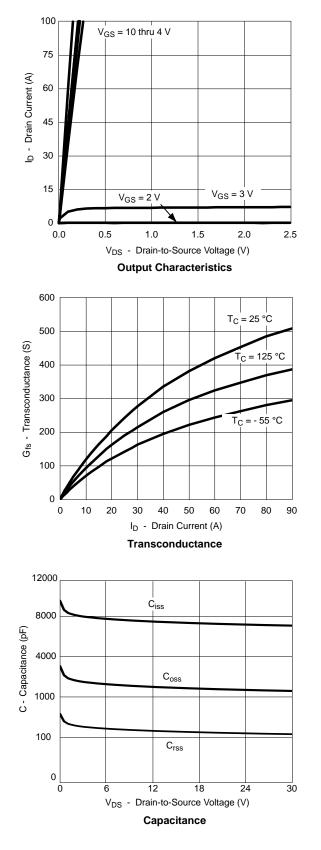
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

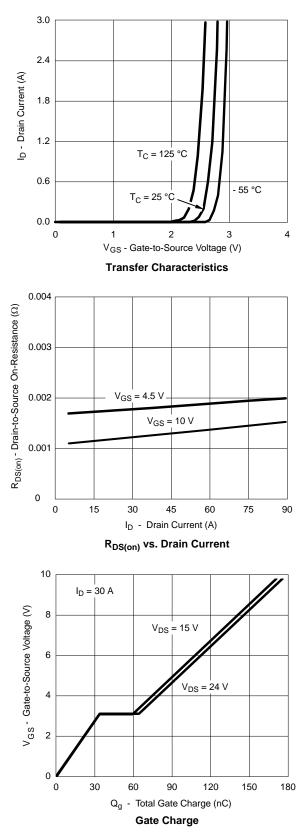
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

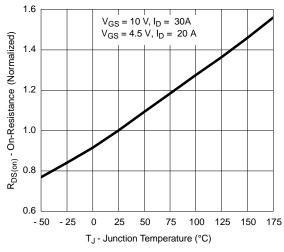


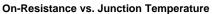


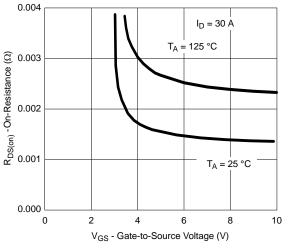


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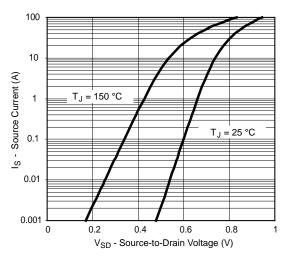
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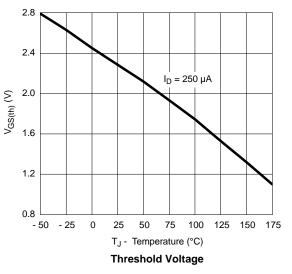


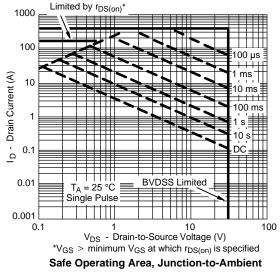


R_{DS(on)} vs. V_{GS} vs. Temperature



Forward Diode Voltage vs. Temperature



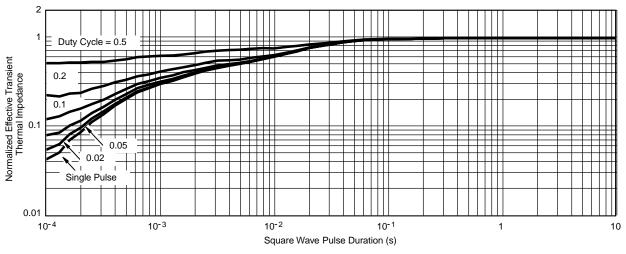




Power Dissipation (W) I_D - Drain Current (A) Package Limited T_C - Case Temperature (°C) T_C - Case Temperature (°C) **Current Derating* Power Derating**

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

* The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

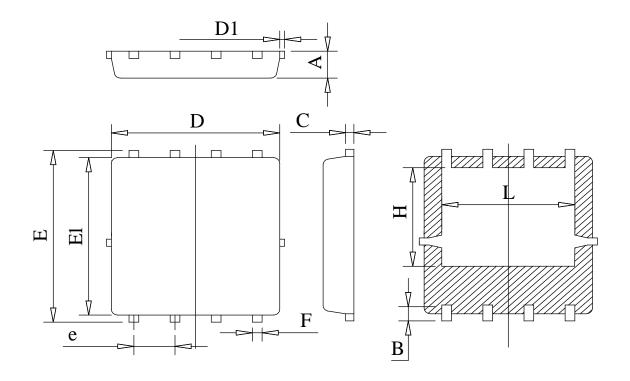


Normalized Thermal Transient Impedance, Junction-to-Case

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DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm			
Symbol	Min	Тур	Max
А	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25



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