

DTQ6400-BC www.din-tek.jp

# N-Channel 40 V (D-S) Power MOSFET

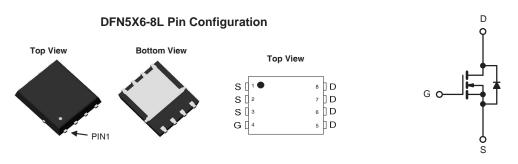
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
40	1.2 at V <sub>GS</sub> = 10 V	170	70 nC		
40	2.0 at V <sub>GS</sub> = 4.5 V	170			

### FEATURES

- DT-Trench Power MOSFET
- · Very low on-resistance
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)

#### **APPLICATIONS**

- Power Management
- Motor Drivers
- DC-DC Converters



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	40	v		
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current (T <sub>.1</sub> = 150 °C) <sup>a</sup>	T <sub>C</sub> = 25 °C	1	170			
Continuous Drain Current $(T_J = 150 \text{ C})^{\sim}$	T <sub>C</sub> = 100 °C	Ι <sub>D</sub>	120	A		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	510	]		
Single Pulse Avalanche Energy		E <sub>AS</sub>	1125	mJ		
Maximum Power Dissipation <sup>c</sup>	T <sub>C</sub> = 25 °C	PD	155 <sup>c</sup>	w		
	T <sub>C</sub> = 100 °C	۰D	38	VV		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C		
Soldering Recommendations (Peak Temperature)			260	] ~		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>d</sup>	t ≤ 10 s	R <sub>thJA</sub>	-	62	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	-	0.8	0/11

#### Notes

a. Calculated continuous current based on maximum allowablejunction temperature.

b. Repetitive rating; pulse width limited by max. junction temperature.

c. Pd is based on max. junction temperature, using junction-case thermal resistance.

d. The value of R<sub>0JA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.

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SPECIFICATIONS (T <sub>J</sub> = 25 °C PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static	OTTIDOL				111-17.	Unit	
Drain-Source Breakdown Voltage	VDS	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$		-	-	V	
Gate Threshold Voltage	1			_	3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	1	-	± 100	nA	
	I <sub>DSS</sub>	$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA	
Zero Gate Voltage Drain Current		$V_{DS} = 32 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55^{\circ}\text{C}$	-	-	10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 10 V$	170	-	-	А	
Drain Source On State Desistance 8		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	1.2	1.6	mΩ	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$	-	2	2.5		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	76	-	S	
Dynamic <sup>b</sup>				•			
Input Capacitance	C <sub>iss</sub>		-	4720	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 20 V, f = 1MHz	-	1590	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	16	-		
Total Gate Charge <sup>c</sup>	Qg		-	70	-	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 20 V, $V_{GS}$ = 10 V, $I_{D}$ = 20 A	-	8.3	-		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	13.6	-	1	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	2.8	-	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	12	-		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 20 \text{ V}, I_D = 20 \text{ A},$	-	9	-		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$V_{GEN}$ = 10 V, $R_g$ = 2 $\Omega$	-	38	-	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	10	-		
Drain-Source Body Diode Ratings and	nd Characteris	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)					
Continuous Source Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	170	Α	
Pulsed Source Current	I <sub>SM</sub>		-	-	510	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = 1 \text{ A}, V_{GS} = 0 \text{ V}$	-	-	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	L = 20  A  di/dt = 100  A/i	-	32	-	ns	
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs	-	112	-	nC	

#### Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

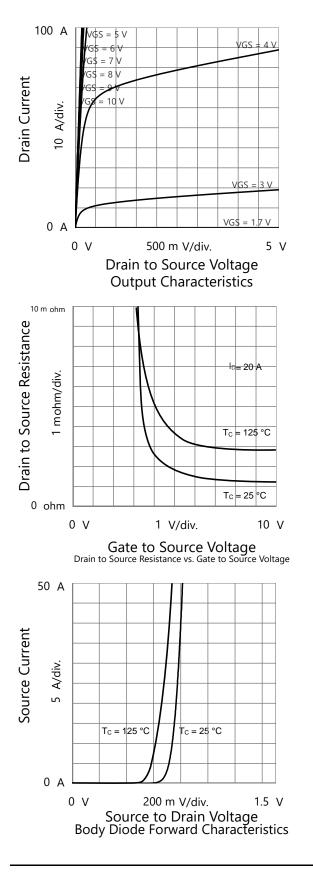
b. Guaranteed by design, not subject to production testing.

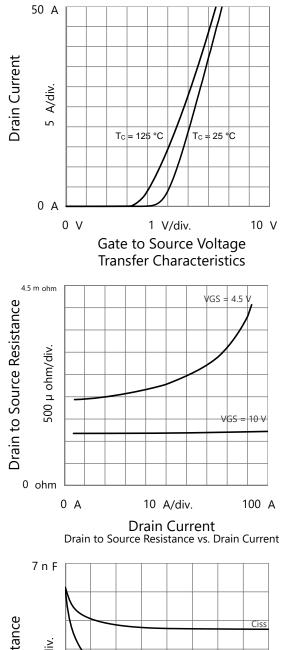
c. Independent of operating temperature.

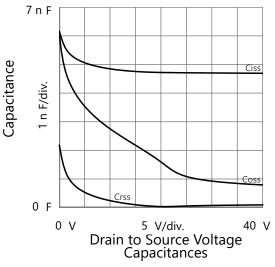
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)

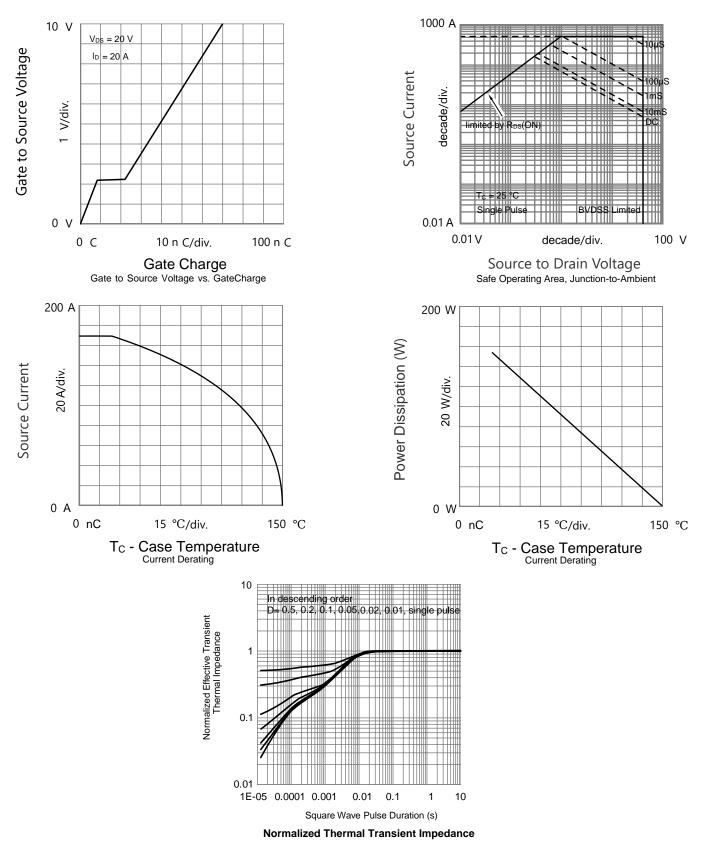




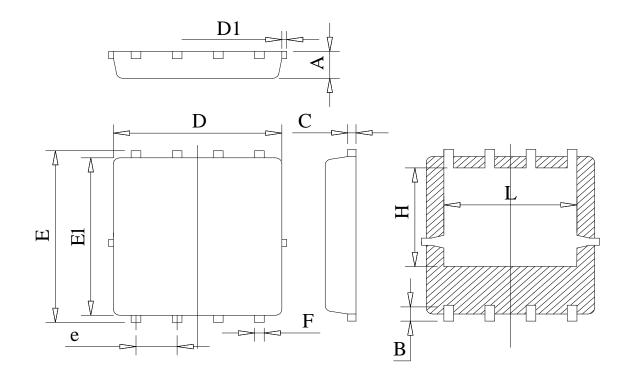




## **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



# DFN5\*6-8L PACKAGE OUTLINE



## COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm			
Symbol	Min	Тур	Max
A	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25



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