

## N-Channel 40 V (D-S) MOSFET

### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ (m $\Omega$ )(TYP.)	$I_D$ (A) <sup>a, e</sup>	$Q_g$ (TYP.)
40	1.4at $V_{GS} = 10$ V	120	40.4nC
	2.1at $V_{GS} = 4.5$ V	100	

### FEATURES

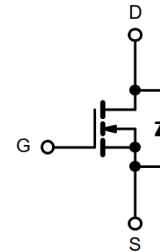
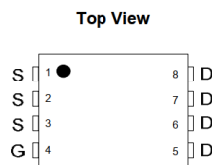
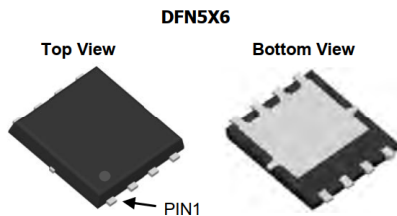
- DT-Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested
- AEC-Q101 Qualified for Automotive Applications



**RoHS**  
COMPLIANT

### APPLICATIONS

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175$ °C)	$I_D$	$T_C = 25$ °C	120 <sup>a, e</sup>
		$T_C = 70$ °C	100 <sup>e</sup>
		$T_A = 25$ °C	38 <sup>b, c</sup>
		$T_A = 70$ °C	26.9 <sup>b, c</sup>
Pulsed Drain Current	$I_{DM}$	420	A
Avalanche Current Pulse	$I_{AS}$	40	
Single Pulse Avalanche Energy	$E_{AS}$	540	
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25$ °C	120 <sup>a, e</sup>
		$T_A = 25$ °C	5.28 <sup>b, c</sup>
Maximum Power Dissipation	$P_D$	$T_C = 25$ °C	140 <sup>a</sup>
		$T_C = 70$ °C	98
		$T_A = 25$ °C	4.10 <sup>b, c</sup>
		$T_A = 70$ °C	2.82 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175	°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$R_{thJA}$	15	18	°C/W
Maximum Junction-to-Case	$R_{thJC}$	0.45	0.7	

Notes:

a. Based on  $T_C = 25$  °C.

b. Surface mounted on 1" x 1" FR4 board.

c.  $t = 10$  s.

d. Maximum under steady state conditions is 90 °C/W.

e. Calculated based on maximum junction temperature.

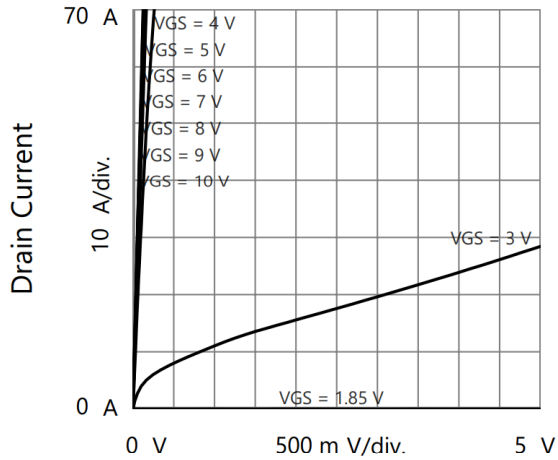
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	40			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		35		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.2		2.2	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 32\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	120			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$		1.4	1.9	m $\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 10\text{ A}$		2.1	2.9	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 15\text{ A}$		78		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		2960		pF
Output Capacitance	$C_{oss}$			898		
Reverse Transfer Capacitance	$C_{rss}$			63		
Total Gate Charge	$Q_g$	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V}, I_D = 20\text{ A}$		40.4		nC
Gate-Source Charge	$Q_{gs}$			5.81		
Gate-Drain Charge	$Q_{gd}$			92		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		14		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20\text{ V}, R_L = 0.555\text{ }\Omega$ $I_D \cong 20\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		16		ns
Rise Time	$t_r$			10		
Turn-Off Delay Time	$t_{d(off)}$			56		
Fall Time	$t_f$			10		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20\text{ V}, R_L = 0.625\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		12		
Rise Time	$t_r$			150		
Turn-Off Delay Time	$t_{d(off)}$			55		
Fall Time	$t_f$			12		
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			120	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				420	
Body Diode Voltage	$V_{SD}$	$I_S = 20\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		35		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			90.2		nC
Reverse Recovery Fall Time	$t_a$			27		ns
Reverse Recovery Rise Time	$t_b$			25		

Notes:

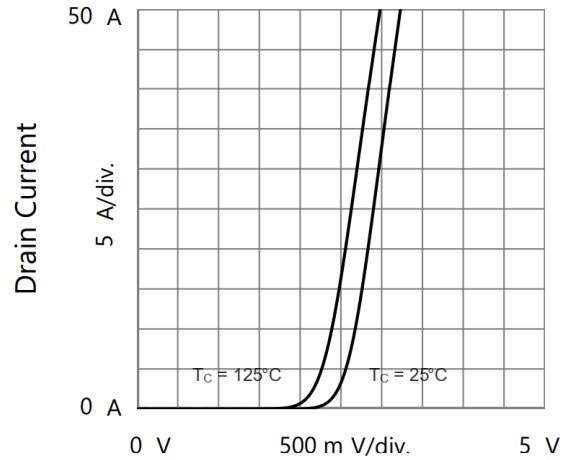
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

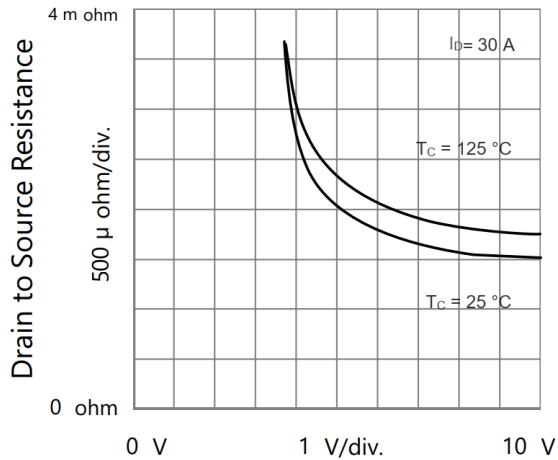
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



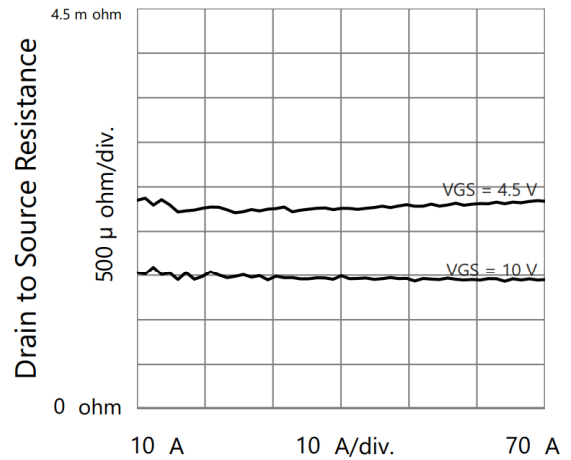
Drain to Source Voltage  
Output Characteristics



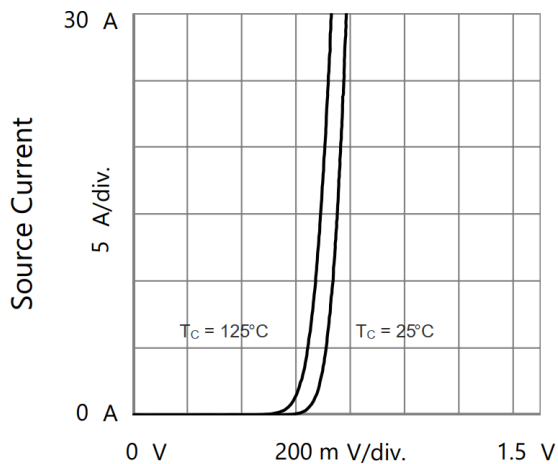
Gate to Source Voltage  
Transfer Characteristics



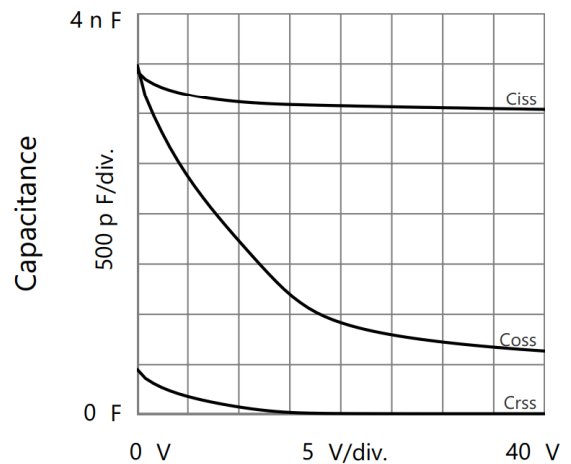
Gate to Source Voltage  
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current  
Drain to Source Resistance vs. Drain Current

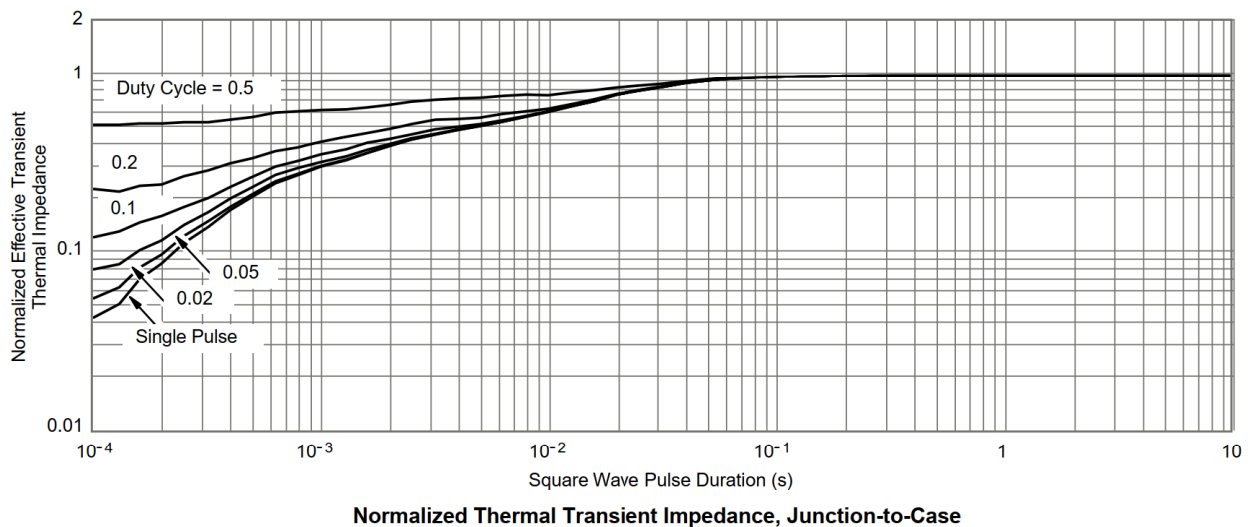
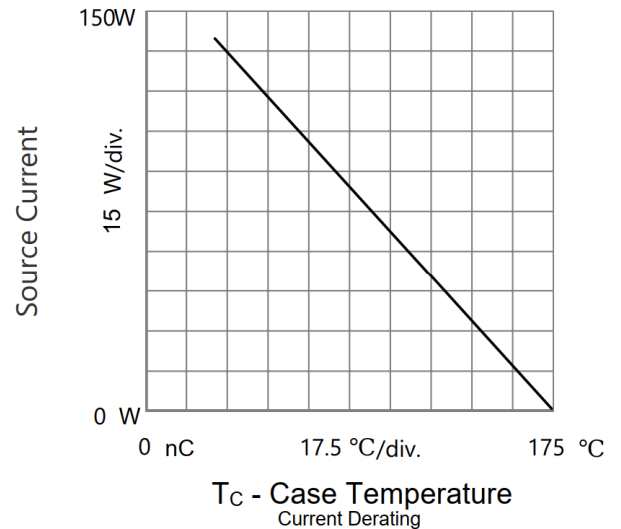
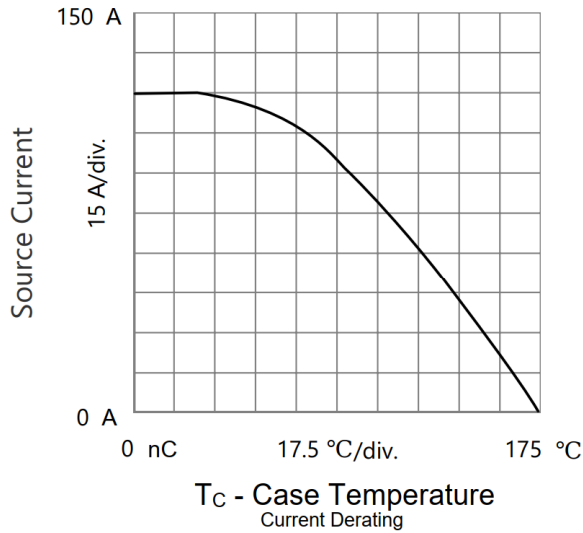
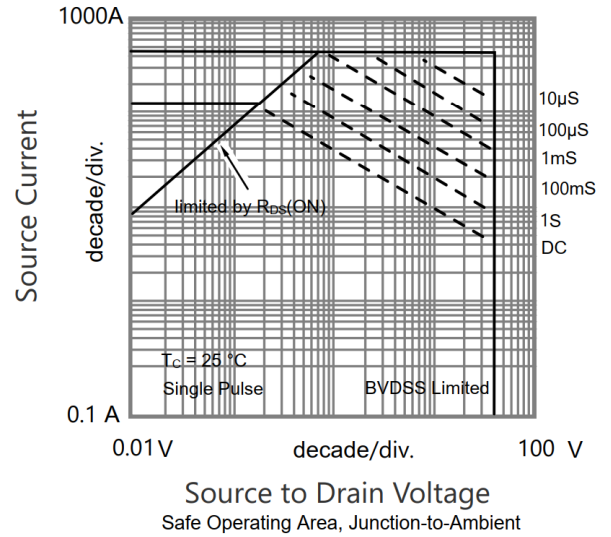
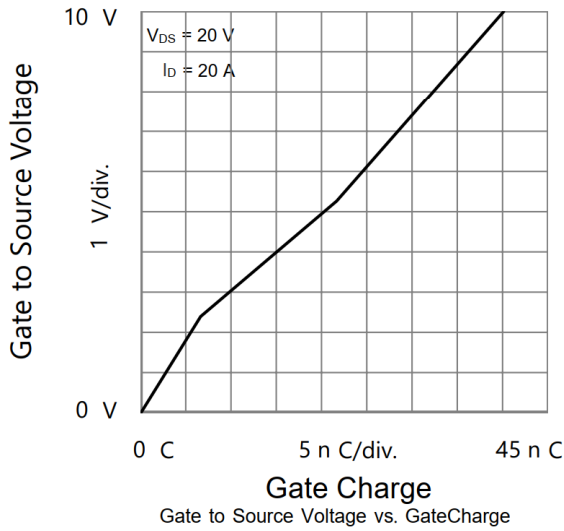


Source to Drain Voltage  
Body Diode Forward Characteristics

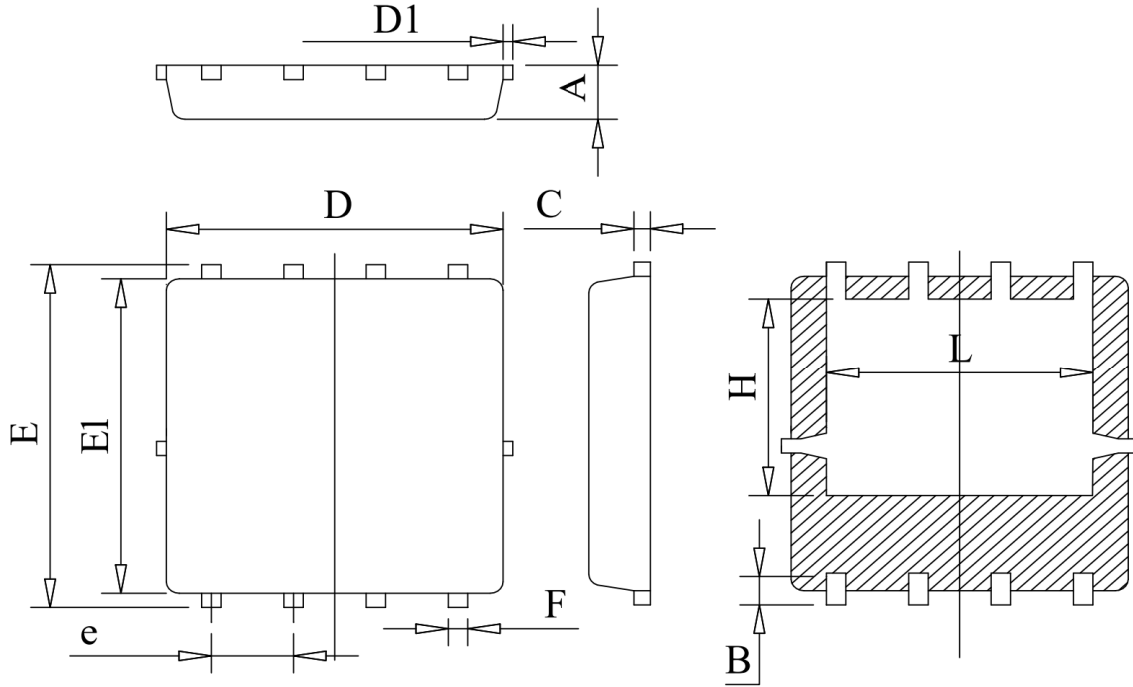


Drain to Source Voltage  
Capacitances

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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