

P-Channel 60-V (D-S) MOSFET



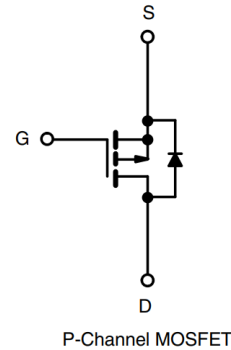
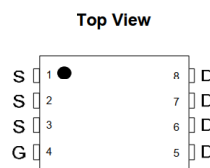
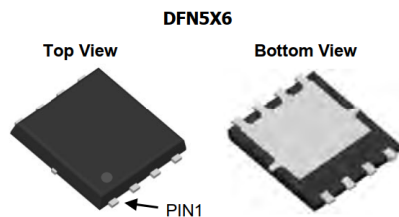
PRODUCT SUMMARY		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
-60	0.024 at V _{GS} = - 10 V	- 40
	0.029 at V _{GS} = - 4.5 V	- 30

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Notebook
- Load Switch



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	Limit	UNIT
Drain-Source Voltage		V _{DS}	- 60	V
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	- 40	A
	T _A = 70 °C		- 36	
Pulsed Drain Current		I _{DM}	- 160	
Continuous Source Current (Diode Conduction) ^a		I _S	- 40	
Avalanche Current	L = 0.1 mH	I _{AS}	- 37	
Single Pulse Avalanche Energy		E _{AS}	135	mJ
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	37	W
	T _A = 70 °C		23.7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C
Soldering Recommendations (Peak Temperature) ^{b, c}			260	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient ^a	t ≤ 10 s	R _{thJA}	20	45	°C/W
	Steady State		40	60	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.3	1.8	

Notes

- Surface mounted on 1" x 1" FR4 board.
- The DFN5x6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

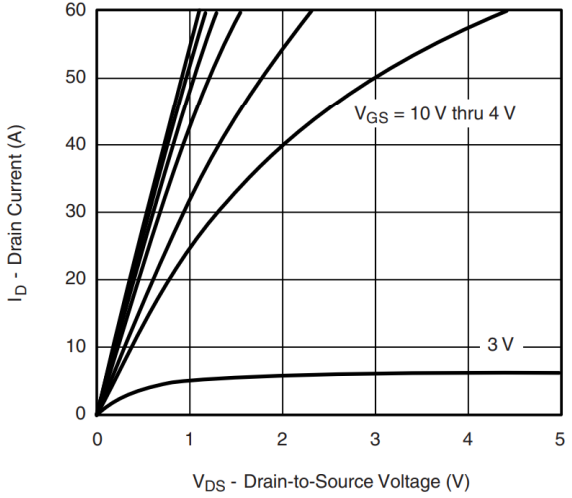
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1	-	-3	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -48\text{ V}, V_{GS} = 0\text{ V}$	-	-	-1	μA
		$V_{DS} = -48\text{ V}, V_{GS} = 0\text{ V}, T_J = 70\text{ }^\circ\text{C}$	-	-	-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}, V_{GS} = -10\text{ V}$	-40	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -15\text{ A}$	-	0.024	0.030	Ω
		$V_{GS} = -4.5\text{ V}, I_D = -10\text{ A}$	-	0.029	0.040	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}, I_D = -15\text{ A}$	-	28	-	S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1\text{ A}, V_{GS} = 0\text{ V}$	-	-0.7	-1.0	V
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	-	1610	-	pF
Output Capacitance	C_{oss}		-	202	-	
Reverse Transfer Capacitance	C_{rss}		-	19	-	
Total Gate Charge	Q_g	$V_{DS} = -30\text{ V}, V_{GS} = -10\text{ V}, I_D = -15\text{ A}$	-	56	-	nC
Gate-Source Charge	Q_{gs}		-	13	-	
Gate-Drain Charge	Q_{gd}		-	12	-	
Gate Resistance	R_g		-	2.5	-	Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -30\text{ V}, R_L = 30\text{ }\Omega$ $I_D \cong -15\text{ A}, V_{GEN} = -10\text{ V}, R_g = 6\text{ }\Omega$	-	20	-	ns
Rise Time	t_r		-	29	-	
Turn-Off Delay Time	$t_{d(off)}$		-	65	-	
Fall Time	t_f		-	23	-	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -15\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}$	-	25	-	ns
Source-Drain Reverse Recovery Charge	Q_{rr}	$I_F = -15\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}$	-	148	-	nC

Notes

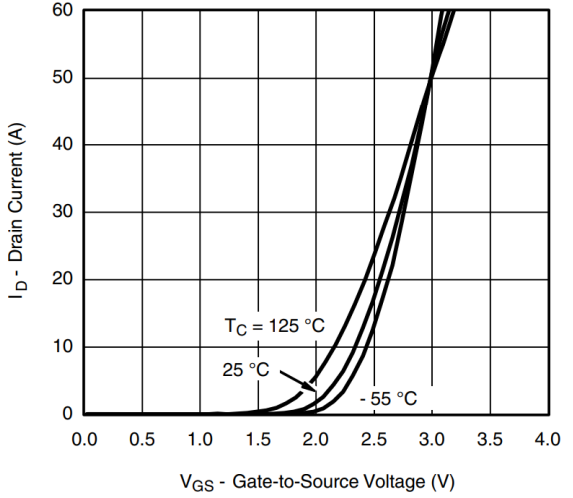
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

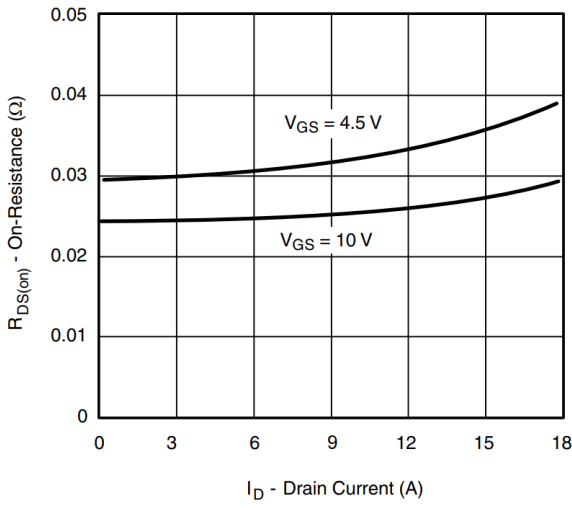
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



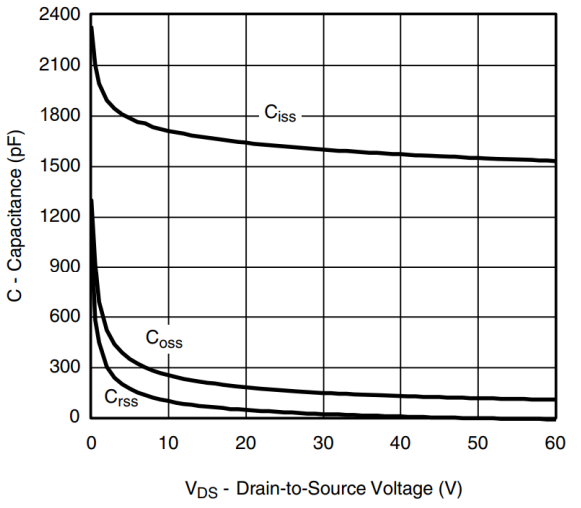
Output Characteristics



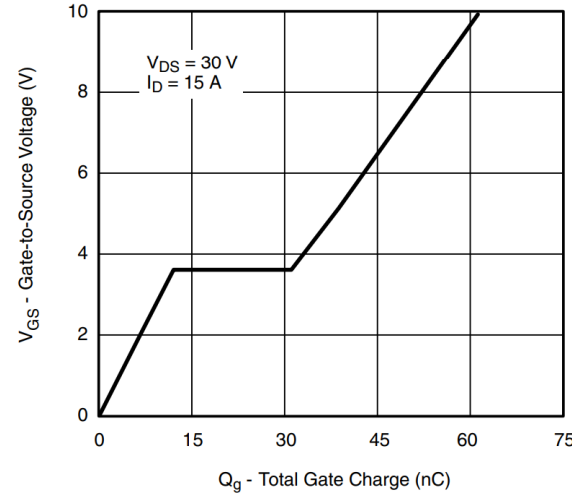
Transfer Characteristics



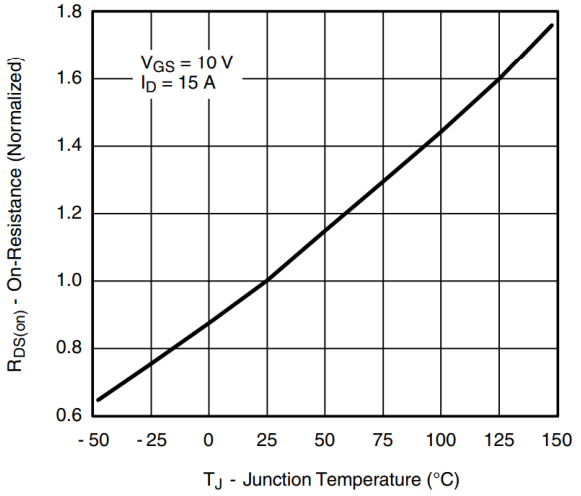
On-Resistance vs. Drain Current



Capacitance

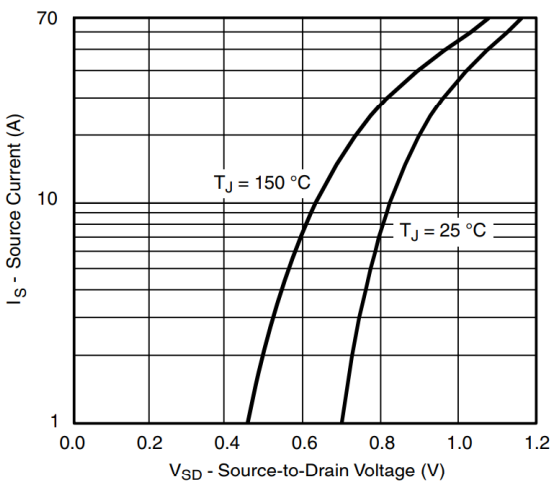


Gate Charge

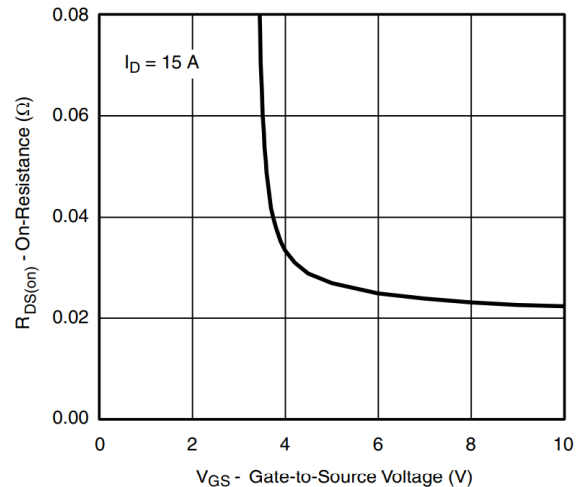


On-Resistance vs. Junction Temperature

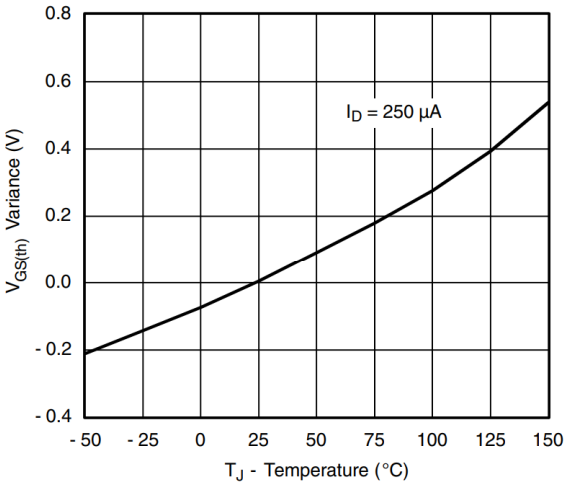
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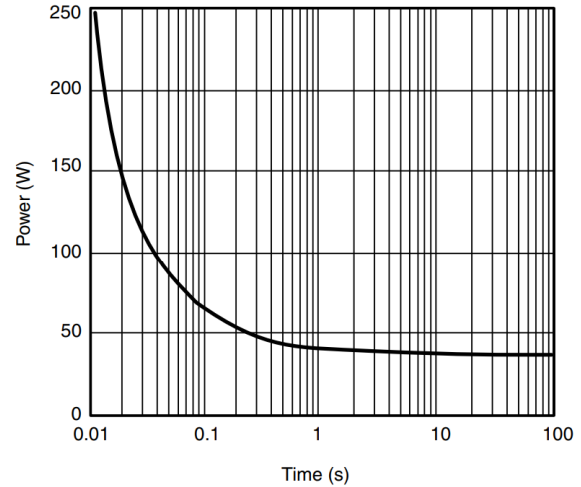
Source-Drain Diode Forward Voltage



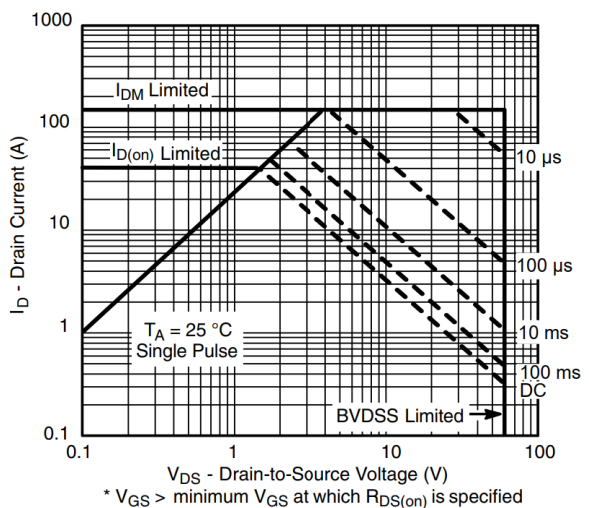
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

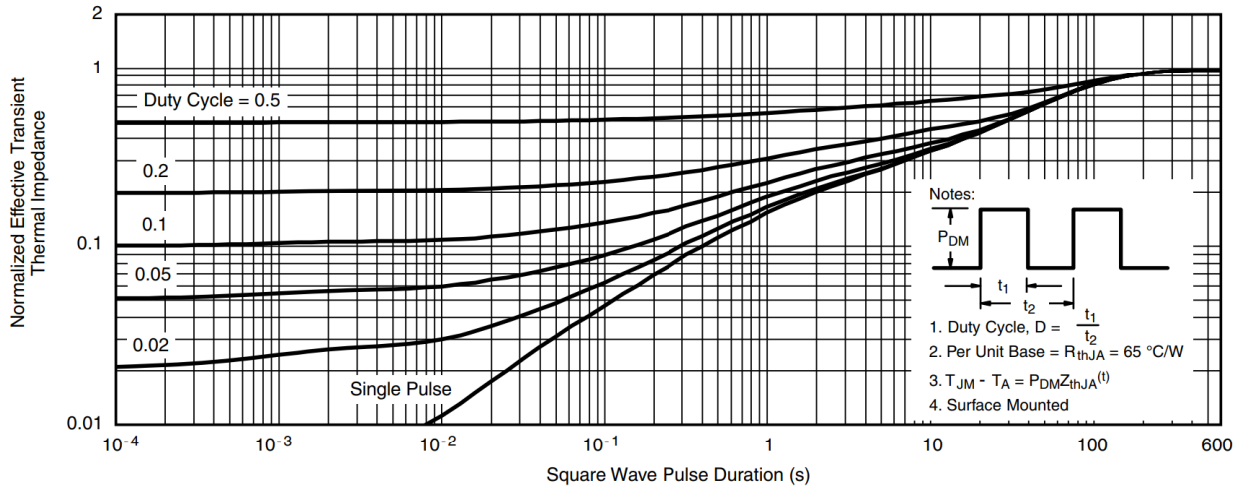


Single Pulse Power, Junction-to-Ambient

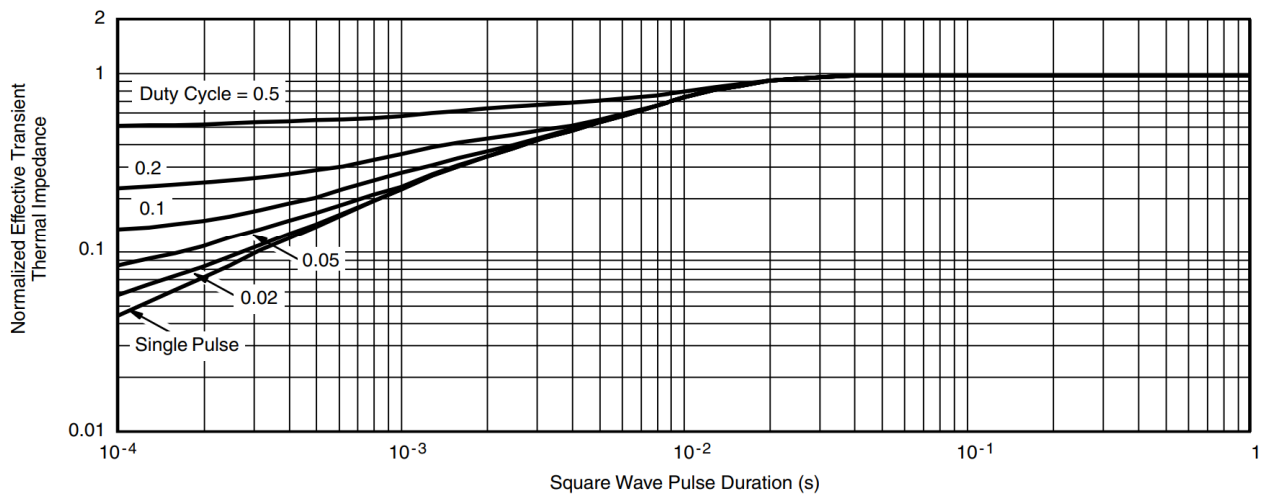


Safe Operating Area

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

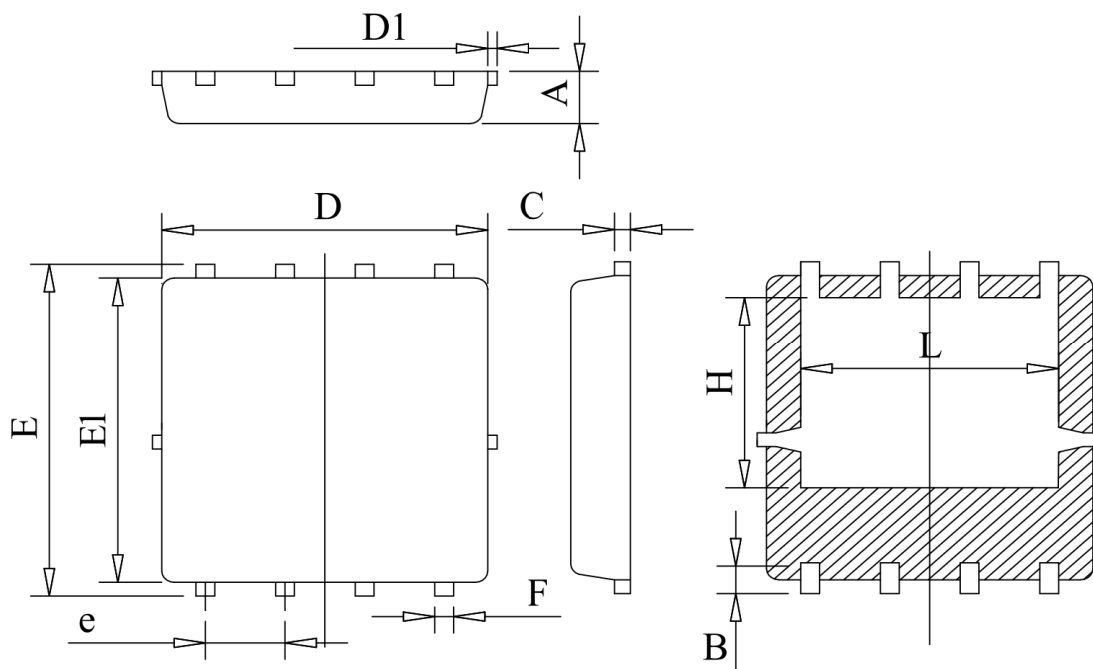


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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