

DTQ6654 www.din-tek.jp

> RoHS COMPLIANT

N-Channel 60 V (D-S) MOSFET

Top View

8] D

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S [] 2 S [] 3

G [] 4

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) Max.	I _D (A) ^a	Q _g (Typ.)			
60	0.0033 at V _{GS} = 10 V	95	51 nC			

Bottom View

DFN5X6

PIN1

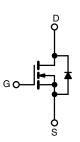
Top View

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Primary Side Switch
- Synchronous Rectifier
- DC/DC Converter
- DC/AC Inverters



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	60	V		
Gate-Source Voltage		V _{GS}			± 20
	T _C = 25 °C		95 ^a		
Continuous Drain Current (T 150 °C)	T _C = 70 °C	1 .	88 ^a		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	36.5 ^{b, c}		
	T _A = 70 °C		27 ^{b, c}	•	
Pulsed Drain Current (t = 300 µs)		I _{DM}	360	— A	
Continuous Source-Drain Diode Current	T _C = 25 °C		95 ^a		
Continuous Source-Drain Diode Current	T _A = 25 °C	۱ _S	10.3 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	93		
Single Pulse Avalanche Energy	L = 0.1 IIIH	E _{AS}	295	mJ	
	T _C = 25 °C		173		
Maximum Bawar Dissinction	T _C = 70 °C	Б	120	w	
Maximum Power Dissipation	T _A = 25 °C	– P _D	5.4 ^{b, c}	vv	
	T _A = 70 °C		3.3 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	14	23	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	0.8	1.5	0/11	

Notes:

a. Package limited.b. Surface mounted on 1" x 1" FR4 board.

d. Maximum under steady state conditions is 65 °C/W.

c. t = 10 s.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	60			V
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	1		3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 48 V, V _{GS} = 0 V V _{DS} = 48 V, V _{GS} = 0 V, T _J = 55 °C			1 10	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 V, V_{GS} = 10 V$	125			А
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	120	0.0033	0.0042	Ω
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$		70		S
Dynamic ^b	315				I	
Input Capacitance	C _{iss}			3115		
Output Capacitance	C _{oss}	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz		1350		pF
Reverse Transfer Capacitance	C _{rss}			98		
	Qg	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 10 A		51		
Total Gate Charge				22		nC
Gate-Source Charge	Q _{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		8.2		
Gate-Drain Charge	Q _{gd}			4.5		
Gate Resistance	R _q	f = 1 MHz		2.2		Ω
Turn-On Delay Time	t _{d(on)}			17		
Rise Time	t _r	$V_{DD} = 30 V, R_1 = 3 \Omega$		8		-
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ Å}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		30		
Fall Time	t _f			8		
Turn-On Delay Time	t _{d(on)}			43		ns
Rise Time	t _r	V_{DD} = 30 V, R_L = 3 Ω		120		-
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ Å}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		32		
Fall Time	t _f			9		
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	ا _S	T _C = 25 °C			95	^
Pulse Diode Forward Current ^a	I _{SM}				360	A
Body Diode Voltage	V _{SD}	I _S = 5 A		0.74	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			40	86	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 10 A, dl/dt = 100 A/μs, T _J = 25 °C		33	74	nC
Reverse Recovery Fall Time	t _a			17		nc
Reverse Recovery Rise Time	t _b			26		ns

Notes:

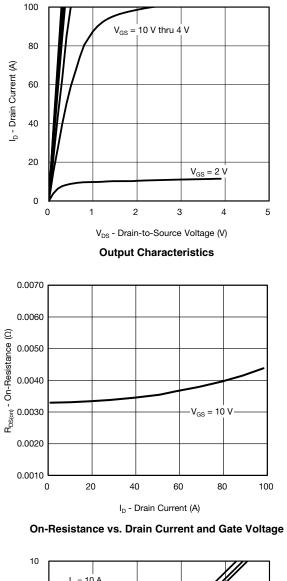
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

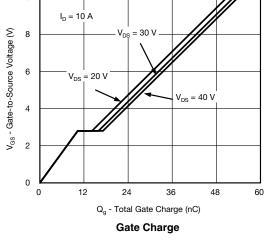
b. Guaranteed by design, not subject to production testing.

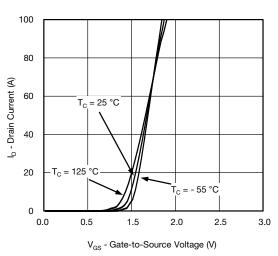
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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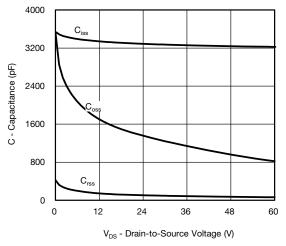
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



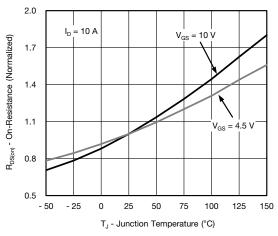




Transfer Characteristics



Capacitance

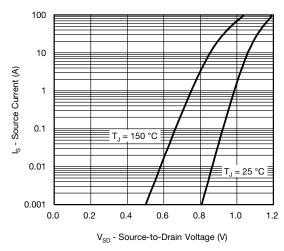


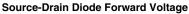
On-Resistance vs. Junction Temperature

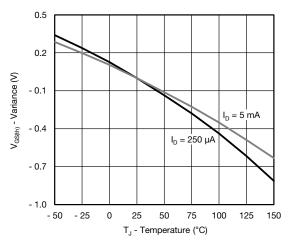


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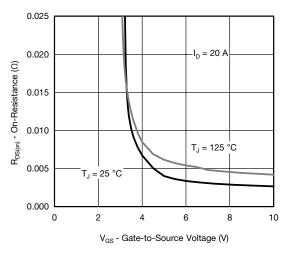
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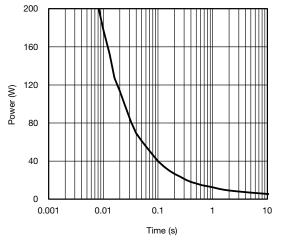




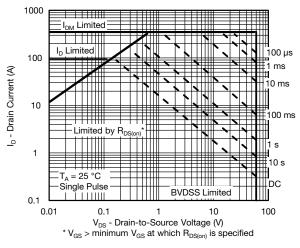
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

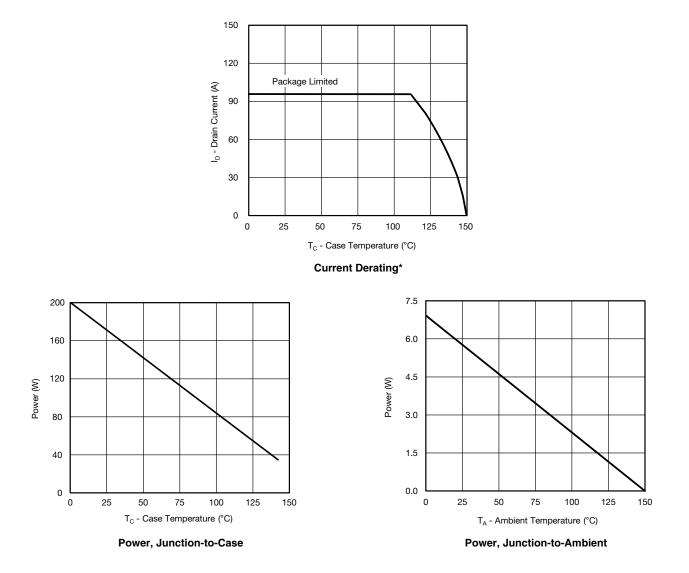


Single Pulse Power, Junction-to-Ambient



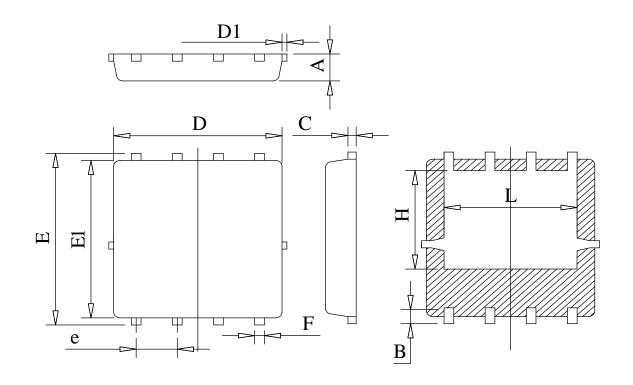
Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm			
Symbol	Min	Тур	Max
A	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25



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