Dual Asymmetric N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)[a, d]	Q_g (Typ.)
30	3.6 at V_{GS} = 10 V	50	25 nC
	5.5 at V_{GS} = 4.5 V		

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

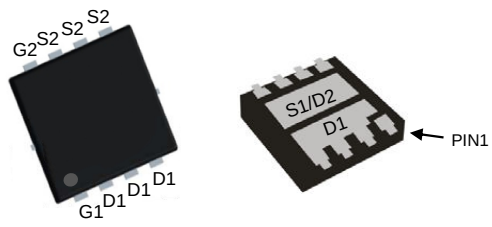
RoHS COMPLIANT

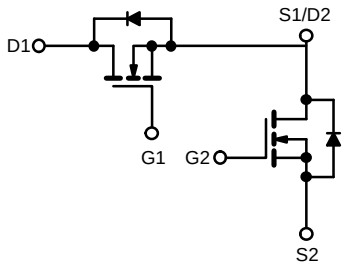
APPLICATIONS

- DC/DC Converters
- On board power for server

DFN5X6 Asymmetric Dual Pin Configuration

Top View **Bottom View**





Dual N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 175 °C)	T_C = 25 °C	I_D	50[a,d]	A
	T_C = 70 °C		36[d]	
	T_A = 25 °C		19[b, c]	
	T_A = 70 °C		15[b, c]	
Pulsed Drain Current		I_{DM}	200	
Avalanche Current Pulse	L = 0.1 mH	I_{AS}	41	
Single Pulse Avalanche Energy		E_{AS}	80.2	mJ
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	50[a, d]	A
	T_A = 25 °C		18.6[b, c]	
Maximum Power Dissipation	T_C = 25 °C	P_D	39[a]	W
	T_C = 70 °C		15	
	T_A = 25 °C		4.9[b, c]	
	T_A = 70 °C		2.8[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[b]	t ≤ 10 s	R_{thJA}	25	30	°C/W
Maximum Junction-to-Case	Steady State	R_{thJC}	2.5	3.6	

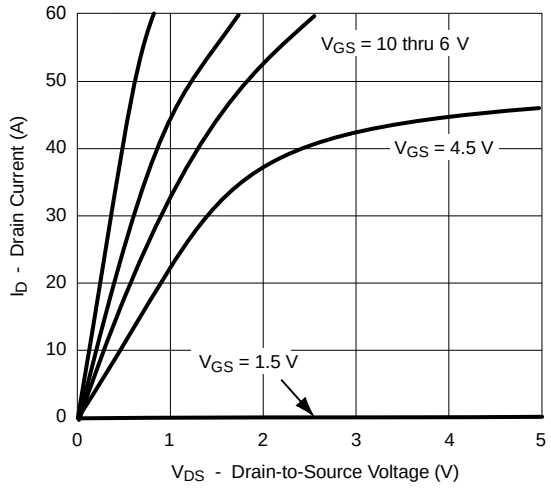
Notes:
a. Based on T_C = 25 °C.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 10 s.
d. Calculatedbased on maximum junction temperature.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min .**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		32		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1		3	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 40 V, V_{GS} = 0 V			1	µA
		V_{DS} = 40 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	$V_{DS} \geq$ 5 V, V_{GS} = 10 V	50			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 20 A		3.6	5	mΩ
		V_{GS} = 4.5 V, I_D = 15 A		5.5	7	
Forward Transconductance[a]	g_{fs}	V_{DS} = 5 V, I_D = 20 A		83		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS}= 15 V, V_{GS} = 0 V, f = 1 MHz		615		pF
Output Capacitance	C_{oss}			203		
Reverse Transfer Capacitance	C_{rss}			21		
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 10 V, I_D = 20 A		25		nC
Gate-Source Charge	Q_{gs}			6		
Gate-Drain Charge	Q_{gd}			10		
Gate Resistance	R_g	f = 1 MHz		1		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 0.555 I_D = 20 A, V_{GEN} = 10 V, R_g = 1		5		ns
Rise Time	t_r			21		
Turn-Off Delay Time	$t_{d(off)}$			40		
Fall Time	t_f			10		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			50	A
Pulse Diode Forward Current[a]	I_{SM}				200	
Body Diode Voltage	V_{SD}	I_S = 1 A		0.6	1	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 20 A, di/dt = 100 A/µs, T_J = 25 °C		18		ns
Body Diode Reverse Recovery Charge	Q_{rr}			10		nC
Reverse Recovery Fall Time	t_a			27		ns
Reverse Recovery Rise Time	t_b			25		

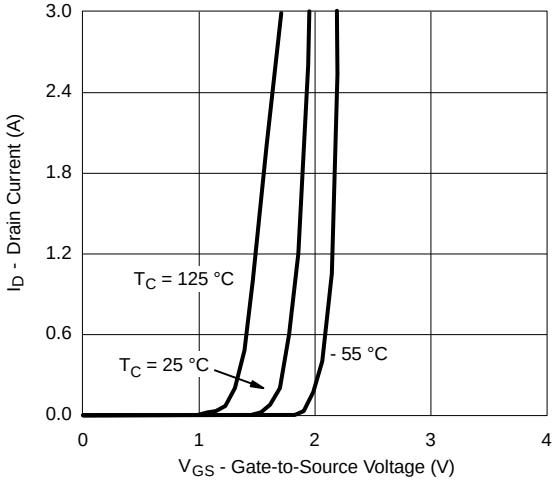
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

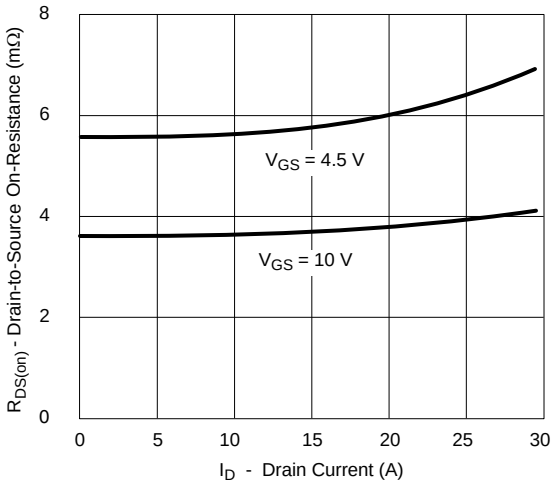
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



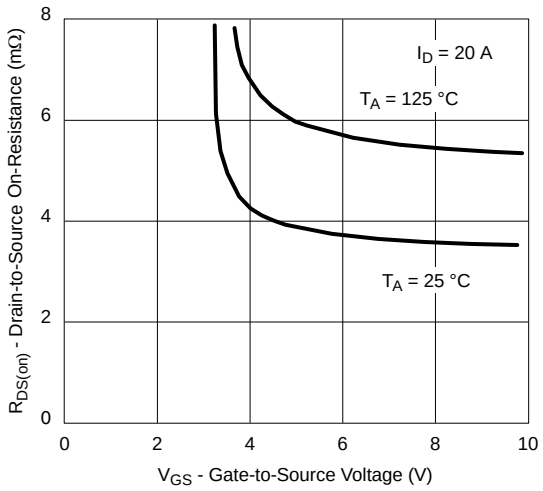
Output Characteristics



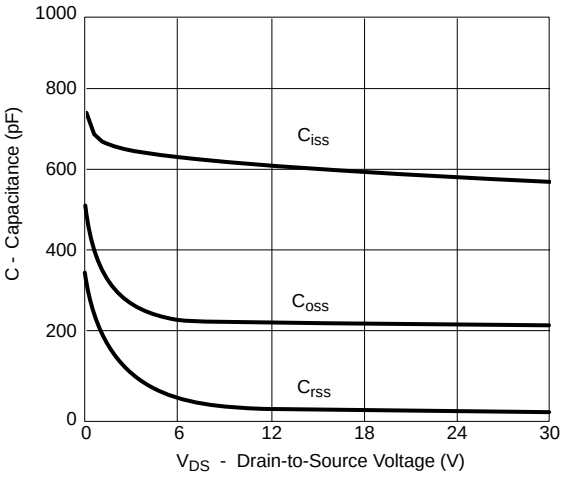
Transfer Characteristics



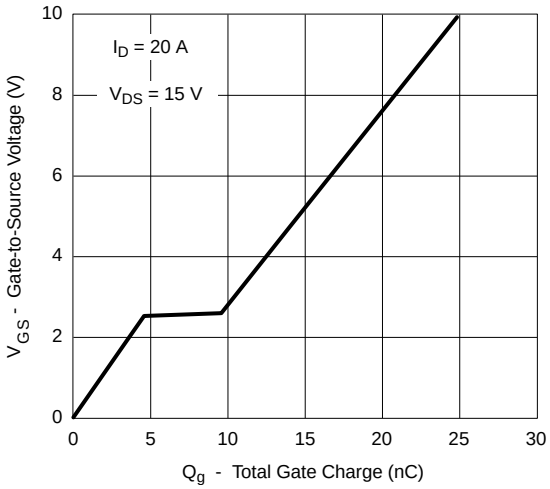
$R_{DS(on)}$ vs. Drain Current



$R_{DS(on)}$ vs. V_{GS} vs. Temperature

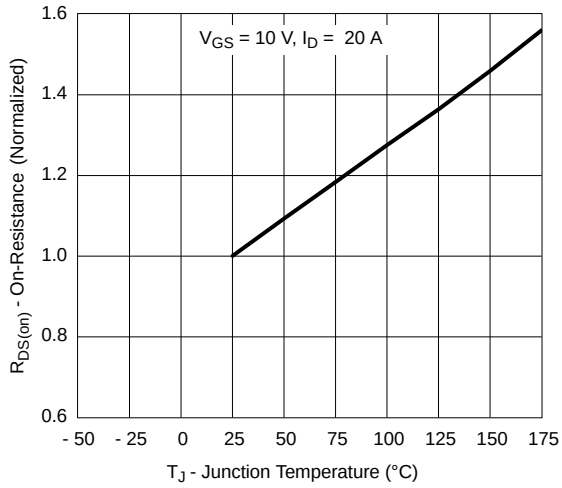


Capacitance

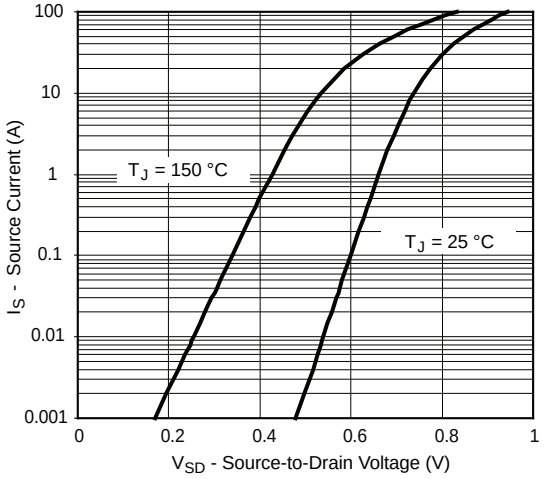


Gate Charge

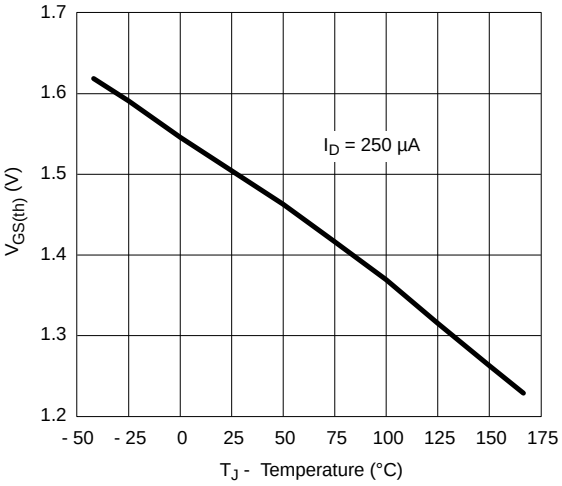
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



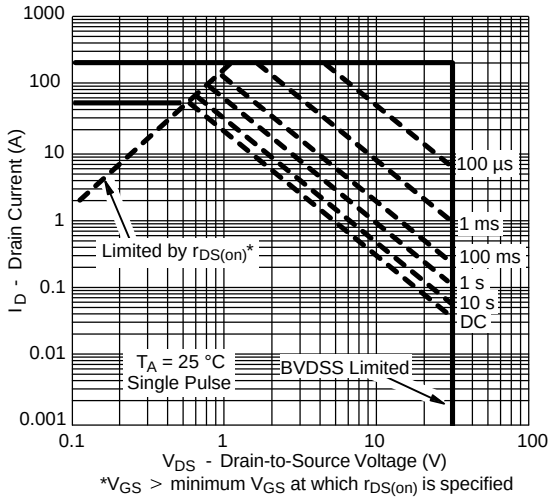
On-Resistance vs. Junction Temperature



Forward Diode Voltage vs. Temperature

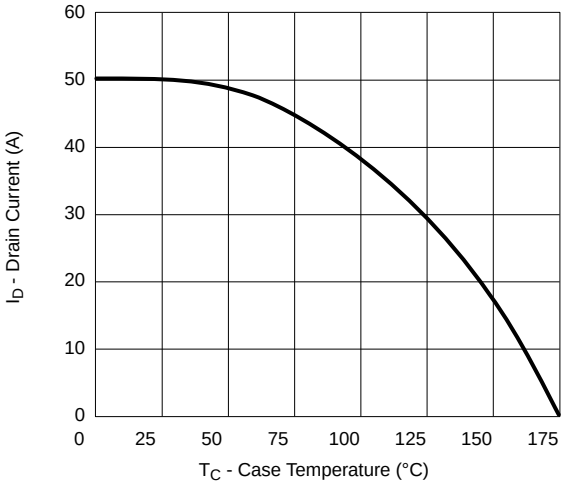


Threshold Voltage

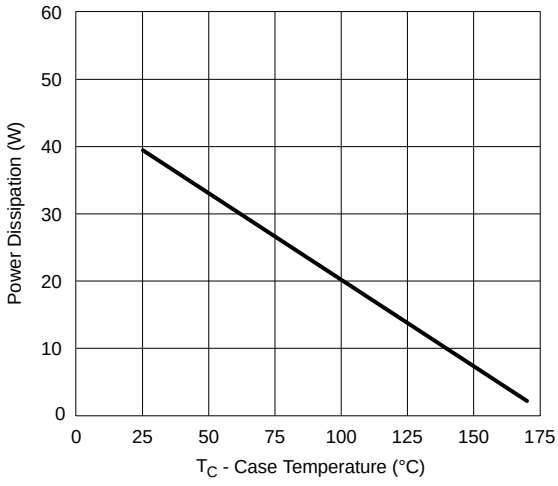


Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

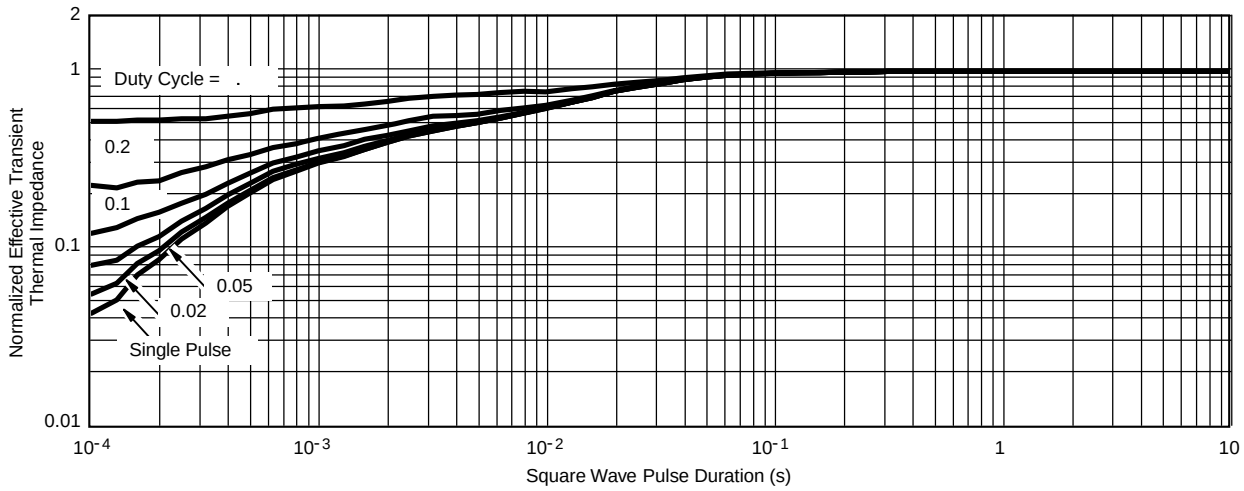


Current Derating*



Power Derating

* The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Case

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