



Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)		
40	10 at V _{GS} = 10 V	42	45 nC		
40	15 at V _{GS} = 4.5 V	42			

FEATURES

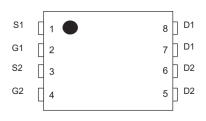
- DT-Trench Power MOSFET
- 100 % $\rm R_{\rm g}$ and UIS Tested



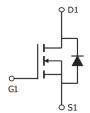
APPLICATIONS

- 12 V Automotive systems
- · Motors, lamps and solenoid control
- · Transmission control
- · Ultra high performance power switching

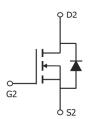
DFN5X6







N-Channel MOSFET



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V _{DS}	40	V		
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 25 °C		42		
Continuous Drain Current (1, = 130 °C)	T _C = 100 °C	- I _D	28	A	
Pulsed Drain Current	I _{DM}	168			
Single Avalanche Energy ^a	E _{AS}	85	mJ		
Maximum Power Dissipation	T _C = 25 °C	P _D	70 b	w	
waxiinum Fowei Dissipation	T _C = 100 °C	T PD	28 b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	LIMIT	UNIT			
Junction-to-Ambient (PCB Mount) ^{c,d}	t ≤ 10 s	R _{thJA}	45	°C/W		
Junction-to-Case (Drain)	Steady State	R _{thJC}	1.78	- *C/VV		

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under steady state conditions is 45 $^{\circ}\text{C/W}.$



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA	40			V
Gate-Source Threshold Voltage	V _G S(th)	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1		3	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zoro Coto Voltago Proin Current		V _{DS} = 40 V, V _{GS} = 0 V			1	
Zero Gate Voltage Drain Current	l _{DSS}	V _{DS} = 32 V, V _{GS} = 0 V, T _J = 55 °C	1		10	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	42			Α
Davis Occurs On Otata Basistana 3	D	V _{GS} = 10 V, I _D = 10 A		10	12	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 10 A		15	19.5	mΩ
Forward Transconductance ^a	9 _{fs}	V _{DS} = 5 V,I _D = 10 A		50		S
Dynamic ^b			,			
Input Capacitance	C _{iss}			2240		pF
Output Capacitance	C _{oss}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		175		
Reverse Transfer Capacitance	C _{rss}			160		
Total Gate Charge	Q_g			45		
Gate-Source Charge	Q _{gs}	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 10 A		4.5		nC
Gate-Drain Charge	Q _{gd}	1		11		
Gate Resistance	R _q f = 1 MHz			2.5		Ω
Turn-On Delay Time	t _{d(on)}			10		
Rise Time	t _r	$V_{DD} = 20 \text{ V, R}_{L} = 0.75 \Omega$		20		1
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 3 \Omega$		27		ns
Fall Time	t _f	7		15		
Drain-Source Body Diode Characterist	tics					
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			42	A
Pulse Diode Forward Current (100 μs)	I _{SM}				168	1 ^
Body Diode Voltage	V_{SD}	I _S = 1 A			1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 10 A, dl/dt = 100 A/µs, T _I = 25 °C		20		ns
Body Diode Reverse Recovery Charge	Q _{rr}	2 _{rr} 15 - 15 / 1, divat = 155 / 1 ps, 1 j = 25 · 5		35		nC

Notes:

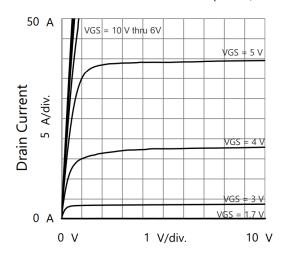
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

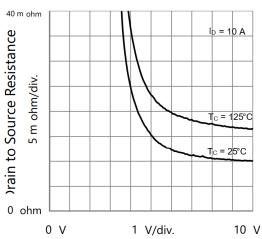


TYPICAL CHARACTERISTICS (25°C, unless otherwise noted)

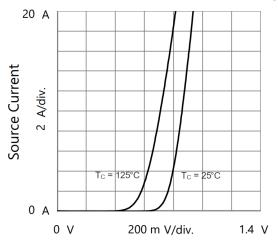
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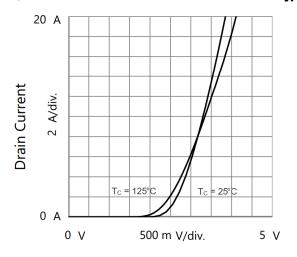
Drain to Source Voltage Output Characteristics



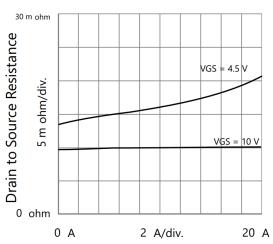
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



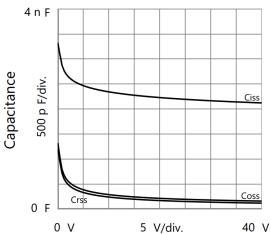
Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



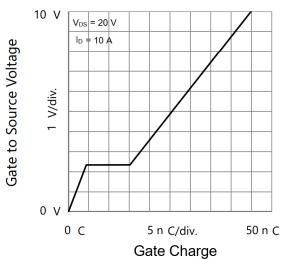
Drain Current
Drain to Source Resistance vs. Drain Current



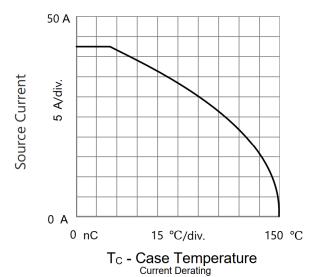
Drain to Source Voltage Capacitances



TYPICAL CHARACTERISTICS (25°C, unless otherwise noted)



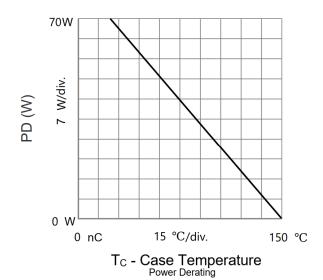
Gate to Source Voltage vs. GateCharge



1000 A

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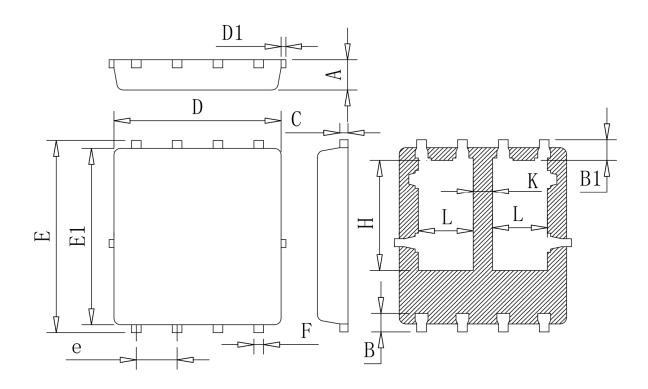
Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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DFN5X6-8L-D PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
A	0.85	0.95	1.05
В	0.46	0.58	0.73
B1	0.52	0.65	0.78
С	0.18	0.254	0.32
D	4.70	5.20	5.50
D1	-	-	0.18
Е	5.75	6.05	6.35
E1	5.35	5.65	5.85
e	1.15	1.27	1.50
F	0.15	0.30	0.50
Н	3.15	3.47	3.80
L	1.35	1.70	2.10
K	0.35	0.60	1.00





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