

# Dual N-Channel 40 V (D-S) Super Junction MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ) (Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
40	7 at V <sub>GS</sub> = 10 V	40	15 nC			
40	11 at V <sub>GS</sub> = 4.5 V	40	13110			

#### **FEATURES**

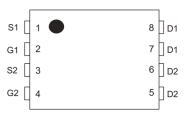
- DT-SJ Power MOSFET
- Very low on-resistance
- · Low input capacitance (Ciss)
- Low Gate Charge
- 100 % Rg and UIS Tested



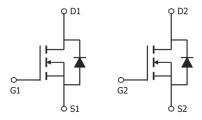
#### **APPLICATIONS**

- · Power Management
- Motor Drivers
- DC-DC Converters

### DFN5X6 Pin Configuration



**Top View** 



N1-Channel MOSFET

N2-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V <sub>DS</sub>	40	V			
Gate-Source Voltage	V <sub>GS</sub>	± 20	V			
Out in the Company (T. 150.00)	T <sub>C</sub> = 25 °C		40			
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 100 °C	I <sub>D</sub>	30	. A		
Pulsed Drain Current	I <sub>DM</sub>	160				
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	55	mJ		
Maximum Dowar Dissipation 8	T <sub>C</sub> = 25 °C	В	28 b	W		
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 100 °C	$ P_D$	11.2 b			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	LIMIT	UNIT			
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	46	°C/W			
Junction-to-Case (Drain)	R <sub>thJC</sub>	4.5	- C/VV			

#### Notes

- a. Duty cycle ≤ 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	,				•	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		-	-	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1	-	3	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V	-	-	1	μΑ
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	40	-	-	Α
Drain-Source On-State Resistance a	D	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	-	7	11	mΩ
Diani-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	-	11	16	
Forward Transconductance a	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A	-	38	-	S
Dynamic <sup>b</sup>					_	
Input Capacitance	C <sub>iss</sub>		-	649	-	pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 20 \text{ V}, f = 1 \text{MHz}$	-	118	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	114	-	
Total Gate Charge <sup>c</sup>	Qg		-	15	-	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	-	1.3	-	nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	5.1	-	
Gate Resistance	Rg	f = 1 MHz	-	2.5	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	10	-	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 20 \text{ V}, R_q = 6 \Omega$	-	15	-	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D = 10 \text{ A}, V_{GEN} = 10 \text{ V},$	-	22	-	- ns -
Fall Time <sup>c</sup>	t <sub>f</sub>		-	5	-	
Drain-Source Body Diode Ratings ar	d Characteris	tics <sup>b</sup> (T <sub>C</sub> = 25 °C)				
Continuous Source Current	Is	T <sub>C</sub> = 25 °C	-	-	40	А
Pulsed Source Current	I <sub>SM</sub>		-	-	160	Α
Forward Voltage <sup>a</sup> V <sub>SD</sub>		I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0 V	-	-	1	V
Reverse Recovery Time	t <sub>rr</sub>	1 10 1 17/11 100 17	-	12	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 10 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s}$	-	10	-	nC

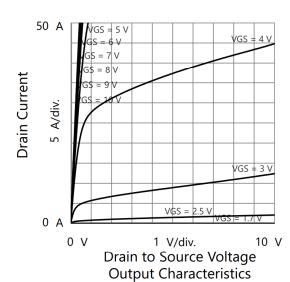
#### **Notes**

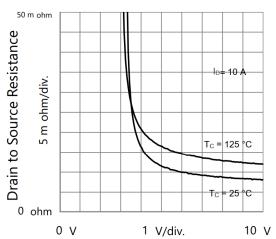
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

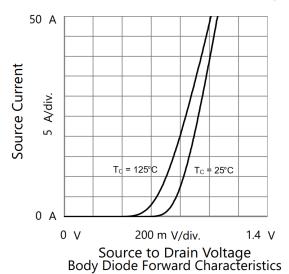


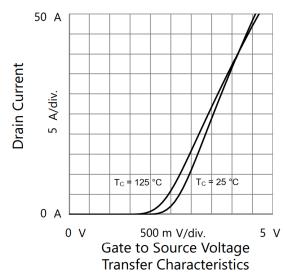
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

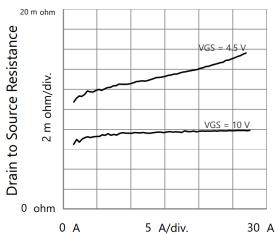




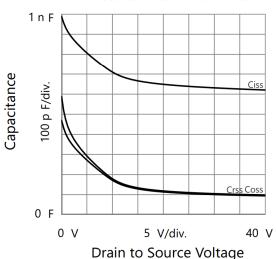
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage





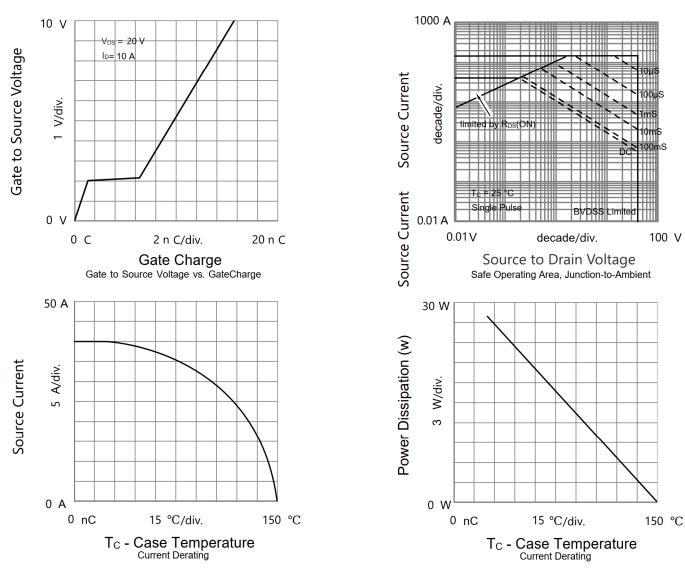


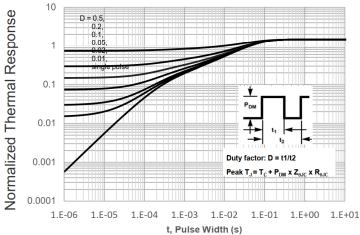
Drain Current
Drain to Source Resistance vs. Drain Current



Capacitances

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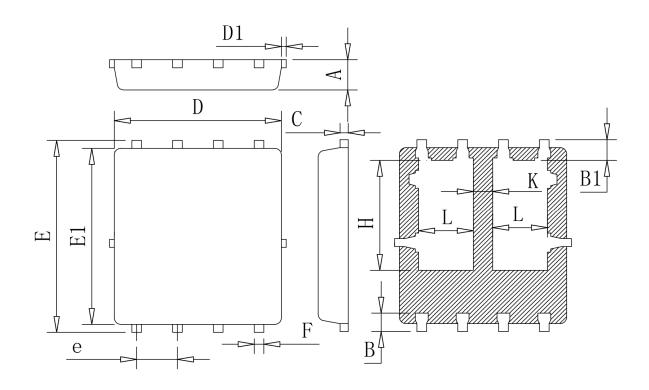




Normalized Thermal Transient Impedance



# **DFN5X6-8L-D PACKAGE OUTLINE**



## **COMMON DIMENSIONS** (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
A	0.85	0.95	1.05
В	0.46	0.58	0.73
B1	0.52	0.65	0.78
C	0.18	0.254	0.32
D	4.70	5.20	5.50
D1	-	-	0.18
Е	5.75	6.05	6.35
E1	5.35	5.65	5.85
e	1.15	1.27	1.50
F	0.15	0.30	0.50
Н	3.15	3.47	3.80
L	1.35	1.70	2.10
K	0.35	0.60	1.00





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