

## N-Channel 30 V (D-S) MOSFET

### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
30	7 at V <sub>GS</sub> = 10 V	42	16.7 nC
	10 at V <sub>GS</sub> = 4.5 V		

### FEATURES

- DT-Trench Power MOSFET
- Improved dv/dt Capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

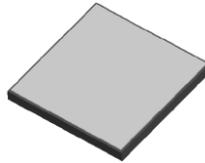
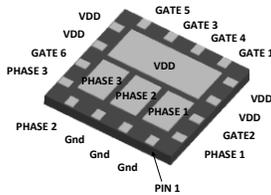


**RoHS**  
COMPLIANT

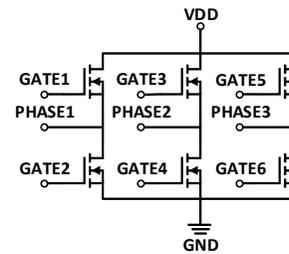
### APPLICATIONS

- 3-PHASE Applications

### DFN6×6 6 IN 1 Pin Configuration



Cu Exposed Pad



Schematic Diagram

### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	I <sub>D</sub>	T <sub>A</sub> = 25 °C	42
		T <sub>A</sub> = 100 °C	26.6
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	168	A
Single Avalanche Energy	E <sub>AS</sub>	45	mJ
Maximum Power Dissipation <sup>c</sup>	P <sub>D</sub>	T <sub>A</sub> = 25 °C	24.7
		T <sub>A</sub> = 100 °C	9.9
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Case (Drain)	R <sub>thJA</sub>	62	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	5.06	

### Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
- The value of R<sub>thJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.

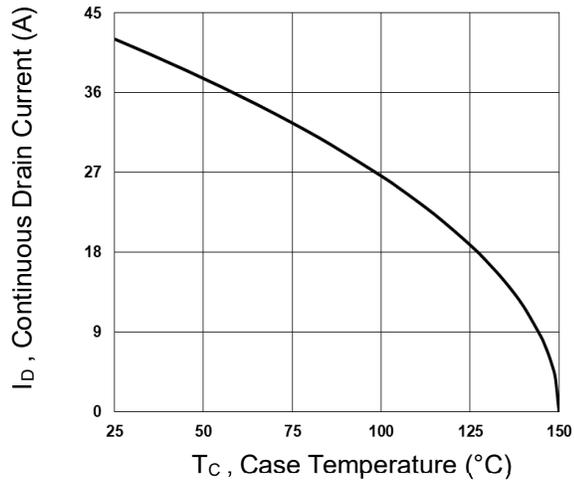
SPECIFICATIONS ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.2	-	2.5	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	42	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 16\text{ A}$	-	7	8.5	m $\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$	-	10	13	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 10\text{ V}, I_D = 8\text{ A}$	-	9.5	-	S
<b>Dynamic <sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	850	-	pF
Output Capacitance	$C_{oss}$		-	133	-	
Reverse Transfer Capacitance	$C_{rss}$		-	78	-	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	16.7	-	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$		-	4.5	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	1.3	-	
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	-	2.7	-	$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 15\text{ V}, I_D = 15\text{ A}, R_g = 3.3\text{ }\Omega$ $V_{GS} = 10\text{ V}$	-	4.8	-	ns
Rise Time <sup>c</sup>	$t_r$		-	12.5	-	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$		-	27.6	-	
Fall Time <sup>c</sup>	$t_f$		-	8.2	-	
<b>Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> (<math>T_A = 25\text{ }^\circ\text{C}</math>)</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_A = 25\text{ }^\circ\text{C}$	-	-	42	A
Pulsed Current	$I_{SM}$		-	-	84	A
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = 1\text{ A}, V_{GS} = 0\text{ V}$	-	-	1	V
Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	8.1	-	ns
Reverse Recovery Charge	$Q_{rr}$		-	1.6	-	nC

**Notes**

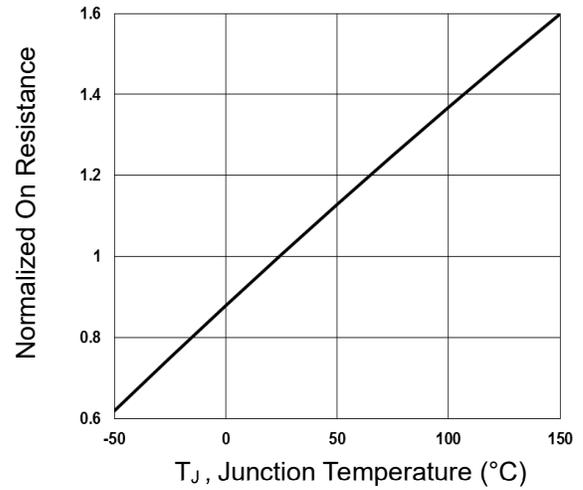
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

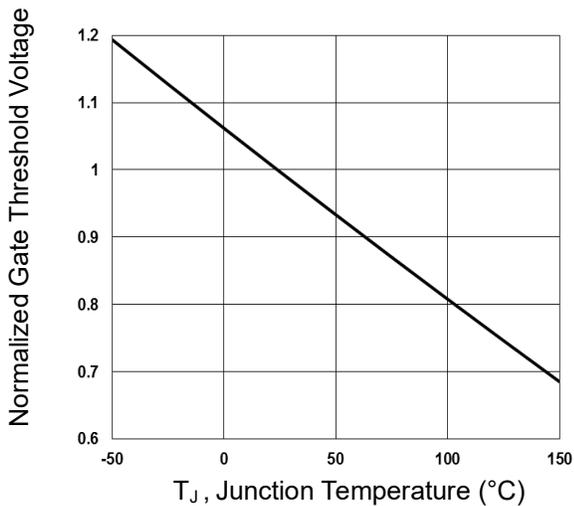
TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)



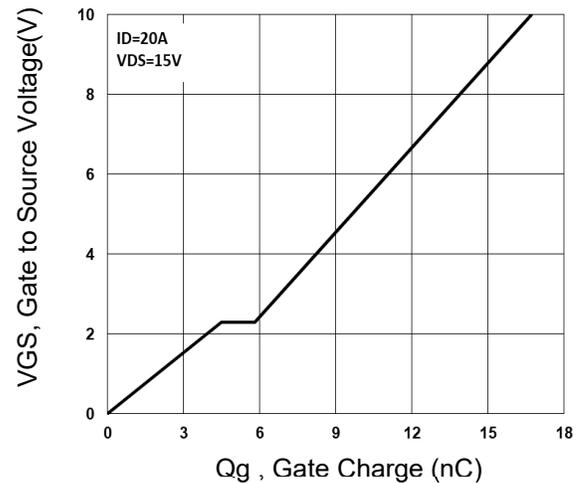
**Fig.1** Continuous Drain Current vs.  $T_c$



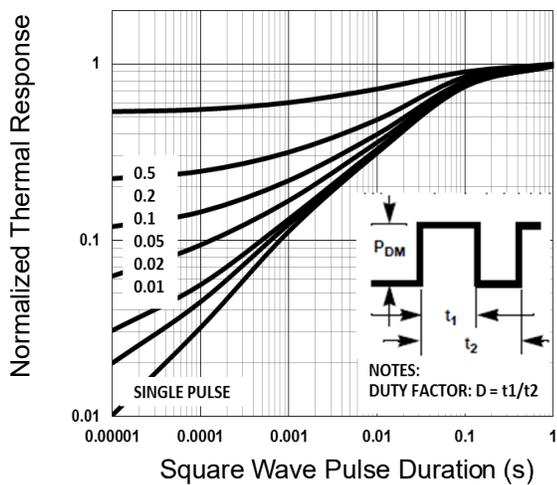
**Fig.2** Normalized  $R_{DS(on)}$  vs.  $T_j$



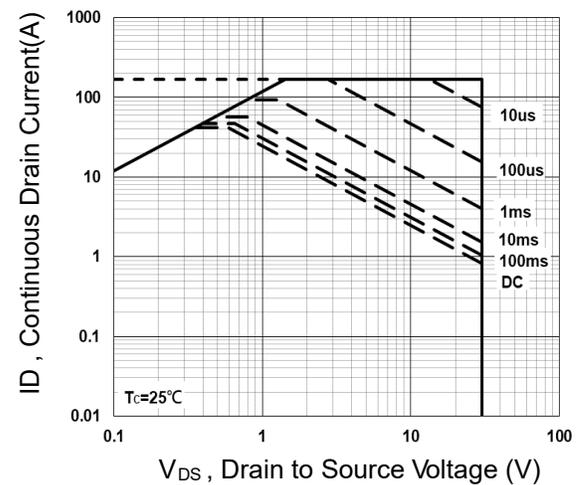
**Fig.3** Normalized  $V_{th}$  vs.  $T_j$



**Fig.4** Gate Charge Waveform



**Fig.5** Normalized Transient Impedance



**Fig.6** Maximum Safe Operation Area

TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

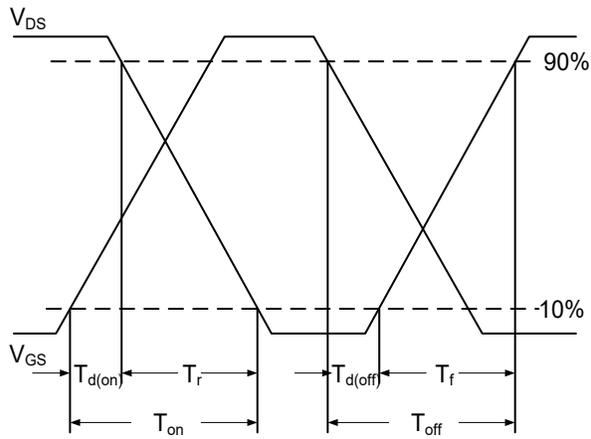


Fig. 7 Switching Time Waveform

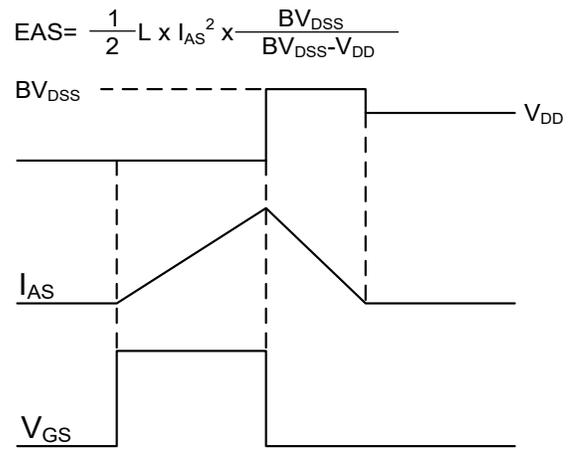
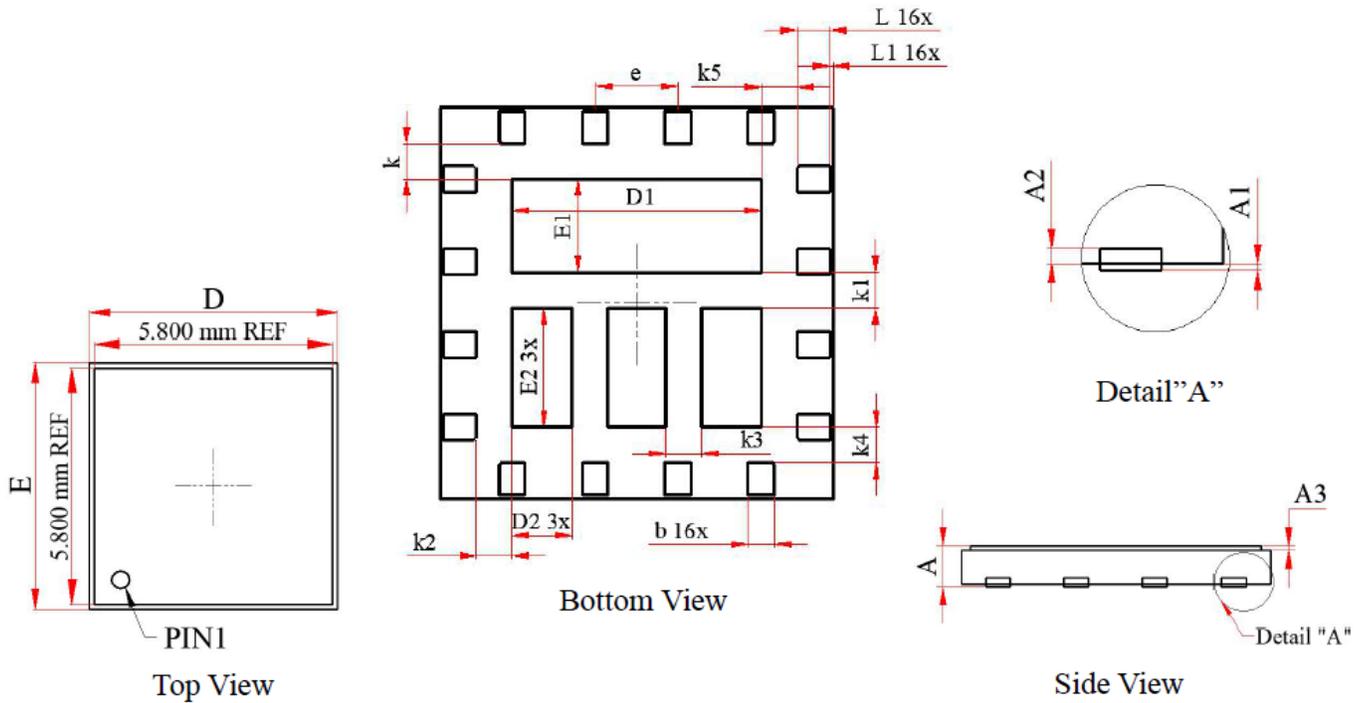


Fig. 8 EAS Waveform

## DFN6X6 6 IN 1 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters			Symbol	Dimensions In Millimeters		
	MIN	Normal	MAX		MIN	Normal	MAX
A	0.530	---	0.600	b	0.350	0.400	0.450
A1	---	---	0.005	L	0.450	0.500	0.550
A2	0.030	---	0.100	L1	0.010	0.050	0.090
A3	0.050	---	0.100	k	0.550 REF		
D	5.900	6.000	6.100	k1	0.550 REF		
E	5.900	6.000	6.100	k2	0.550 REF		
D1	3.700	3.800	3.900	k3	0.550 REF		
E1	1.325	1.425	1.525	k4	0.550 REF		
D2	0.800	0.900	1.000	k5	0.550 REF		
E2	1.725	1.825	1.925	e	1.27 BSC		



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