# P-Channel 100 V (D-S) MOSFET

Q<sub>g</sub> (Typ.)

14 nC

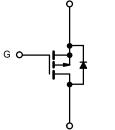
I<sub>D</sub> (A)<sup>a</sup>

- 1

- DT-Trench Power MOSFET
- 100 % R<sub>q</sub> and UIS Tested
- · Low Gate Charge

#### **APPLICATIONS**

- · Power management
- Video Montior



D P-Channel MOSFET

S

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $I_c = 25  {}^{\circ}C$ , unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	- 100	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v	
Continuous Drain Current (T $= 150^{\circ}$ C)a	T <sub>C</sub> = 25 °C		- 1		
Continuous Drain Current (T <sub>J</sub> = 150° C) <sup>a</sup>	T <sub>C</sub> = 100 °C	I <sub>D</sub>	- 0.6	А	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	- 4		
Single Avalanche Energy		E <sub>AS</sub>	1	mJ	
Maximum Power Dissipation <sup>c</sup>	T <sub>C</sub> = 25 °C	- P <sub>D</sub>	0.62	W	
	T <sub>C</sub> = 100 °C	ГD	0.25		
Operating Junction and Storage Temperature Rai	nge	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) <sup>d</sup>	R <sub>thJA</sub>	300	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	201	°C/W	

#### Notes

A

a. Calculated continuous current based on maximum allowablejunction temperature.

b. Repetitive rating; pulse width limited by max. junction temperature.

c. Pd is based on max. junction temperature, using junction-case thermal resistance.

d. The value of R<sub>0JA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.



V<sub>DS</sub> (V)

- 100

**PRODUCT SUMMARY** 



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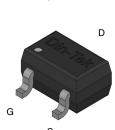
#### **SOT-23 Pin Configuration**

**Top View** 

R<sub>DS(on)</sub> (mΩ)(Typ.)

620 at V\_{GS}\, = - 10 V

660 at V<sub>GS</sub> = - 4.5 V





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<b>SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ , unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. 1		TYP.	MAX.	UNIT	
Static		·					
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$	- 100	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 1	-	- 3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V},  V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zero Gate Voltage Drain Current	1	V <sub>DS</sub> = - 80 V, V <sub>GS</sub> = 0 V		- 1	μА		
Zelo Gale Voltage Drain Guirent	I <sub>DSS</sub>	$V_{DS}$ = - 80V, $V_{GS}$ = 0 V, $T_J$ = 55 $^\circ C$	-	-	- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}\!\geq$ - 5 V, $V_{GS}$ = - 10 V	- 1	-	-	А	
Drain Source On State Resistance a	Provide	$V_{GS}$ = - 10 V, I <sub>D</sub> = - 500 mA	-	620	745	mΩ	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = - 4.5 V, I <sub>D</sub> = - 500 mA	-	660	825		
Forward Transconductance <sup>a</sup>	<b>g</b> fs	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 20 A	-	2	-	S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		-	592	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = - 25 V, f = 1 MHz	-	19	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	13	-		
Total Gate Charge <sup>c</sup>	Qg		-	14	-		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = -50 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -0.5 \text{ A}$	-	3	-	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	2	-		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	11	-	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	7.8	-		
Rise Time <sup>c</sup>	tr	$V_{DD} = -50 \text{ V}, \ \text{R}_{g} = 3 \Omega,$	-	15	-	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	I <sub>D</sub> = - 0.5 A , V <sub>G</sub> s = - 10 V	-	20	-		
Fall Time <sup>c</sup>	t <sub>f</sub>	]	-	17	-		
Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> ( $T_C = 25 \text{ °C}$ )							
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	- 1	А	
Pulsed Current	I <sub>SM</sub>		-	-	- 4	А	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = -0.5 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$	-	-	-1.2	V	

#### Notes

a. Pulse test; pulse width  $\leq 200 \ \mu$ s, duty cycle  $\leq 2 \ \%$ .

b. Guaranteed by design, not subject to production testing.

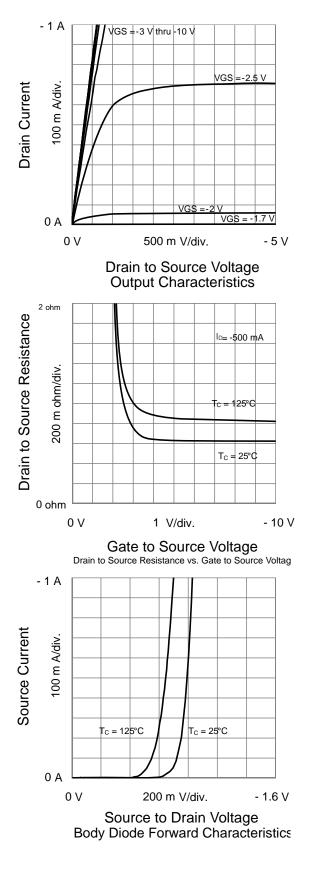
c. Independent of operating temperature.

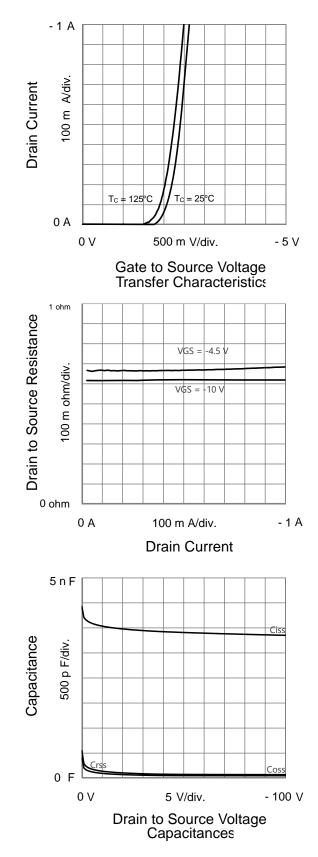
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended pe riods may affect device reliability.



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### TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

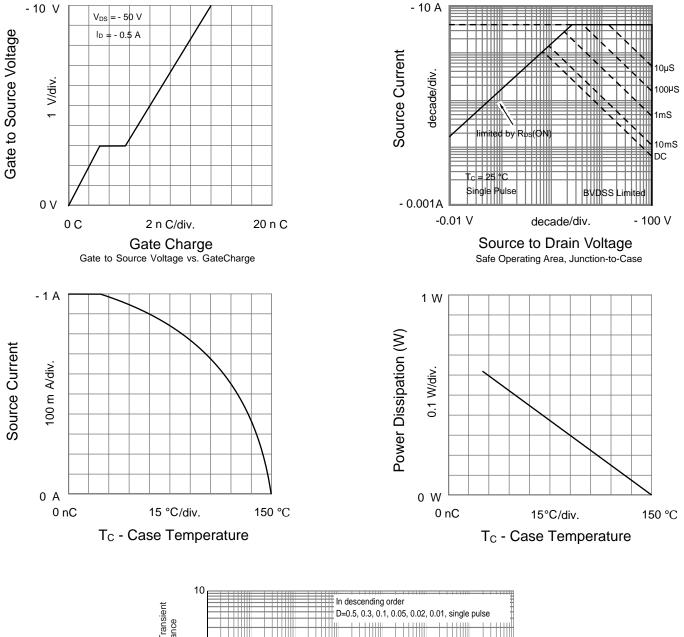


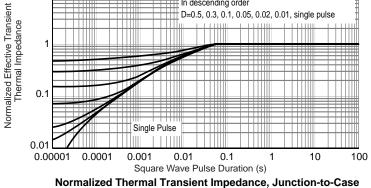




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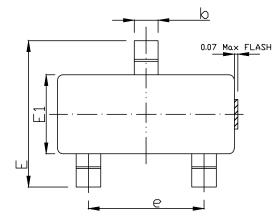


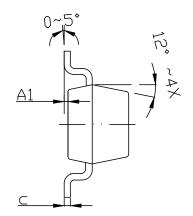


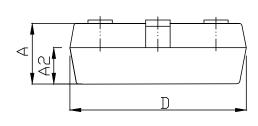


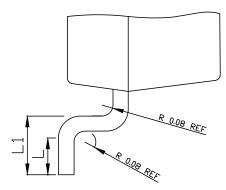
# Package Information www.din-tek.jp

## SOT-23 PACKAGE OUTLINE









### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER			
	MIN	NOM	MAX	
A	0.80	1.00	1.30	
A1	0.00	0.05	0.15	
b	0.25	0.40	0.55	
С	0.11 BSC			
D	2.60	2.90	3.20	
E	2.10	2.40	2.70	
E1	1.10	1.30	1.48	
е	1.90 BSC			
L	0.17	-	-	
L1	0.28	0.40	0.53	
A2	0.60 REF			



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