

PRODUCT SUMMARY							
V _{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I _D (A)	Q _g (Typ.)				
400	180 at V _{GS} = - 10 V	- 3	7.0				

-2.8

FEATURES

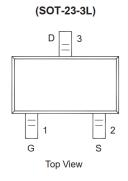
- DT-Trench Power MOSFET
- Ultra Low On-Resistance
- 100 % R_g Tested 100 % UIS Tested



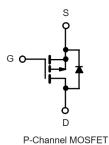
COMPLIANT

APPLICATIONS

· Active Clamp Circuits in DC/DC Power Supplies



200 at V_{GS} = - 4.5 V



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 100		V
Gate-Source Voltage		V _{GS}	± 20		
0	T _A = 25 °C	I _D	- 3.8	- 3	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		- 2.5	- 2.8	
Pulsed Drain Current		I _{DM}	- 10.6		Α
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 3.8	- 3	
Single Pulse Avalanche Current	L = 1.0 mH	I _{AS}	4.5		
Single Pulse Avalanche Energy	L = 1.0 IIII	E _{AS}	1.01		mJ
M · D D· · · · ah	T _A = 25 °C	P _D	1.25 0.75		W
Maximum Power Dissipation ^{a, b}	T _A = 70 °C] 'D	0.8	0.48	VV
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrianna lunation to Ambient	t ≤ 5 s	R _{thJA}	75	100	
Maximum Junction-to-Ambient ^a	Steady State	' thJA	120	166	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	50	

- a. Surface Mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.



SPECIFICATIONS T _J = 25 °C	C, unless c	otherwise noted					
-			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 100			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current		V _{DS} = - 100 V, V _{GS} = 0 V	-1		- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -15 \text{ V}, V_{GS} = 10 \text{ V}$	- 3			Α	
D : 0	В	$V_{GS} = -10 \text{ V}, I_D = -0.5 \text{ A}$		180		0	
Drain-Source On-Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -0.5 \text{ A}$	200			mΩ	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 0.5 A		2.2		S	
Diode Forward Voltage	V _{SD}	I _S = - 1.0 A, V _{GS} = 0 V		0.7	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg	V - 50V V - 10 V		7.7			
Gate-Source Charge	Q _{gs}	$V_{DS} = -50V$, $V_{GS} = 10 V$, $I_{D} \cong -0.5 A$		1.5		nC	
Gate-Drain Charge	Q_{gd}	1D = - 0.0 A		2.5		1	
Gate Resistance	R_g	f = 1.0 MHz		14		Ω	
Input Capacitance	C _{iss}			1270			
Output Capacitance	C _{oss}	$V_{DS} = -50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		48		pF	
Reverse Transfer Capacitance	C _{rss}			34			
Switching ^c							
Turn-On Time	t _{d(on)}	$V_{DD} = -50 \text{ V, } R_L = 75 \Omega$ $I_D \cong -1.0 \text{ A, } V_{GEN} = -10 \text{ V}$		7		ns	
Turn-On Time	t _r			11			
Turn-Off Time	t _{d(off)}	$R_{a} = 6 \Omega$		16		1115	
Turri-Oil Tiffle	t _f	y		11			
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 0.5 A, dl/dt = 100 A/μs		90		nC	

Notes:

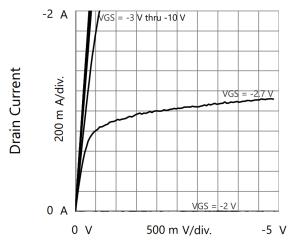
- a. Pulse test: PW \leq 300 μ s duty cycle \leq 2 %. b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

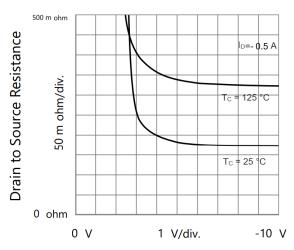




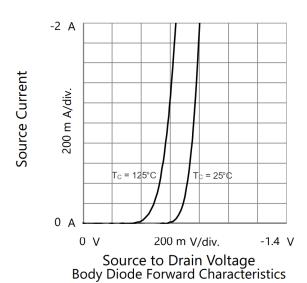
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

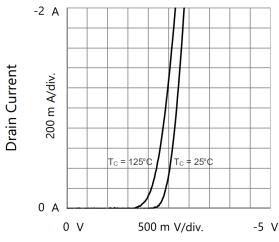


Drain to Source Voltage Output Characteristics

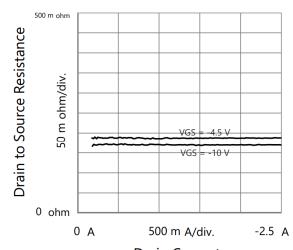


Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage

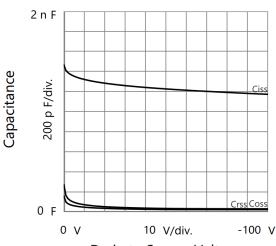




Gate to Source Voltage Transfer Characteristics



Drain Current
Drain to Source Resistance vs. Drain Current

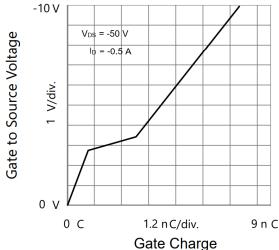


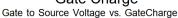
Drain to Source Voltage Capacitances

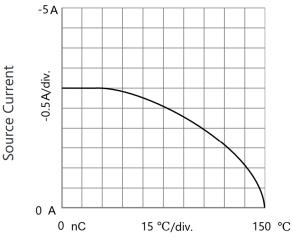


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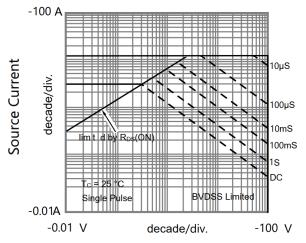




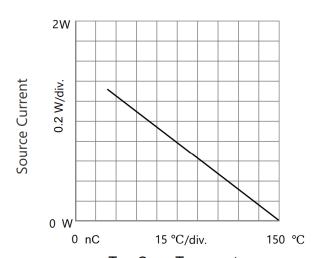


T_C - Case Temperature

Current Derating

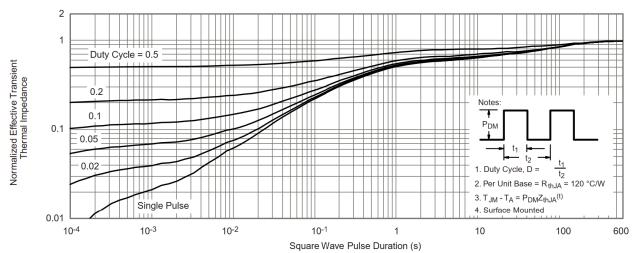


Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



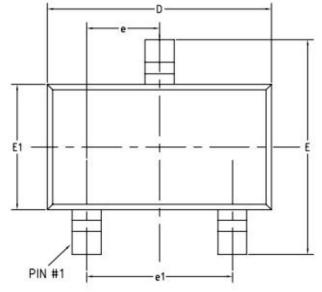
T_C - Case Temperature

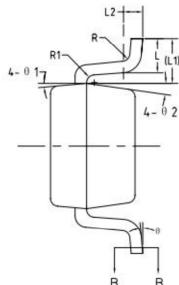
Current Derating

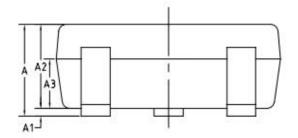


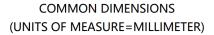
Normalized Thermal Transient Impedance, Junction-to-Ambient

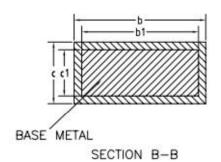
SOT-23-3L PACKAGE OUTLINE











SYMBOL	MIN	TYP	MAX	
Α	-	-	1.50	
A1	0.00	-	0.18	
A2	0.85	1.10	1.35	
A3	0.58	0.65	0.72	
b	0.23	1	0.53	
b1	0.20	0.40	0.50	
С	0.09	-	0.22	
c1	0.08	0.13	0.21	
D	2.78	2.95	3.10	
E	2.58	2.80	3.03	
E1	1.55	1.65	1.78	
е	0.83	0.95	1.07	
e1	1.78	1.90	2.02	
L	0.28	0.45	0.62	
L1	0.59REF			
L2	0.25BSC			
R	0.04	-	1	
R1	0.04	-	0.21	
θ	0°	-	8°	
θ1	8°	10°	12°	
θ2	8°	10°	12°	



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