N-Channel 100 V (D-S) MOSFET

RoHS COMPLIANT

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)[a]	Q_g (Typ.)
100	125 at V_{GS} = 10 V	3	18.5 nC
	130 at V_{GS} = 4.5 V		

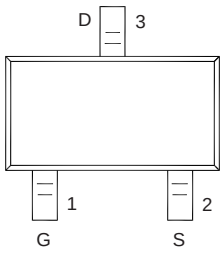
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Optimized for fast-switching applications

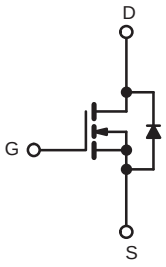
APPLICATIONS

- Synchronous Rectification
- Isolated DC/DC Converters in Telecom and Industrial

(SOT-23-3L)



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)[a]	T_C = 25 °C	I_D	3	A
	T_C = 100 °C		1.8	
Pulsed Drain Current[b]		I_{DM}	12	
Maximum Power Dissipation[c]	T_C = 25 °C	P_D	3	W
	T_C = 100 °C		1.28	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to +150	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	MAX	UNIT
Junction-to-Ambient (PCB Mount)[d]	R_{thJA}	120	°C/W
Junction-to-Case (Drain)	R_{thJC}	39	

Notes

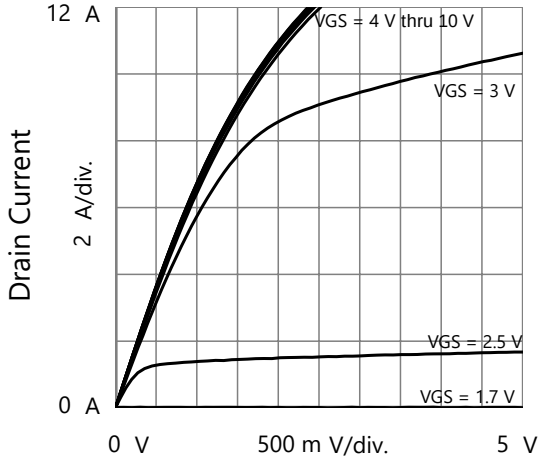
a. Calculated continuous current based on maximum allowablejunction temperature.
b. Repetitive rating; pulse width limited by max. junction temperature.
c. Pd is based on max. junction temperature, using junction-case thermal resistance.
d. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0, I_D = 250 µA	100			V
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1		3	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100 V, V_{GS} = 0 V			1	µA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 4.5 V	3			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 2 A		125	150	mΩ
		V_{GS} = 4.5 V, I_D = 2 A		130	160	
Forward Transconductance[a]	g_{fs}	V_{DS} = 5 V, I_D = 2 A		5		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz		1040		pF
Output Capacitance	C_{oss}			23		
Reverse Transfer Capacitance	C_{rss}			17		
Total Gate Charge	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 2 A		18.5		nC
Gate-Source Charge	Q_{gs}			2		
Gate-Drain Charge	Q_{gd}			2.5		
Gate Resistance	R_g	f = 1 MHz		2.1		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 50 V, R_L = 25 Ω I_D ≅ 2 A, V_{GEN} = 10 V, R_g = 1 Ω		8		ns
Rise Time	t_r			6		
Turn-Off DelayTime	$t_{d(off)}$			23		
Fall Time	t_f			5		
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I_S	T_C = 25 °C			3	A
Pulse Diode Forward Current	I_{SM}				12	
Body Diode Voltage	V_{SD}	I_S = 1 A			1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 2 A, dI/dt = 100 A/µs, T_J = 25 °C		15		ns
Body Diode Reverse Recovery Charge	Q_{rr}			30		nC

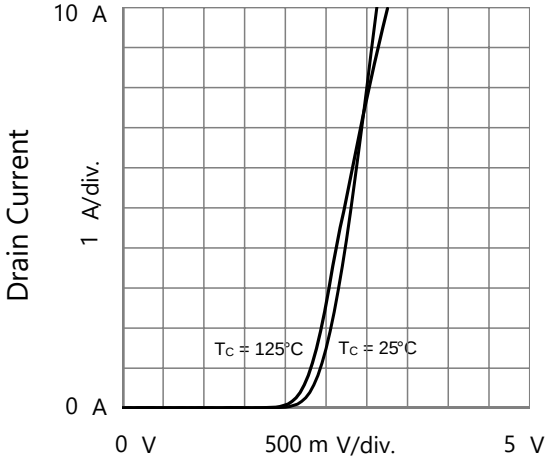
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

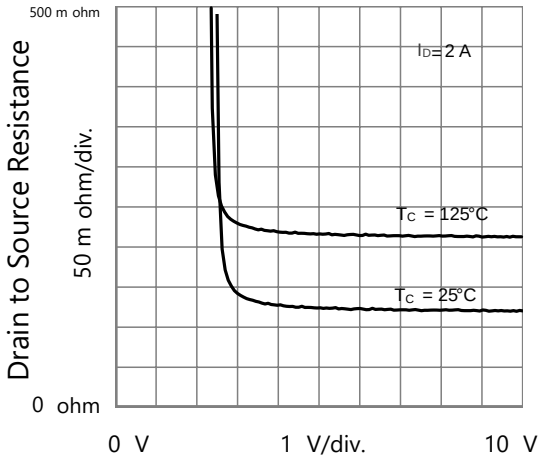
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



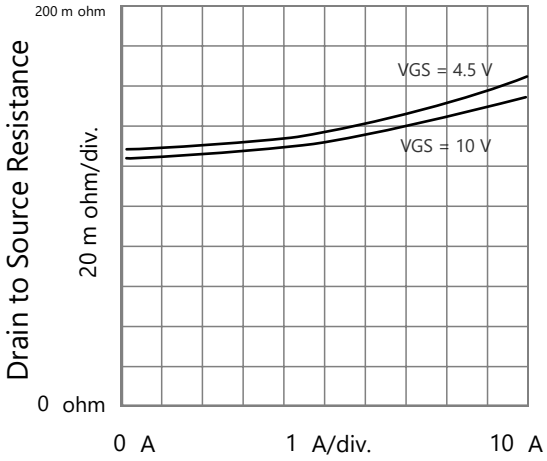
Drain to Source Voltage
Output Characteristics



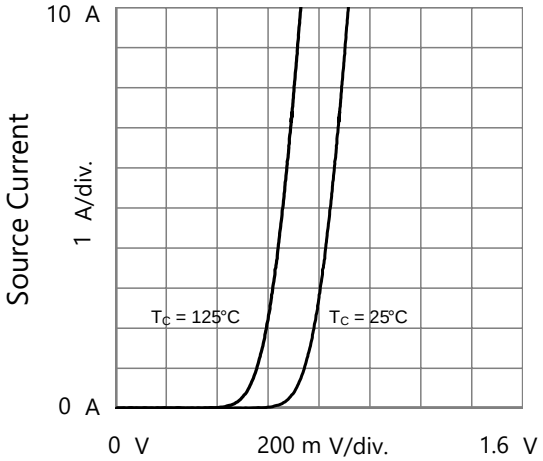
Gate to Source Voltage
Transfer Characteristics



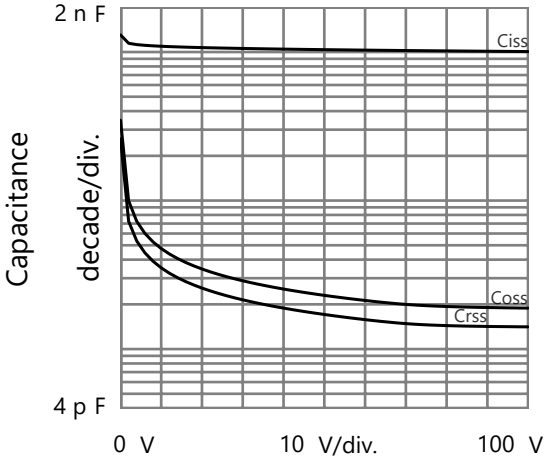
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

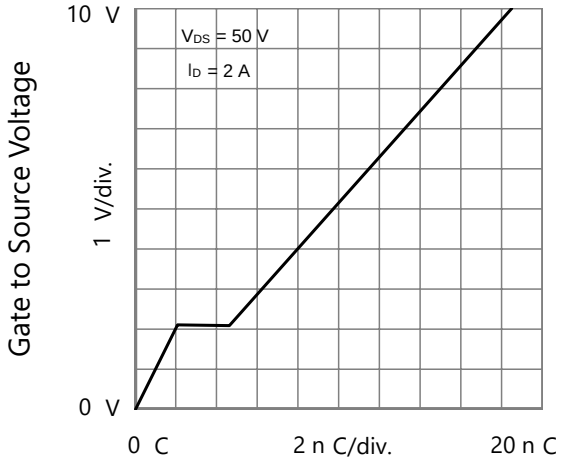


Source to Drain Voltage
Body Diode Forward Characteristics

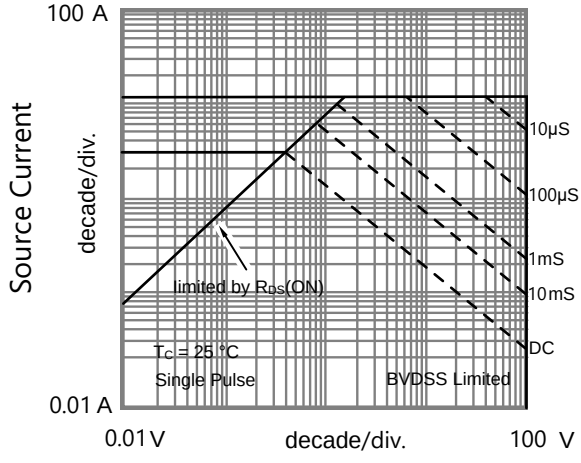


Drain to Source Voltage
Capacitances

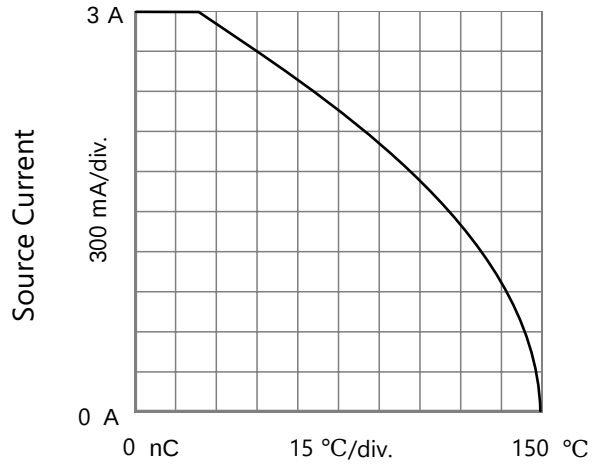
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

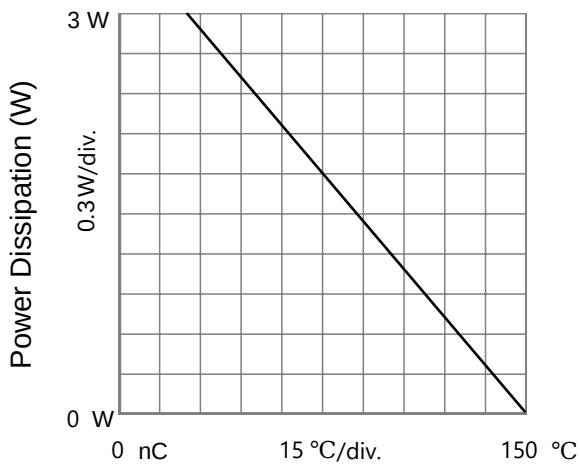


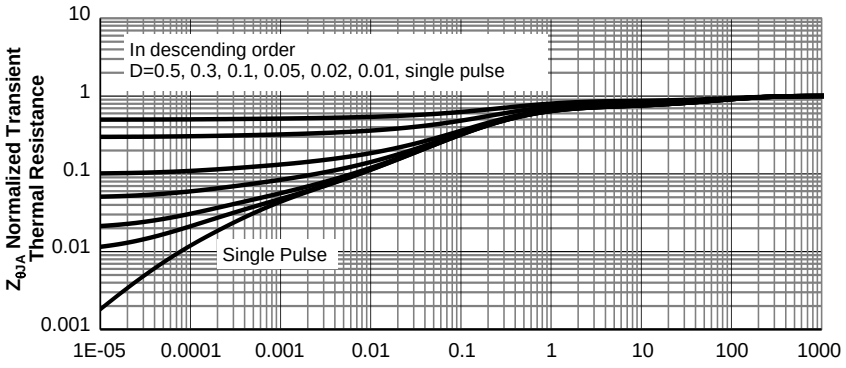
Gate to Source Voltage vs. GateCharge



Safe Operating Area, Junction-to-Ambient

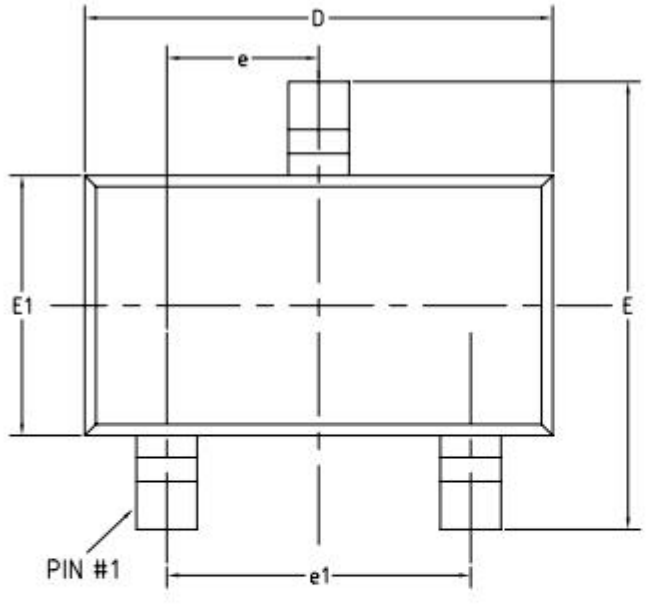


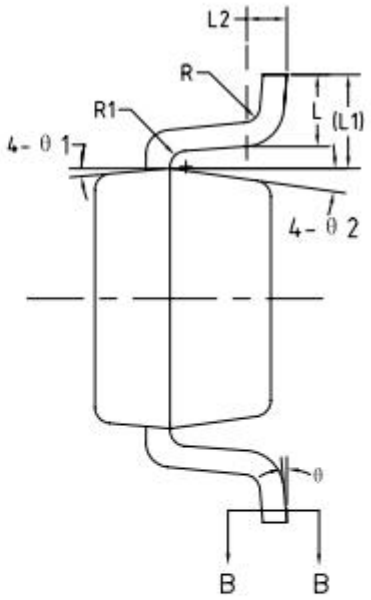


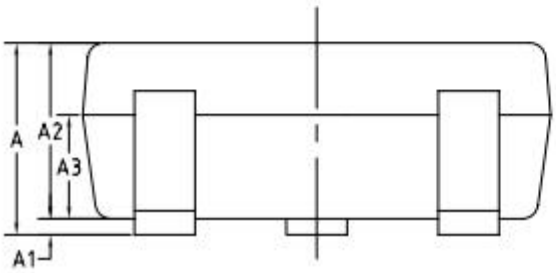


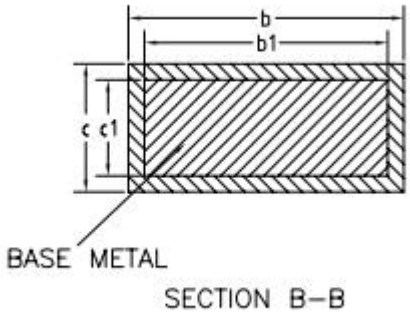
Normalized Maximum Transient Thermal Impedance

SOT-23-3L PACKAGE OUTLINE









SECTION B–B

COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	-	-	1.50
A1	0.00	-	0.18
A2	0.85	1.10	1.35
A3	0.58	0.65	0.72
b	0.23	-	0.53
b1	0.20	0.40	0.50
c	0.09	-	0.22
c1	0.08	0.13	0.21
D	2.78	2.95	3.10
E	2.58	2.80	3.03
E1	1.55	1.65	1.78
e	0.83	0.95	1.07
e1	1.78	1.90	2.02
L	0.28	0.45	0.62
L1	0.59REF		
L2	0.25BSC		
R	0.04	-	-
R1	0.04	-	0.21
θ	0°	-	8°
θ 1	8°	10°	12°
θ 2	8°	10°	12°

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