# N-Channel 30-V (D-S) Power MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(TYP.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
30	9.8 at V <sub>GS</sub> = 10 V	10	23 nC			
30	11 at V <sub>GS</sub> = 4.5 V	10				

#### **FEATURES**

- DT-Trench Power MOSFET
- 100 %  $R_{\rm g}$  and UIS Tested
- ESD Protected

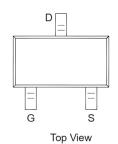


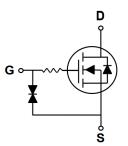
# COMPLIANT

#### **APPLICATIONS**

DC/DC Converter

#### SOT-23-3L Pin Configuration





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V <sub>DS</sub>	30	V			
Gate-Source Voltage		V <sub>GS</sub>	± 20	- V		
Continuous Proin Current (T. – 175 °C)	T <sub>C</sub> = 25 °C		10 <sup>a, d</sup>			
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 70 °C	l <sub>D</sub>	8.6 <sup>d</sup>	Α		
Pulsed Drain Current		I <sub>DM</sub>	40			
Single Pulse Avalanche Current			8			
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	70	mJ		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	10 <sup>a, d</sup>	Α		
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	4.5 <sup>a</sup>	W		
	T <sub>C</sub> = 70 °C		3	] VV		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	25	50	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	3.5	4.5	- C/W	

- Notes:
  a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.
- c. Maximum under steady state conditions is 90 °C/W. d. Calculated based on maximum junction temperature.



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static					<u>'</u>		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, } I_D = 250  \mu\text{A}$	30			V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.5		1.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 10	uA	
Zoro Coto Voltago Droin Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μA	
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10 μ/		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A		9.8	12		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 6 A		11	15	mΩ	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 8 A		40		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			848			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 100 \text{ KHz}$		326		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			48			
Total Gate Charge	$Q_g$			23		nC	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 8 A		1.2			
Gate-Drain Charge	Q <sub>gd</sub>			3.6			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		660		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			385			
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V, R}_{L} = 0.555 \Omega$		580		nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	ID = 8A, VGEN = 10 V, Rg = 1 $\Omega$		1600			
Fall Time	t <sub>f</sub>			920		1	
<b>Drain-Source Body Diode Characteristics</b>							
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			10	A	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				40	] ^	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1 A			1	V	
Body Diode Reverse Recovery Time		L = 0.A di/dk = 400 A/ = T = 05.00		10		nS	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 8 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		12		nC	

#### Notes:

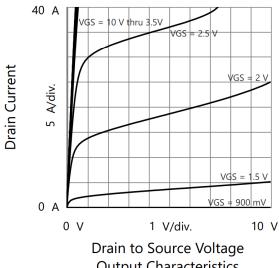
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

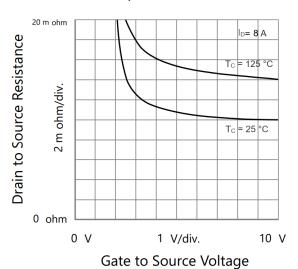
b. Guaranteed by design, not subject to production testing.



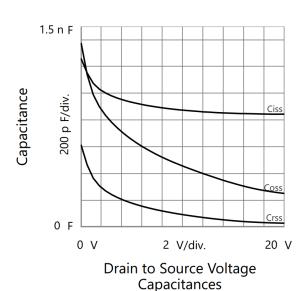
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

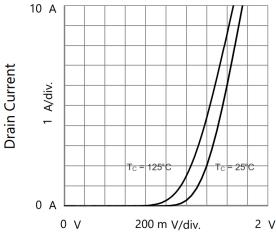


**Output Characteristics** 

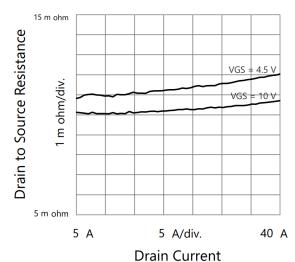


Drain to Source Resistance vs. Gate to Source Voltage

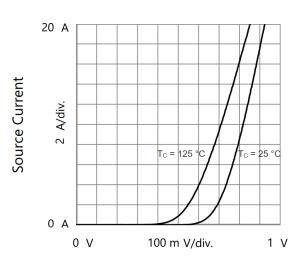




Gate to Source Voltage **Transfer Characteristics** 



Drain to Source Resistance vs. Drain Current



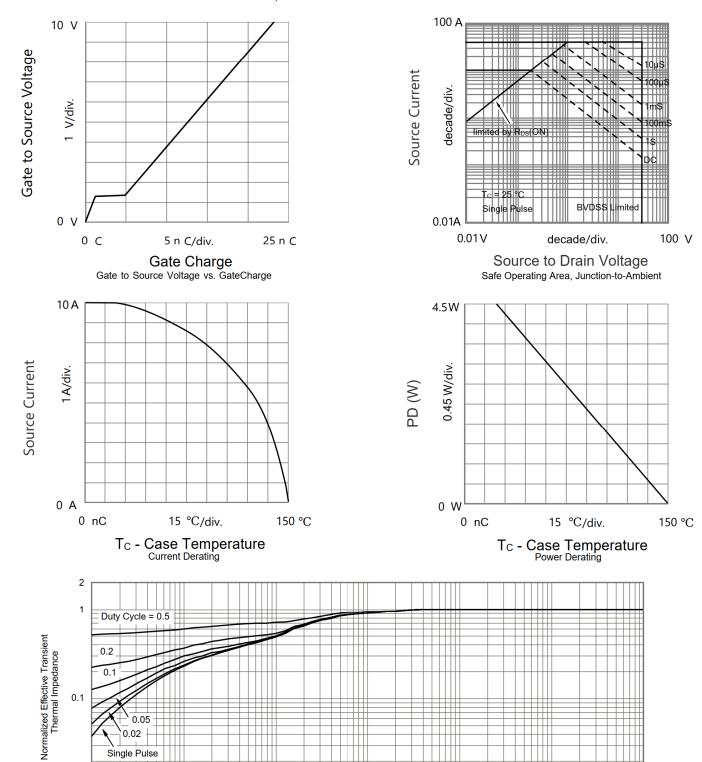
Source to Drain Voltage **Body Diode Forward Characteristics** 

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Single Pulse

10<sup>-3</sup>

0.01 10-4



Normalized Thermal Transient Impedance, Junction-to-Case

Square Wave Pulse Duration (s)

10<sup>-2</sup>

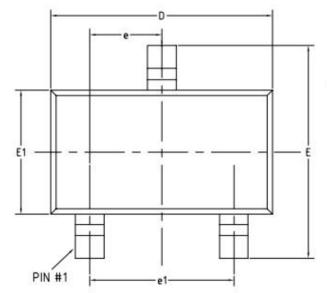
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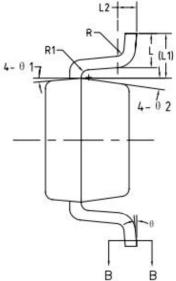
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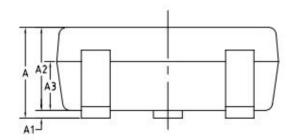


Din-Tek SEMICONDUCTOR

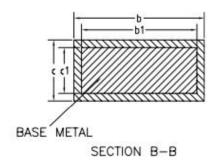
www.din-tek.jp







# COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



SYMBOL	MIN	TYP	MAX	
Α	-	-	1.50	
A1	0.00	-	0.18	
A2	0.85	1.10	1.35	
A3	0.58	0.65	0.72	
b	0.23	-	0.53	
b1	0.20	0.40	0.50	
С	0.09	-	0.22	
c1	0.08	0.13	0.21	
D	2.78	2.95	3.10	
E	2.58	2.80	3.03	
E1	1.55	1.65	1.78	
е	0.83	0.95	1.07	
e1	1.78	1.90	2.02	
L	0.28	0.45	0.62	
L1	0.59REF			
L2	0.25BSC			
R	0.04			
R1	0.04	-	0.21	
θ	0°	-	8°	
θ1	8°	10°	12°	
θ2	8°	10°	12°	





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