

P-Channel 150-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)			
- 150	0.635 at V _{GS} = - 10 V	- 1.1	7.7			
- 130	0.890 at V _{GS} = - 4.5 V	- 0.7	7.7			

FEATURES

• TrenchFET Power MOSFET



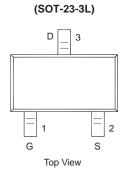
100% R_g and UIS Tested

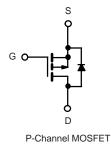


ROHS

APPLICATIONS

• Active Clamp Circuits in DC/DC Power Supplies





ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	LIMIT			
Drain-Source Voltage		V_{DS}	- 150	V		
Gate-Source Voltage		V_{GS}	± 20	 		
Continuous Dunin Comment /T 150 °C\2 b	T _A = 25 °C	1	- 1.1			
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C	l _D	- 0.75			
Pulsed Drain Current	I _{DM}	- 4.0	A			
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 1.1			
Single Pulse Avalanche Current	L = 1.0 mH	I _{AS}	1.0			
Single Pulse Avalanche Energy	L = 1.0 IIII	E _{AS}	1.01	mJ		
Mariana Barra Birata ya a h	T _A = 25 °C	P _D	0.75	W		
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	'D	0.48	vv		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 5 s	R	75	100		
Maximum Junction-to-Ambient ^a	Steady State	R_{thJA}	120	166	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	40	50]	

Notes:

- a. Surface Mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.



			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			<u>'</u>	•			
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$ $V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		- 150			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 2.0		- 4.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valta da Busia Comunant		V _{DS} = - 120 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -15 \text{ V}, V_{GS} = 10 \text{ V}$	- 1.1			Α	
		$V_{GS} = -10 \text{ V}, I_D = -0.5 \text{ A}$		635	795		
Drain-Source On-Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -0.5 \text{ A}$		890	1150	$m\Omega$	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 0.5 A		2.2		S	
Diode Forward Voltage	V _{SD}	I _S = - 1.0 A, V _{GS} = 0 V		- 0.7	- 1.2	٧	
Dynamic ^b							
Total Gate Charge	Q_g	V 100 V V 10 V		7.7	12		
Gate-Source Charge	Q_{gs}	$V_{DS} = -120 \text{ V}, V_{GS} = 10 \text{ V},$ $I_{D} \cong -0.5 \text{ A}$		1.5		nC	
Gate-Drain Charge	Q _{gd}	ID = - 0.3 A		2.5			
Gate Resistance	R_g	f = 1.0 MHz		9		Ω	
Input Capacitance	C _{iss}			340	510		
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		30		pF	
Reverse Transfer Capacitance	C _{rss}			16			
Switching ^c							
Turn On Time	t _{d(on)}	$V_{DD} = -120 \text{ V}, R_L = 75 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -10 \text{ V}$		7	11	ne	
Turn-On Time	t _r			11	17		
Time Off Time	t _{d(off)}	$R_{\rm g} = 6 \Omega$		16	25	- ns	
Turn-Off Time	t _f	g – 0 22		11	17		
Body Diode Reverse Recovery Charge	Q _{rr}	r I _F = 0.5 A, dI/dt = 100 A/μs		90	135	nC	

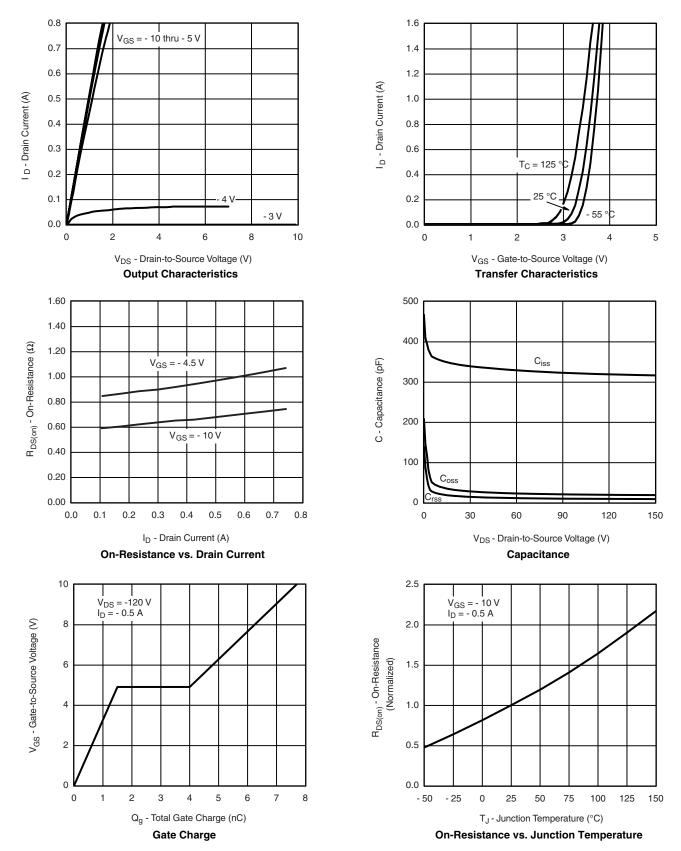
Notes:

- a. Pulse test: PW \leq 300 μs duty cycle \leq 2 %.
- b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

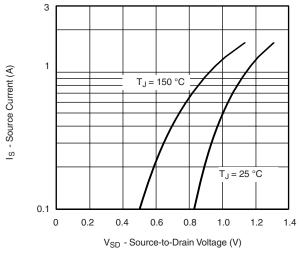


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

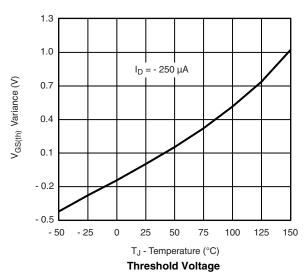


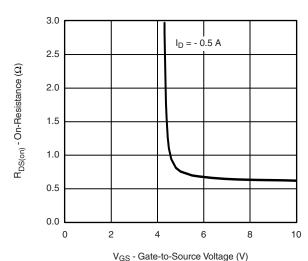


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

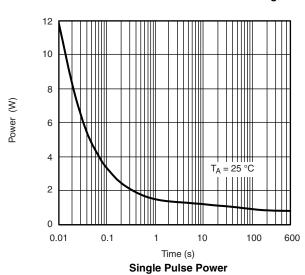


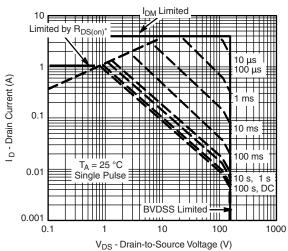
Source-Drain Diode Forward Voltage





On-Resistance vs. Gate-to-Source Voltage



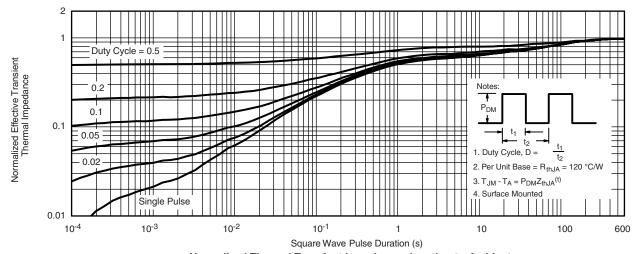


 * V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area



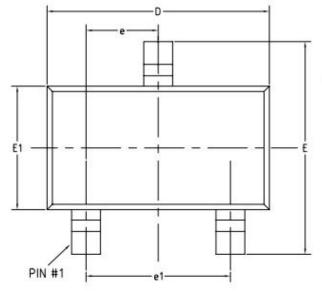
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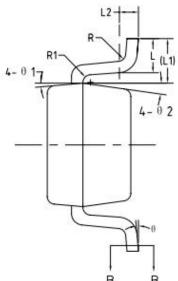


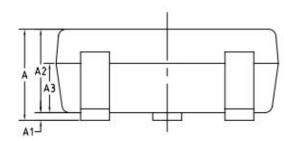
Normalized Thermal Transient Impedance, Junction-to-Ambient

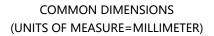


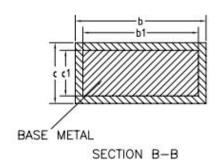
SOT-23-3L PACKAGE OUTLINE











SYMBOL	MIN	TYP	MAX
Α	-	-	1.50
A1	0.00	-	0.18
A2	0.85	1.10	1.35
A3	0.58	0.65	0.72
b	0.23	-	0.53
b1	0.20	0.40	0.50
С	0.09	ı	0.22
c1	0.08	0.13	0.21
D	2.78	2.95	3.10
Е	2.58	2.80	3.03
E1	1.55	1.65	1.78
е	0.83	0.95	1.07
e1	1.78	1.90	2.02
L	0.28	0.45	0.62
L1	0.59REF		
L2	0.25BSC		
R	0.04	-	-
R1	0.04	-	0.21
θ	0°	-	8°
θ1	8°	10°	12°
θ2	8°	10°	12°





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