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N-Channel 25 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)(TYP.)	I _D (A) ^c	Q _g (TYP.)	
	0.225 at V _{GS} = 10 V	0.7		
25	0.265 at V _{GS} = 4.5 V	0.6	1.4 nC	
	0.369 at V _{GS} = 2.5 V	0.4	1	



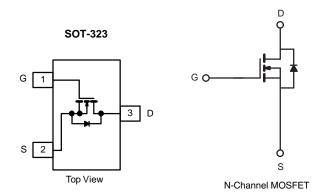
RoHS COMPLIANT

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Smart phones, tablet PC's
 - DC/DC converters
 - Boost converters
 - Load switch, OVP switch



PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	25	V
Gate-Source Voltage		V _{GS}	± 10	v
	T _C = 25 °C		0.7	
Continuous Drain Current (T 150 °C)	T _C = 70 °C		0.5	
ontinuous Drain Current (T _J = 150 °C)	T _A = 25 °C	'D	0.65 ^{a, b}	
	T _A = 70 °C	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	0.46 ^{a, b}	A
Pulsed Drain Current (t = 300 μs)		I _{DM}	2.8	
Continuous Course Dusin Diada Courset	T _C = 25 °C		0.7	
Continuous Source-Drain Diode Current	T _A = 25 °C	's	0.3	
	T _C = 25 °C		0.27	
Mariana Darra Dissipation	T _C = 70 °C		0.15	w
Maximum Power Dissipation	T _A = 25 °C	P _D	0.2 ^{a, b}	VV
	T _A = 70 °C		0.11 ^{a, b}	
Operating Junction and Storage Temperature Rang	е	T _J , T _{stg} -55 to +150		°C
Soldering Recommendations (Peak Temperature)			260	

Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Based on T_C = 25 °C.



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	25	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 050 A	-	32	-	mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-3	-		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.4	-	1.5	V	
Oata Oa aaa Laalaa		V _{DS} = 0 V, V _{GS} = 4.5 V	-	-	1		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	-	-	± 20	1 .	
Zana Oaka Walliana Buria Oanada		V _{DS} = 20 V, V _{GS} = 0 V	-	-	1	— μA —	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10		
On-State Drain Current a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	0.7	-	-	Α	
		V _{GS} = 10 V, I _D = 0.5 A	-	0.225	0.252		
Drain-Source On-State Resistance a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 0.3 \text{ A}$	-	0.265	0.344	Ω	
		V _{GS} = 2.5 V, I _D = 0.3 A	-	0.369	0.485		
Forward Transconductance a	9 _{fs}	V _{DS} = 10 V, I _D = 0.5 A	-	5	-	S	
Dynamic ^b				•			
Input Capacitance	C _{iss}		-	185	-		
Output Capacitance	C _{oss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz	-	43	-	pF	
Reverse Transfer Capacitance	C _{rss}		-	11	-		
Table Oats Observe	0	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 0.5 A	-	2.7	4.1	nC	
Total Gate Charge	Q_g		-	1.4	2.1		
Gate-Source Charge	Q _{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 0.3 \text{ A}$	-	0.3	-		
Gate-Drain Charge	Q _{gd}		-	0.5	-		
Gate Resistance	R_g	f = 1 MHz	-	7	-	Ω	
Turn-On Delay Time	t _{d(on)}		-	2	-		
Rise Time	t _r	$V_{DD} = 20 \text{ V}, R_{L} = 13.6 \Omega$	-	9	-		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 0.7 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		8	-		
Fall Time	t _f		-	8	-		
Turn-On Delay Time	t _{d(on)}		-	8	-	ns	
Rise Time	t _r	$V_{DD} = 20 \text{ V}, R_L = 13.6 \Omega$	-	13	-		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 0.3 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		15	-		
Fall Time	t _f		-	6	-	1	
Drain-Source Body Diode Characterist	ics			•			
Continuous Source-Drain Diode Current	Is	T _C = 25 °C	-	-	0.7	Λ.	
Pulse Diode Forward Current ^a	I _{SM}		-	-	2.8	Α	
Body Diode Voltage	V_{SD}	I _F = 0.5 A	-	0.7	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}		-	8	16	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 0 5 A 31/31 400 A/ T 05 00	-	3	6	nC	
Reverse Recovery Fall Time	ta	$I_F = 0.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$	-	5	-		
Reverse Recovery Rise Time	t _b	_		3	-	ns	

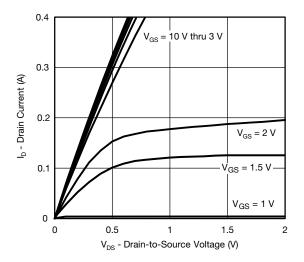
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

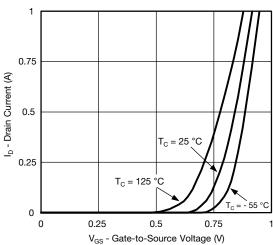
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



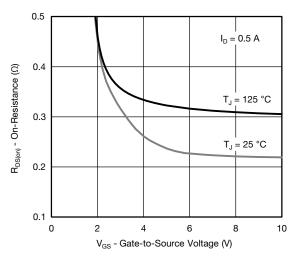
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



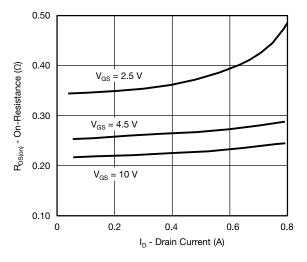




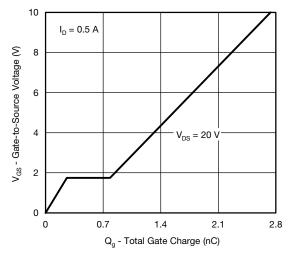
Transfer Characteristics



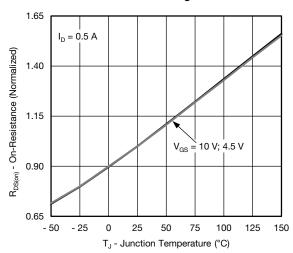
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Drain Current



Gate Charge

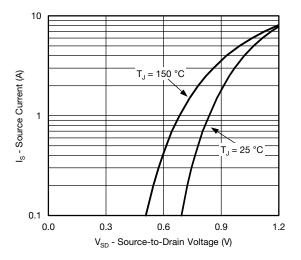


On-Resistance vs. Junction Temperature

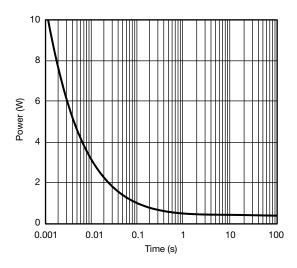




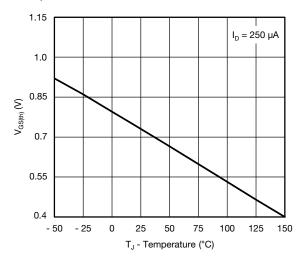
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



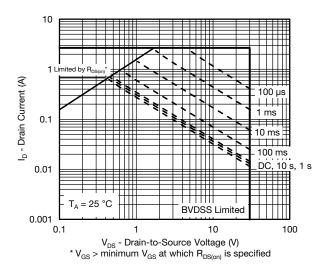
Source-Drain Diode Forward Voltage



Single Pulse Power, Junction-to-Ambient



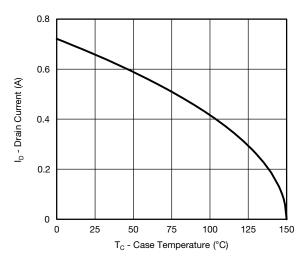
Threshold Voltage



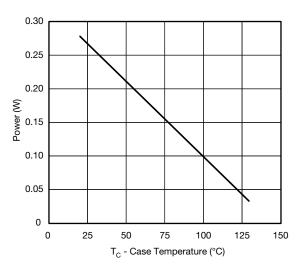
Safe Operating Area, Junction-to-Ambient

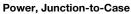
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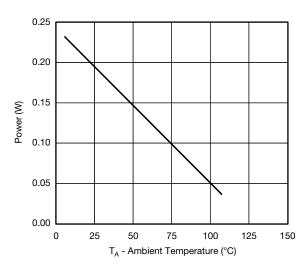
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*





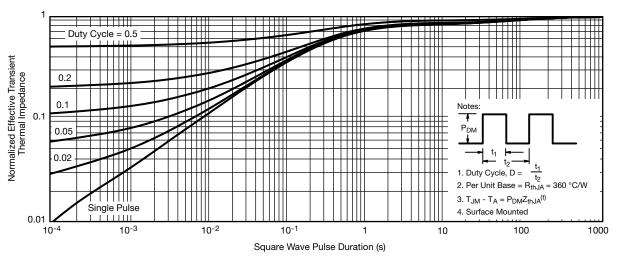


Power, Junction-to-Ambient

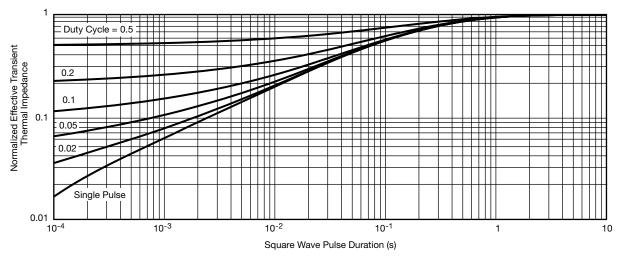
^{*} The power dissipation P_D is based on $T_{J \text{ (max.)}} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



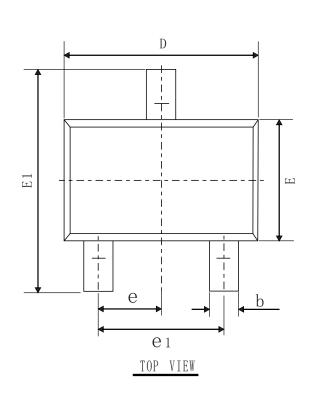
Normalized Thermal Transient Impedance, Junction-to-Ambient

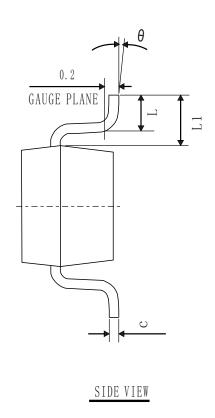


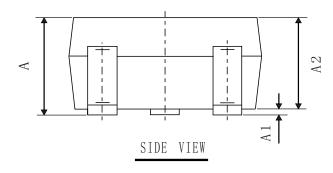
Normalized Thermal Transient Impedance, Junction-to-Foot

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SOT-323 PACKAGE OUTLINE







COMMON DIMENSIONS (UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.90	1.00	1.10
A 1	0.00	0.05	0.10
A2	0.90	0.95	1.00
b	0.20	0.25	0.30
С	0.08	0.10	0.15
e 1	1.20	1.30	1.40
D	2.00	2.10	2.20
Е	1. 15	1.25	1.35
E1	2.15	2.30	2.45
L	0.26	0.36	0.46
θ	0°	4°	8°
L1	0.525 REF		
е	0.65 TYP		





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