

N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(m\Omega)(Typ.)$	I _D (A) ^a	Q _g (Typ.)			
30	17 at V _{GS} = 10 V	7.5	7.2 nC			
30	25 at V _{GS} = 4.5 V	7.5				

FEATURES

- DT-Trench Power MOSFET
- Very low on-resistance
- Excellent gate charge x R_{DS (on)} product(FOM)

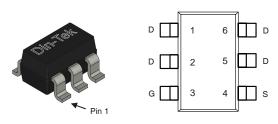


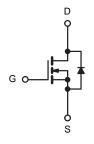
APPLICATIONS

- DC-DC Converter
- Ideal for high-frequency switching and synchronous rectification

SOT-23-6L Pin Configuration

Top View





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	30	V		
Gate-Source Voltage	V _{GS}	± 20	_ v		
Continuous Drain Current /T 450 9C\2	T _C = 25 °C	,	7.5		
Continuous Drain Current (T _J = 150 °C) ^a	T _C = 70 °C	I _D	6.0	А	
Pulsed Drain Current ^b	I _{DM}	64	1		
Maximum Power Dissipations	T _C = 25 °C	В	2.0	w	
Maximum Power Dissipation ^c	T _C = 70 °C	P _D	1.28	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	110	°C/W		
Junction-to-Case (Drain)	R _{thJC}	62.5			

Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R_{8JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V_{DS} $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$ 1.5		-	2.6		
Gate-Body Leakage			-	-	± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 V, V_{GS} = 0 V$	1	-	1	1 5 μΑ	
Zelo Gate Voltage Diaili Culterit		V_{DS} = 30 V, V $_{GS}$ = 0 V, T_{J} = 55 °C	-	-	5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	64	-	-	Α	
Drain-Source On-State Resistance ^a	D	V _{GS} = 10 V, I _D = 7.5 A	-	17	22	mΩ	
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 7.5 A	-	25	35		
Forward Transconductance ^a	9fs	V _{DS} = 5 V, I _D = 7.5 A		20	-	S	
Dynamic ^b					•		
Input Capacitance	C _{iss}		-	373	-	pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 15 V, f = 1 MHz	-	67	-		
Reverse Transfer Capacitance	C _{rss}		-	41	-		
Total Gate Charge ^c	Qg		-	7.2	-		
Gate-Source Charge ^c	Q_{gs}	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 7.5 A		1.3	-	nC	
Gate-Drain Charge ^c	Q_{gd}		-	1.7	-		
Gate Resistance	R _g	f = 1 MHz	-	2	-	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	4.5	-		
Rise Time ^c	t _r	$V_{DS} = 15 \text{ V}, R_L=2\Omega, R_{GEN}=3 \Omega,$	-	2.7	-		
Turn-Off Delay Time ^c	t _{d(off)}	V _{GS} = 10 V	-	14.9	-	ns	
Fall Time ^c	t _f		-	2.9	-		
Drain-Source Body Diode Ratings and	Characterist	ics ^b (T _C = 25 °C)					
Continuous Source-Drain Diode Current	I _S		-	-	2.5	Α	
Pulsed Current	I _{SM}		-	-	64	Α	
Forward Voltage ^a	V_{SD}	I _F = 1 A, V _{GS} = 0 V	-	-	1	V	
Reverse Recovery Time	t _{rr}	1 75A 45/4 - 400 A/v-	-	10.5	-	ns	
Reverse Recovery Charge	Q _{rr}	$I_F = 7.5 \text{A}, \text{di/dt} = 100 \text{A/}\mu\text{s}$	-	4.5	-	nC	

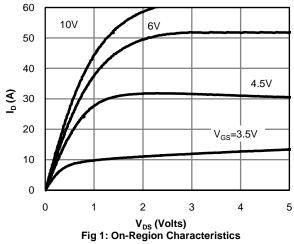
Notes

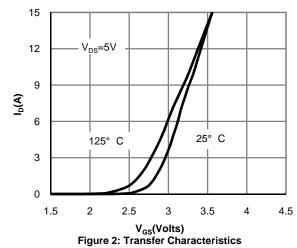
- a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended pe riods may affect device reliability.



TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)





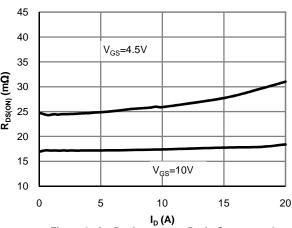


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

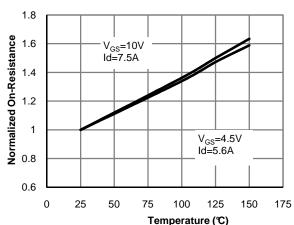
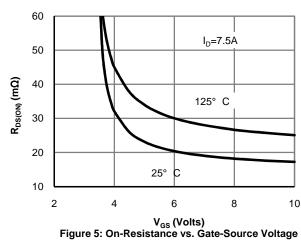
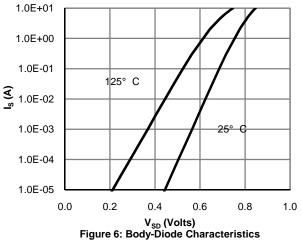


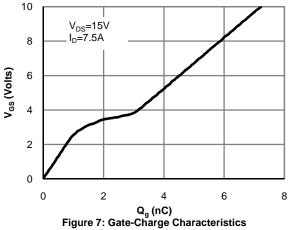
Figure 4: On-Resistance vs. Junction Temperature

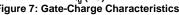


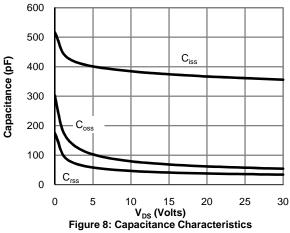




TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)







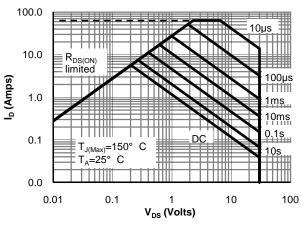


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

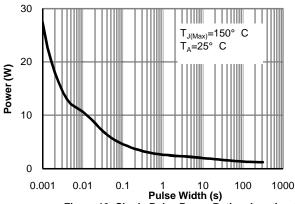


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

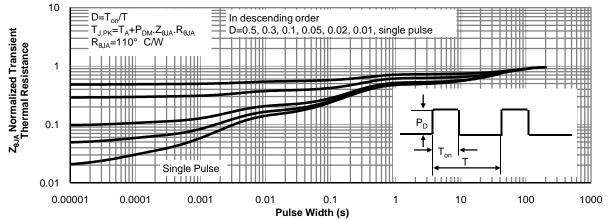


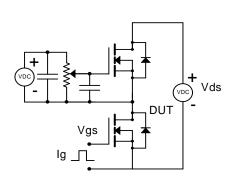
Figure 11: Normalized Maximum Transient Thermal Impedance

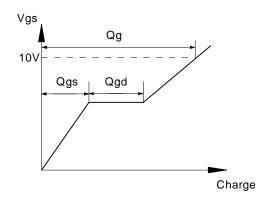




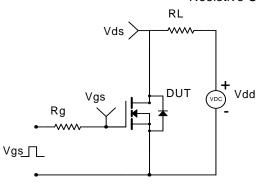
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

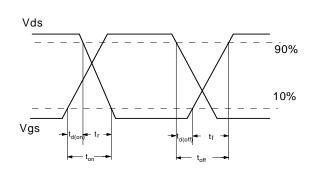
Gate Charge Test Circuit & Waveform



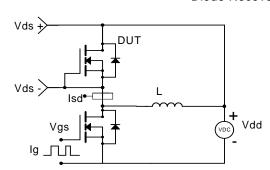


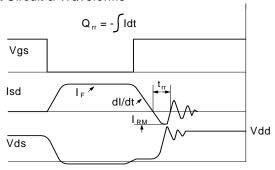
Resistive Switching Test Circuit & Waveforms





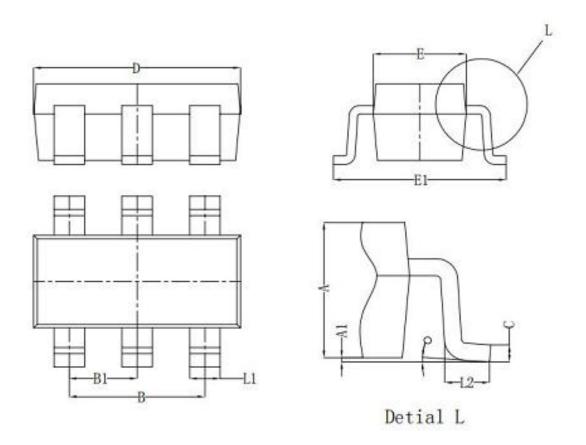
Diode Recovery Test Circuit & Waveforms







SOT-23-6L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE= MILLIMETER)

SYMBOL	MIN	TYP	MAX
А	0.95	1.10	1.35
A1	0.00	0.05	0.12
L1	0.25	0.40	0.55
С	0.08	0.15	0.22
D	2.77	2.92	3.12
Е	1.45	1.60	1.75
E1	2.50	2.80	3.10
В	1.75	1.90	2.10
B1	0.95TYP		
L2	0.28	0.45	0.63
0	0°	4°	8°





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