

P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	- 20			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	0.030			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -2.5 V$	0.041			
I _D (A)	- 7			
Configuration	Single			

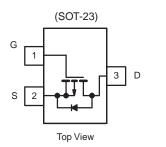
FEATURES

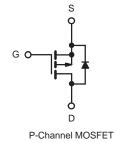
• DT-Trench Power MOSFET

APPLICATIONS

Load Switch for Portable Devices







ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \text{ °C}$, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	- 20	V	
Gate-Source Voltage		V _{GS}	± 8	v	
Continuous Drain Current	T _C = 25 °C	1	- 7		
	T _C = 125 °C	ID	- 5		
Continuous Source Current (Diode Conduction)	I _S	- 4.5	А		
Pulsed Drain Current ^a		I _{DM}	- 25		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 15		
Single Pulse Avalanche Energy	L = 0.1 MH	E _{AS}	7	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	D	2.3	W	
	T _C = 125 °C	P _D	0.72	٧V	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	165	°C/W	
Junction-to-Foot (Drain)		R _{thJF}	70	0/10	

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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static					•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$		- 20	-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$		-	- 2	v
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$		-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = - 12 V	-	-	- 1	μA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V_{DS} = - 12 V, T _J = 125 °C	-	-	- 50	
		$V_{GS} = 0 V$	V_{DS} = - 12 V, T _J = 175 °C	-	-	- 150	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = -4.5 V$	$V_{DS} \le$ - 5 V	- 25	-	-	Α
		$V_{GS} = -4.5 V$	I _D = - 3.5 A	-	0.028	0.030	Ω
		$V_{GS} = -4.5 V$	I _D = - 3.5 A, T _J = 125 °C	-	0.037	0.045	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 V$	I _D = - 3.5 A, T _J = 175 °C	-	0.042	0.049	
		$V_{GS} = -2.5 V$	I _D = - 3 A	-	0.036	0.041	
		V _{GS} = - 1.8 V	I _D = - 2 A	-	0.047	0.052	
Forward Transconductance ^b	g _{fs}	V _{DS} =	- 5 V, I _D = - 1.6 A	-	7	-	S
Dynamic ^b	•				•		
Input Capacitance	C _{iss}			-	690	-	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V V _{DS} = - 6 V, f = 1 MHz		266	-	pF
Reverse Transfer Capacitance	C _{rss}			-	191	-	1
Total Gate Charge ^c	Qg			-	8.3	-	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 4.5 V	$V_{DS} = -6 V, I_{D} = -3.85 A$	-	1	-	nC
Gate-Drain Charge ^c	Q _{gd}			-	2.4	-	
Gate Resistance	Rg	f = 1 MHz		4.1	8.2	13.3	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	17	-	
Rise Time ^c	t _r	$V_{DD} = - \ 6 \ V, \ R_L = 1.6 \ \Omega$ $I_D \cong - \ 3.85 \ A, \ V_{GEN} = - \ 4.5 \ V, \ R_g = 1 \ \Omega$		-	19	-	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	28		
Fall Time ^c	t _f			-	13	-	
Source-Drain Diode Ratings and Chara	acteristics ^b	·					
Pulsed Current ^a	I _{SM}			-	-	25	А
	1	I _F = - 2 A, V _{GS} = 0 V			1	- 1.2	V

Notes

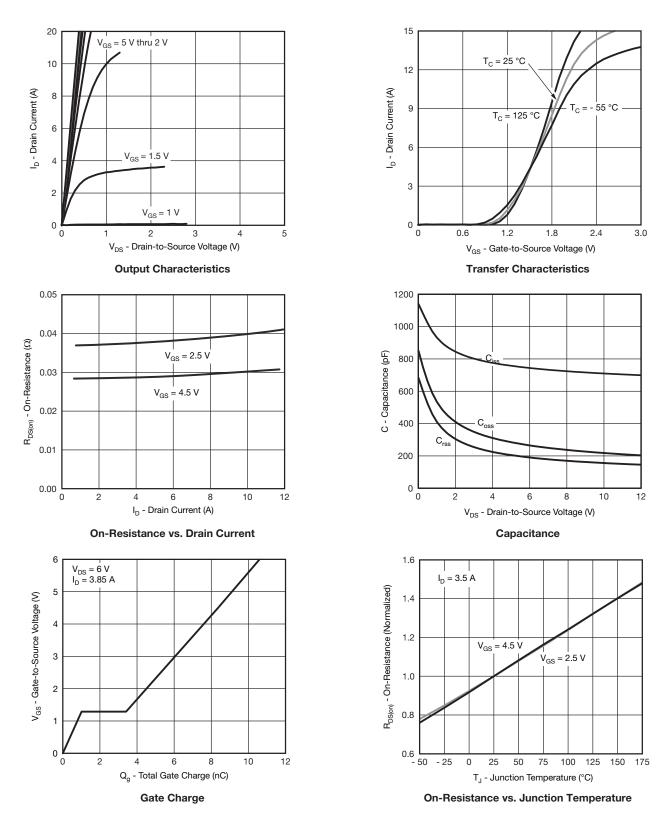
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



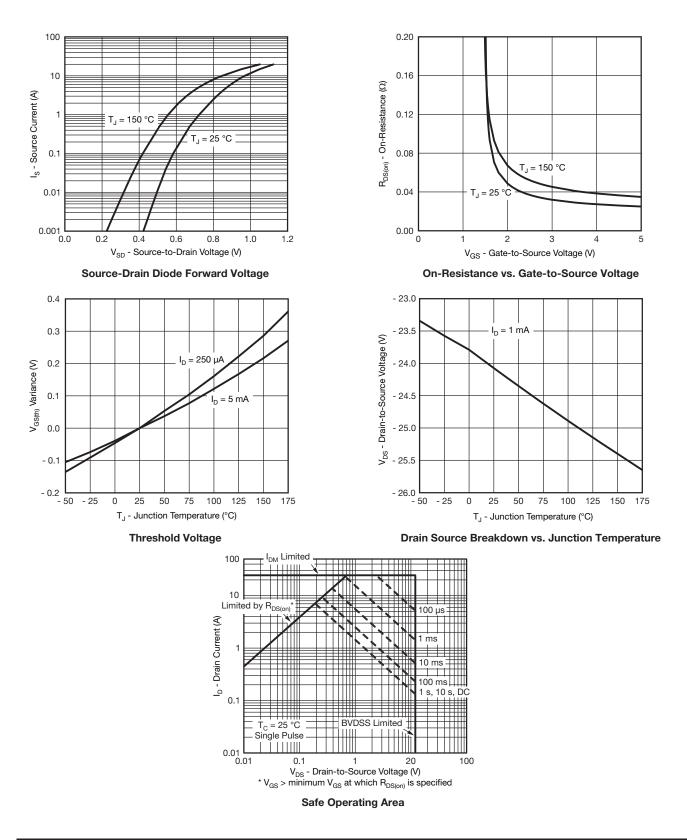


TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



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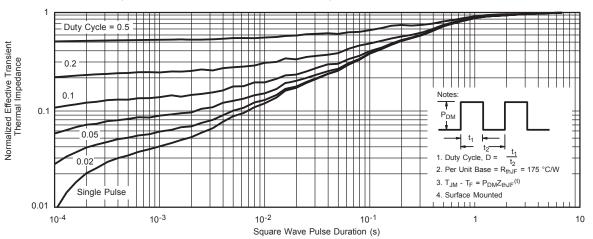
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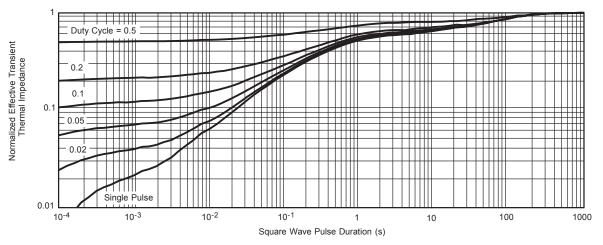


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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot





Note

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

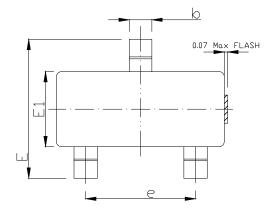
- Normalized Transient Thermal Impedance Junction-to-Foot (25 C)

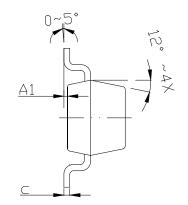
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

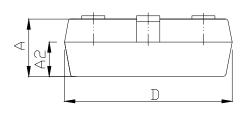
The characteristics shown in the two graphs

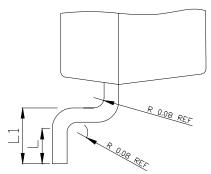


SOT-23 PACKAGE OUTLINE









COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER			
SIMDOL	MIN	NOM	MAX	
А	0.80	1.00	1.30	
A1	0.00	0.05	0.15	
b	0.25	0.40	0.55	
с	0.11 BSC			
D	2 . 6 0	2.90	3.20	
Е	2.10	2.40	2.70	
E1	1.10	1.30	1.48	
е	1.90 BSC			
L	0.17	_	—	
L1	0.28	0.40	0.53	
A2	0.60 REF			



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