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N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^e	Q _g (Typ.)			
	0.033 at V _{GS} = 4.5 V	3.8				
20	0.045 at $V_{GS} = 2.5 \text{ V}$	3.6	8.8 nC			
	0.051 at V _{GS} = 1.8 V	2.6				

FEATURES

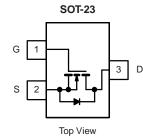
- DT-Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS

APPLICATIONS

- DC/DC Converters
- Load Switch for Portable Applications



Parameter	Symbol	Limit	Unit V	
Drain-Source Voltage Gate-Source Voltage		V _{DS}		20
		V_{GS}		± 8
	T _C = 25 °C		3.8	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C	1 , [3.1	
Continuous Drain Current (1) = 150 °C)	T _A = 25 °C	l _D	3.8	
	T _A = 70 °C	1 1	3	A
Pulsed Drain Current		I _{DM}	20	
Continuous Source-Drain Diode Current	T _C = 25 °C	,	1.75	
Continuous Source-Drain Diode Current	T _A = 25 °C	l _S	1.04 ^{b, c}	
	T _C = 25 °C		2.1	
Maximum Power Discipation	T _C = 70 °C	1 5	1.3	W
Maximum Power Dissipation	T _A = 25 °C	P _D	1.25 ^{b, c}	VV
	T _A = 70 °C	1	0.8 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature)			260	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	80	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	60			

Notes:

- a. Package limited
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 125 °C/W.
- e. Based on T_C = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	<u> </u>		<u>'</u>		•		
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		25		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I _D = 250 μA		- 2.6			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	0.45		1.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 70 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	20			Α	
	. , ,	$V_{GS} = 4.5 \text{ V}, I_D = 3.8 \text{ A}$		0.0265	0.033	+	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 3.6 \text{ A}$		0.0396	0.045	Ω	
	` '	$V_{GS} = 1.8 \text{ V}, I_D = 2.6 \text{ A}$		0.0445	0.051	†	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 3.8 A		24		S	
Dynamic ^b	1 -10				l		
Input Capacitance	C _{iss}			865		pF	
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		105			
Reverse Transfer Capacitance	C _{rss}			55			
Treverse Harrist Capacitation	Q _g	$V_{DS} = 10 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 5.0 \text{ A}$		12	18		
Total Gate Charge		55 11 1, 163 1 1, 16 610 11		8.8	14	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5.0 \text{ A}$		1.1			
Gate-Drain Charge	Q _{gd}	D3 - 7 G3 - 7 D		0.7			
Gate Resistance	R _g	f = 1 MHz	0.5	2.4	4.8	Ω	
Turn-On Delay Time	t _{d(on)}			8	16		
Rise Time	t _r	V_{DD} = 10 V, R_L = 2.2 Ω		17	26		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 4$ A, $V_{GEN}=4.5$ V, $R_g=1$ Ω		31	47		
Fall Time	t _f			8	16	1	
Turn-On Delay Time	t _{d(on)}			5	10	_ ns - -	
Rise Time	t _r	V_{DD} = 10 V, R_L = 2.2 Ω		13	20		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 4$ A, $V_{GEN}=5$ V, $R_g=1$ Ω		21	32		
Fall Time	t _f			6	12		
Drain-Source Body Diode Characteristic	-		<u> </u>				
Continuous Source-Drain Diode Current	Is	T _C = 25 °C			1.75		
Pulse Diode Forward Current	I _{SM}	-			20	Α	
Body Diode Voltage	V _{SD}	I _S = 4 A, V _{GS} = 0 V		0.75	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			12	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			5	10	nC	
Reverse Recovery Fall Time	t _a	$I_F = 4 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		7			
Reverse Recovery Rise Time	t _b			5		ns	

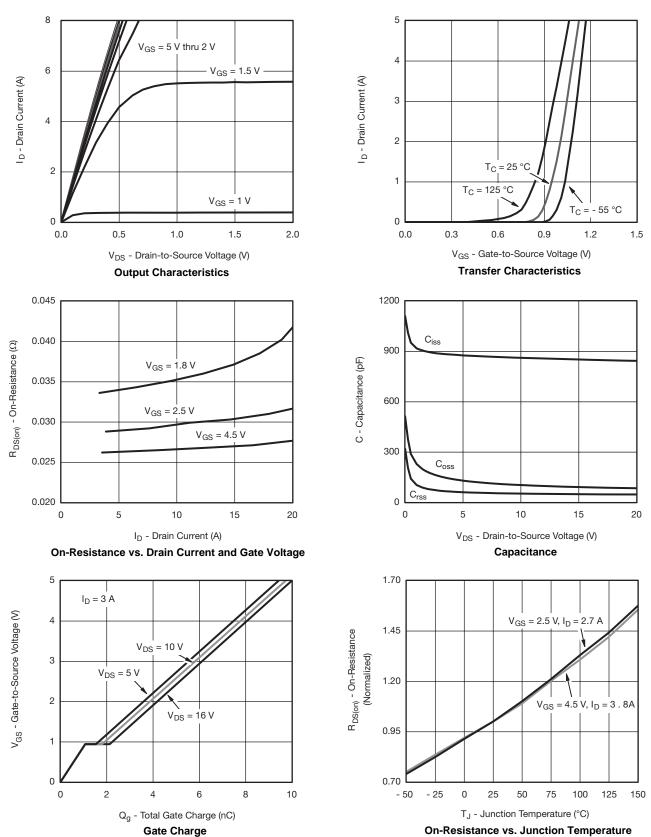
Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$ b. Guaranteed by design, not subject to production testing.

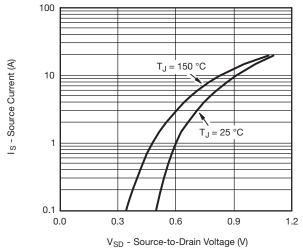


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

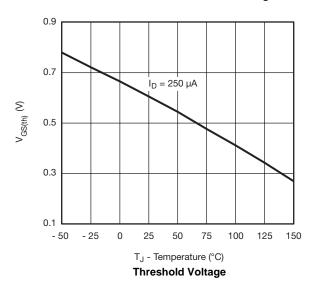


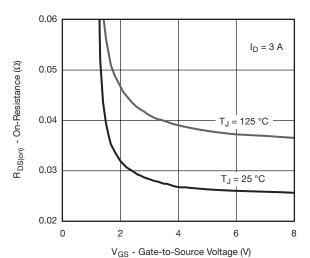


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Source-Drain Diode Forward Voltage

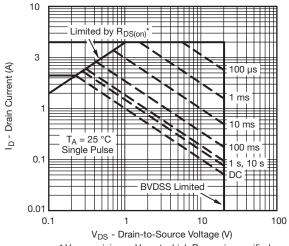




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power (Junction-to-Ambient)



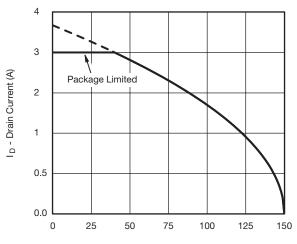
 * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

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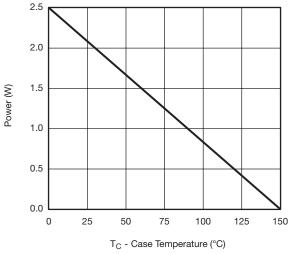
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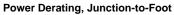
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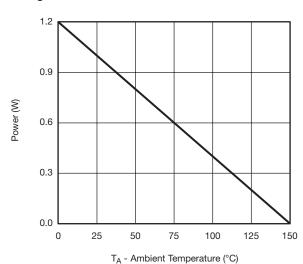


T_C - Case Temperature (°C)

Current Derating*





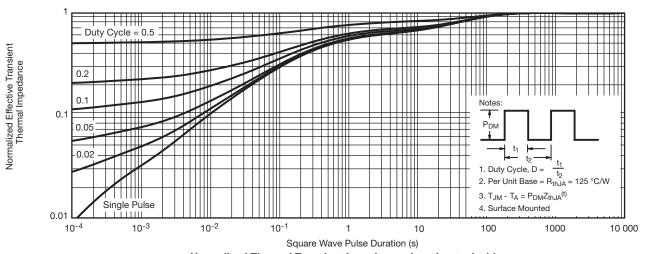


Power Derating, Junction-to-Ambient

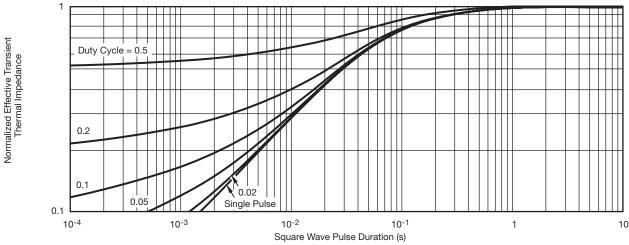
^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

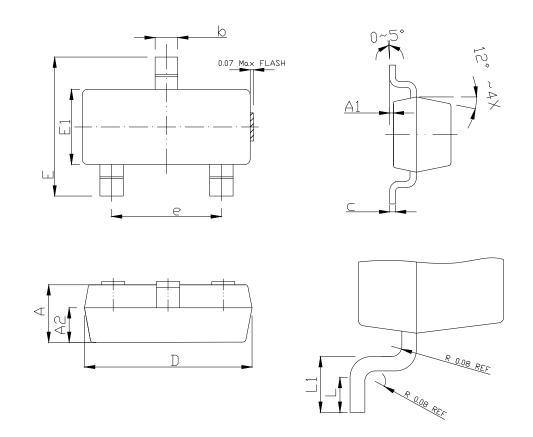


Normalized Thermal Transient Impedance, Junction-to-Foot

Din-Tek SEMICONDUCTOR

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SOT-23 PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER			
	MIN	NOM	MAX	
A	0.80	1.00	1.30	
A1	0.00	0.05	0.15	
b	0. 25	0.40	0.55	
С	0.11 BSC			
D	2 .6 0	2.90	3 . 2 0	
Е	2.10	2.40	2.70	
E1	1.10	1.30	1.48	
е	1.90 BSC			
L	0.17	_	_	
L1	0. 28	0.40	0.53	
A2	0.60 REF			



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