

P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I _D (A) ^a	Q _g (Typ.)			
-20	45 at V _{GS} = -4.5 V	-5.5	13 nC			
-20	60 at V _{GS} = -2.5 V	-5.5				

FEATURES

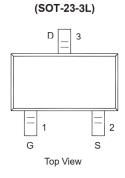
- DT-Trench Power MOSFET
- 100 % R_a and UIS Tested

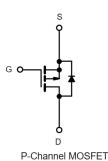
Pb-free

RoHS

APPLICATIONS

- Power Management for Portable and Consumer
 - Load Switches
 - DC/DC Converters





ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted							
Parameter	Symbol	Limit	Unit				
Drain-Source Voltage	V _{DS}	- 20	V				
Gate-Source Voltage	V_{GS}	± 12	7 v				
	T _C = 25 °C		- 5.5				
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C	1 . [- 4.6				
Continuous Diam Current (1) = 130 C)	T _A = 25 °C	l _D	- 3.5 ^{a, b}				
	T _A = 70 °C	1 1	- 2.6 ^{a, b}	٦ ,			
Pulsed Drain Current	I _{DM}	-18	_ A				
Continuous Source-Drain Diode Current	T _C = 25 °C	I-	- 2.4				
Continuous Source-Drain Diode Current	T _A = 25 °C	- I _s	- 1 ^{a, b}				
Avalanche Current	L = 0.1 mH	I _{AS}	- 5.5				
	T _C = 25 °C		1.7				
Maximum Davian Disaination	T _C = 70 °C	1 , [1.1	١ ,,,			
Maximum Power Dissipation	T _A = 25 °C	- P _D -	1 ^{a, b}	- W			
	T _A = 70 °C	1 †	0.6 ^{a, b}	7			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C				

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	65	80	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	55	0, **	

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 100C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						·	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.4		-1.2	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10		
Zana Oata Valta na Duair Oumant		V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μA	
Zero Gate Voltage Drain Current	DSS	V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-15			Α	
	_	V _{GS} = - 4.5 V,I _D = - 2 A		45	50	mΩ	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	V _{GS} = - 2.5 V, I _D = - 2 A		60	70		
Forward Transconductance ^a				15		S	
Dynamic ^b							
Input Capacitance	C _{iss}			970			
Output Capacitance	C _{oss}	V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz		130		pF	
Reverse Transfer Capacitance	C _{rss}			115			
Total Gate Charge	Q _q			13			
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2 \text{ A}$		2		nC	
Gate-Drain Charge	Q _{gd}	1		3			
Gate Resistance	R _q f = 1 MHz			7		Ω	
Turn-On Delay Time	t _{d(on)}			18			
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_L = 2.8 \Omega$		20		1	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -2 \text{ A,V}_{GEN} = -4.5 \text{ V,R}_q = 1 \Omega$		58			
Fall Time	t _f			10			
Turn-On Delay Time	t _{d(on)}			8		ns	
Rise Time	t _r	$V_{DD} = -10 \text{ V, R}_{L} = 2.8 \Omega$		5		-	
Turn-Off DelayTime	$t_{d(off)}$	$I_D \cong -2 \text{ A,V}_{GEN} = -8 \text{ V,R}_g = 1 \Omega$		60			
Fall Time	t _f	1		15			
Drain-Source Body Diode Characteris	tics						
ontinous Source-Drain Diode Current I _S		T _C = 25 °C			-5.5	_	
Pulse Diode Forward Current	I _{SM}	-			-18	A	
Body Diode Voltage	V _{SD}	I _S = - 1 A, V _{GS} = 0 V		-0.5	-1	V	
Body Diode Reverse Recovery Time t _{rr}		3 33		12	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	†		5	10	nC	
Reverse Recovery Fall Time	t _a	I _F = -2 A, dl/dt = 100 A/μs, T _J = 25 °C		8			
Reverse Recovery Rise Time	t _b	 		5		ns	

Notes:

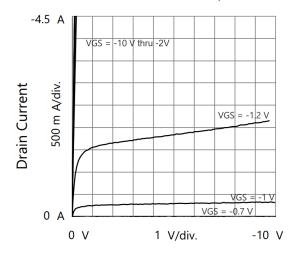
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

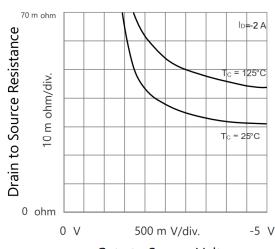


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

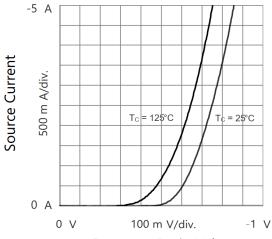
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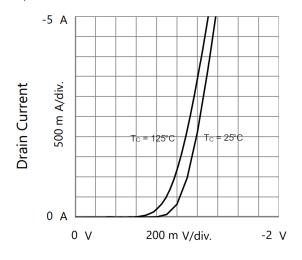
Drain to Source Voltage Output Characteristics



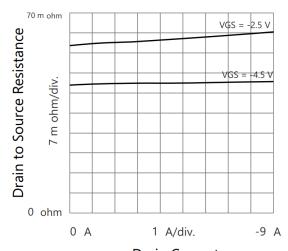
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



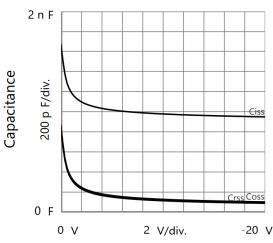
Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



Drain Current
Drain to Source Resistance vs. Drain Current



Drain to Source Voltage Capacitances

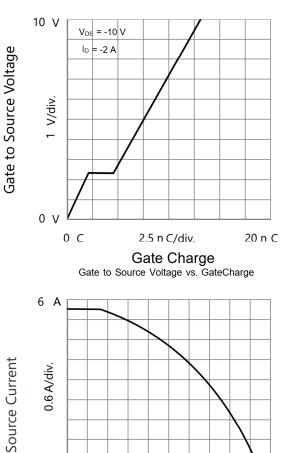


0 A

0 nC

TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

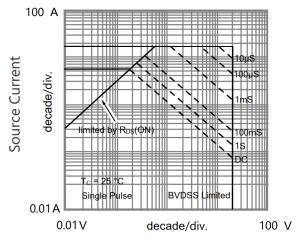
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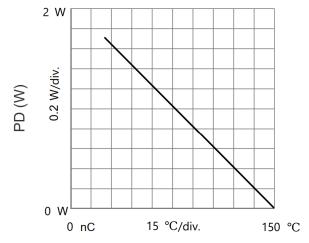
15 °C/div.

 $T_{C} \textbf{-} \textbf{Case Temperature}_{\text{Current Derating}}$





Source to Drain Voltage Safe Operating Area, Junction-to-Ambient

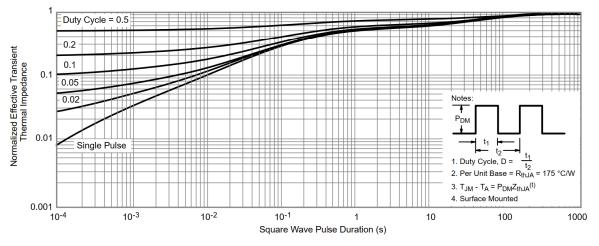


T_C - Case Temperature

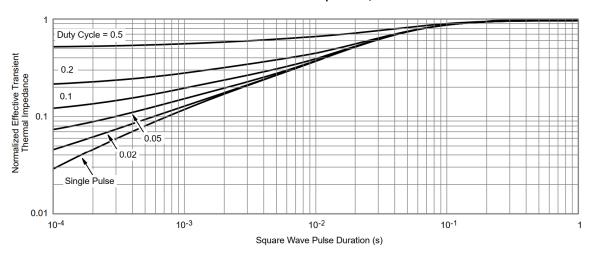
Power Derating



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



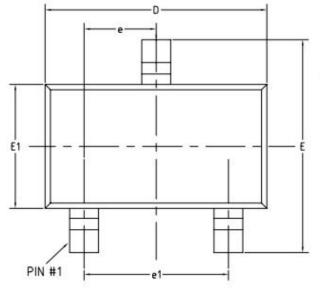
Normalized Thermal Transient Impedance, Junction-to-Ambient

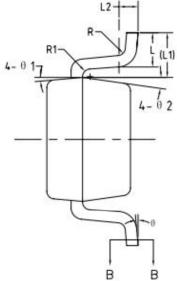


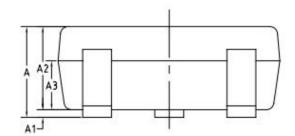
Normalized Thermal Transient Impedance, Junction-to-Foot



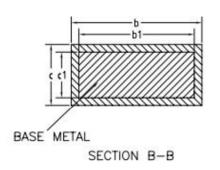
SOT-23-3L PACKAGE OUTLINE







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



SYMBOL	MIN	TYP	MAX	
Α	-	-	1.50	
A1	0.00	1	0.18	
A2	0.85	1.10	1.35	
A3	0.58	0.65	0.72	
b	0.23	1	0.53	
b1	0.20	0.40	0.50	
С	0.09	1	0.22	
c1	0.08	0.13	0.21	
D	2.78	2.95	3.10	
E	2.58	2.80	3.03	
E1	1.55	1.65	1.78	
е	0.83	0.95	1.07	
e1	1.78	1.90	2.02	
L	0.28	0.45	0.62	
L1	0.59REF			
L2	0.25BSC			
R	0.04	-	-	
R1	0.04	-	0.21	
θ	0°	-	8°	
θ1	8°	10°	12°	
θ2	8°	10°	12°	





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