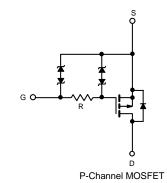


SOT-23

# P-Channel 25-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)			
- 25	$0.028 \text{ at V}_{GS} = -10 \text{ V}$	- 8 <sup>a</sup>	19 nC			
- 25	0.036 at V <sub>GS</sub> = - 4.5 V	- 6 <sup>a</sup>	19110			



### **FEATURES**

- **DT-Trench Power MOSFET**
- 100 % R<sub>g</sub> Tested Built in ESD Protection with Zener Diode
- Typical ESD Performance: 1800 V
- Compliant to RoHS Directive 2002/95/EC



### **APPLICATIONS**

- Portable Devices
  - Load Switch
  - Battery Switch
  - Charger Switch

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 25	V		
Gate-Source Voltage	V <sub>GS</sub>	v			
Continuous Drain Current (T <sub>J</sub> = 150 °C)	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$	I <sub>D</sub>	- 8 <sup>a</sup> - 6 <sup>a</sup> - 6 <sup>b, c</sup>		
Pulsed Drain Current	T <sub>A</sub> = 70 °C	I <sub>DM</sub>	- 4.7 <sup>b, c</sup> - 26	А	
Continuous Source-Drain Diode Current	$\frac{T_C = 25 \text{ °C}}{T_A = 25 \text{ °C}}$	I <sub>S</sub>	- 7 <sup>a</sup> - 3.5 <sup>b, c</sup>		
Maximum Power Dissipation	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	P <sub>D</sub>	18 11 3.3 <sup>b, c</sup> 2.1 <sup>b, c</sup>	w	
Operating Junction and Storage Temperature Rai	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature	, and the second	260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, e</sup>	t ≤ 5 s	R <sub>thJA</sub>	28	36	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	5.3	6.5	- C/ VV	

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- e. Maximum under Steady State conditions is 80 °C/W.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					L	L	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 25			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		12		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			3			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.5		- 1.5	V	
0.11. 0.1	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 20		
Gate-Source Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 0.5		
Zara Cata Valtaga Brain Current	ı	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V		- 1	- μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 19			Α	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 2.2 A		0.028	0.035	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.6 A		0.036	0.043		
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 1.5 A		0.050	0.065		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 1.6 A		35		S	
Dynamic <sup>b</sup>				<u> </u>			
Input Capacitance	C <sub>iss</sub>			1650			
Output Capacitance	C <sub>oss</sub>			826		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	50 00		201		•	
Total Gate Charge		Vpe = - 10 V, Vce = - 8 V, Ip = - 5 A		48	72		
	$Q_g$	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5 A		19	28	nC	
Gate-Source Charge	Q <sub>gs</sub>			3.3			
Gate-Drain Charge	$Q_{gd}$			8.4			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.2	1	2	kΩ	
Turn-On Delay Time	t <sub>d(on)</sub>			0.71	1.1		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 1 $\Omega$		1.7	2.6		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 5 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1		6	9		
Fall Time	t <sub>f</sub>	Ω		3.2	5		
Turn-On Delay Time	t <sub>d(on)</sub>			0.3	0.45	us	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 1 $\Omega$		0.6	0.9		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 5 A, $V_{GEN}$ = - 10 V, $R_g$ = 1		10	15		
Fall Time				3.5	5.5		
<b>Drain-Source Body Diode Characterist</b>	ics						
Continuous Source-Drain Diode Current I <sub>S</sub>		T <sub>C</sub> = 25 °C			- 8	А	
Pulse Diode Forward Current	I <sub>SM</sub>				- 26		
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = - 5 A, V <sub>GS</sub> = 0 V		- 0.85	- 1.2	V	
Body Diode Reverse Recovery Time t <sub>rr</sub>				30	60	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 6 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C		20	40	nC	
Reverse Recovery Fall Time	ta	$i_F = 0$ A, $\alpha i / \alpha i = 100$ A/ $\mu$ s, $i_J = 25$		13		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			17			

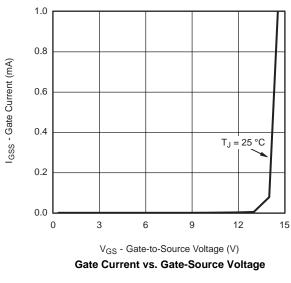
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

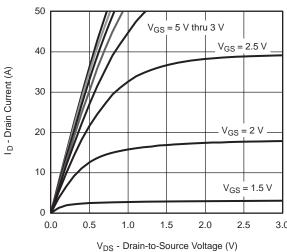
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



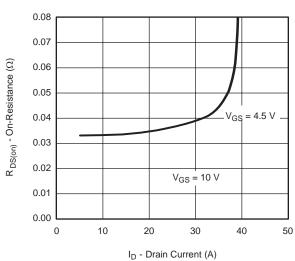
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



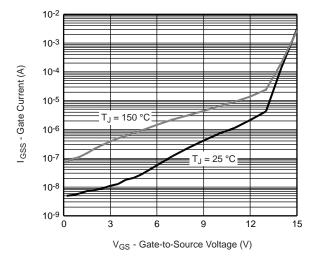
Din-Tek SEMICONDUCTOR



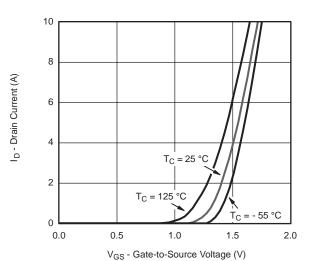
**Output Characteristics** 



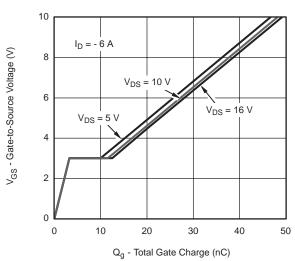
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



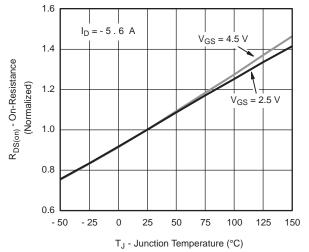
Transfer Characteristics



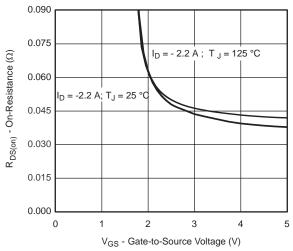
Gate Charge



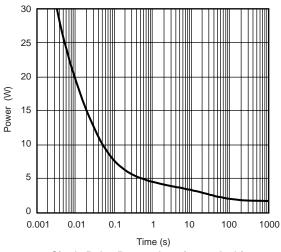
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



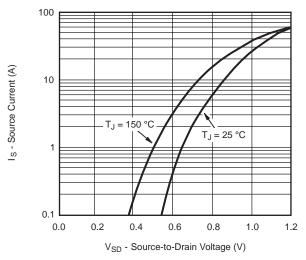
#### On-Resistance vs. Junction Temperature



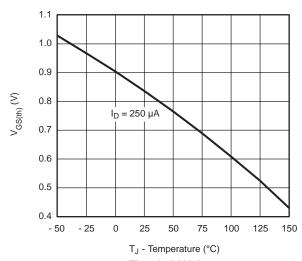
On-Resistance vs. Gate-to-Source Voltage



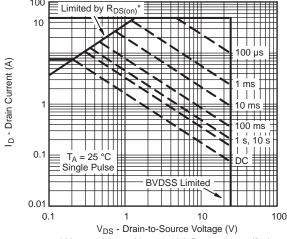
Single Pulse Power, Junction-to-Ambient



### Soure-Drain Diode Forward Voltage



Threshold Voltage



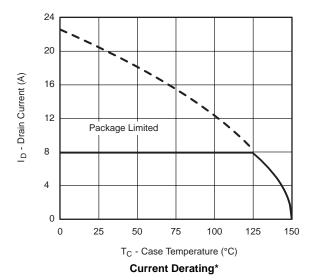
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

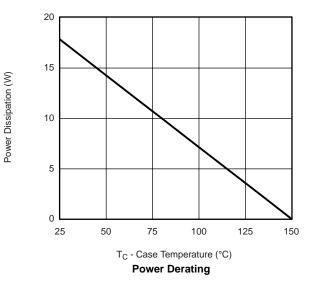
Safe Operating Area, Junction-to-Ambient



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# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



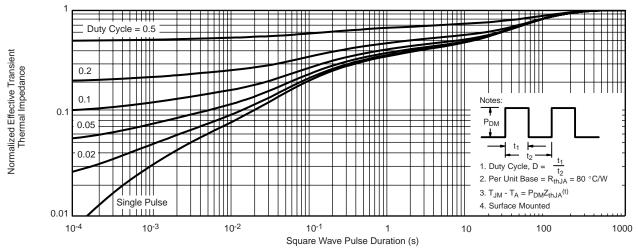


<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

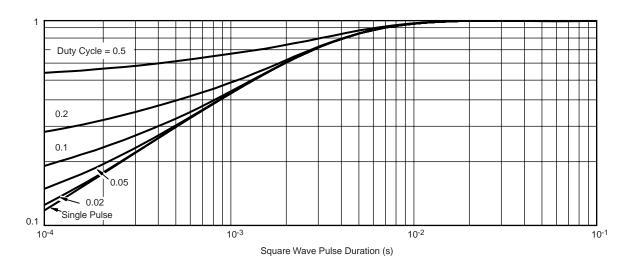


Normalized Effective Transient Thermal Impedance

## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



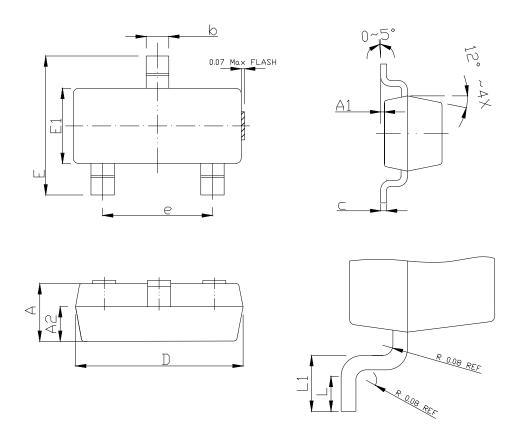
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



# SOT-23 PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER				
SIMDOL	MIN	NOM	MAX		
A	0.80	1.00	1.30		
A1	0.00	0.05	0.15		
b	0. 25	0.40	0 <b>. 5</b> 5		
С	0.11 BSC				
D	2 <b>.6</b> 0	2.90	3 <b>. 2</b> 0		
Е	2.10	2.40	2.70		
E1	1.10	1.30	1.48		
е	1.90 BSC				
L	0.17	_	_		
L1	0. 28	0.40	0.53		
A2	0.60 REF				





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