

# N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>e</sup>	Q <sub>g</sub> (Typ.)			
	0.015 at V <sub>GS</sub> = 4.5 V	6.5 <sup>a</sup>				
20	0.020 at V <sub>GS</sub> = 2.5 V	4.8 <sup>a</sup>	8.6nC			
	0.024 at V <sub>GS</sub> = 1.8 V	3.9				

#### **FEATURES**

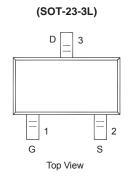
- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

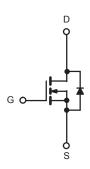


ROHS

#### **APPLICATIONS**

- · DC/DC Converters
- · Load Switch for Portable Applications





N-Channel MOSFET

ABSOLUTE MAXIMUM RATIN	<b>IGS</b> 1 <sub>A</sub> = 25 °C,		1		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		$V_{DS}$	20	V	
Gate-Source Voltage		$V_{GS}$	± 12		
	T <sub>C</sub> = 25 °C	I <sub>D</sub>	6.5 <sup>a</sup>		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		6		
Continuous Diam Current (1) = 130 °C)	T <sub>A</sub> = 25 °C		5.5 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		5 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	24	1	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		3.95		
	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	1.16 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		2.5		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	1.7	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		1.45 <sup>b, c</sup>	¬	
	T <sub>A</sub> = 70 °C	1	1.1 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature)			260		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	70	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	30	60	C/VV		

#### Notes:

- a. Package limited
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 125 °C/W.
- e. Based on  $T_C$  = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		25		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1 <sub>D</sub> = 230 μΛ		- 2.6			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	0.45		1.0	V	
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	4	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	20			Α	
	, ,	$V_{GS} = 4.5 \text{ V}, I_D = 5.0 \text{ A}$		0.015	0.019		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 5 \text{ A}$		0.020	0.024	Ω	
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 4.5 A		0.024	0.030		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5.0 A		25		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			860		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		95			
Reverse Transfer Capacitance	C <sub>rss</sub>			59			
·		$V_{DS} = 10 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 5.0 \text{ A}$		11	19		
Total Gate Charge	Qg			8.9	15		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5.0 \text{ A}$		1.1		nC -	
Gate-Drain Charge	$Q_{gd}$			0.7			
Gate Resistance	$R_{g}$	f = 1 MHz	0.5	2.3	4.9	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			8	17		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 2.2 $\Omega$		19	25		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 4$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		30	45		
Fall Time	t <sub>f</sub>			9	19	ns	
Turn-On Delay Time	t <sub>d(on)</sub>			6	10	113	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 2.2 $\Omega$		14	20		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 4 A, $V_{GEN}$ = 5 V, $R_g$ = 1 $\Omega$		20	32		
Fall Time	t <sub>f</sub>			7	12	1	
<b>Drain-Source Body Diode Characteristic</b>	s						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			6.5		
Pulse Diode Forward Current	I <sub>SM</sub>				24	A	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 4 A, V <sub>GS</sub> = 0 V		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			13	21	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 4 A dl/dt = 100 A/vo T = 25 °C		6	10	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 4 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		8		1	
Reverse Recovery Rise Time	t <sub>b</sub>			5		ns	

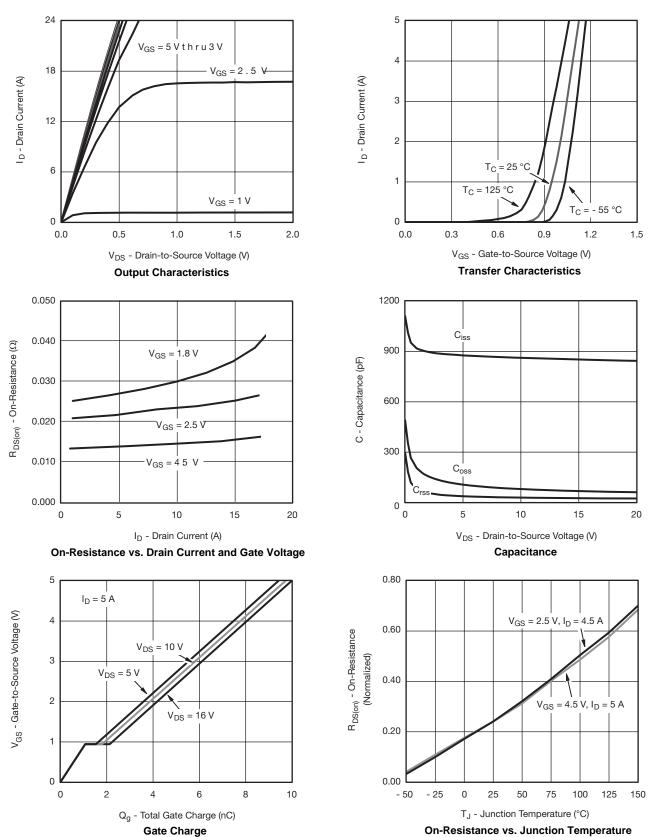
#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 % b. Guaranteed by design, not subject to production testing.

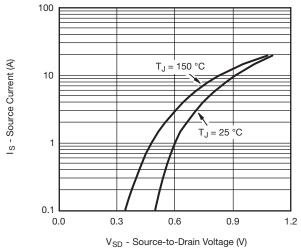


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

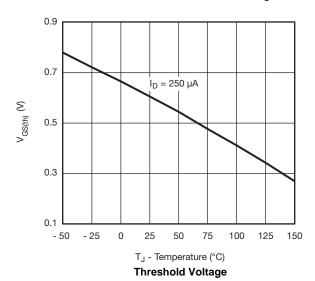


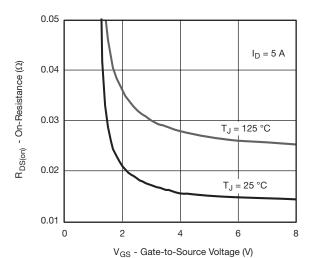


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



#### Source-Drain Diode Forward Voltage

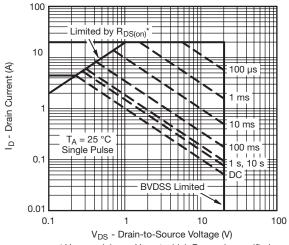




On-Resistance vs. Gate-to-Source Voltage



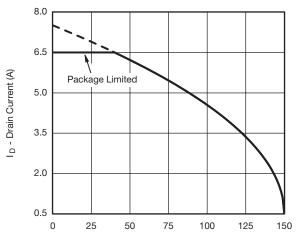
Single Pulse Power (Junction-to-Ambient)



 $^{\star}$   $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

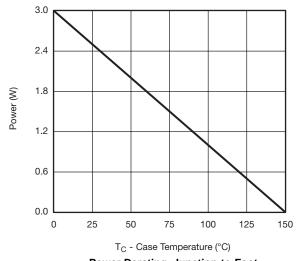
Safe Operating Area, Junction-to-Ambient

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

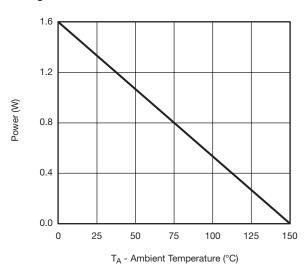


T<sub>C</sub> - Case Temperature (°C)

#### **Current Derating\***



Power Derating, Junction-to-Foot



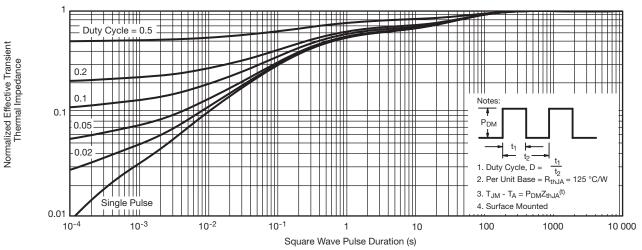
Power Derating, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max.)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

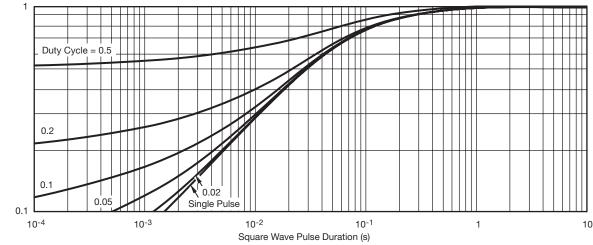


Normalized Effective Transient Thermal Impedance

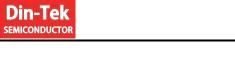
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



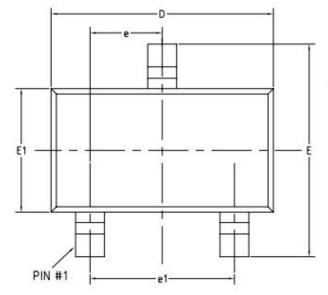
Normalized Thermal Transient Impedance, Junction-to-Ambient

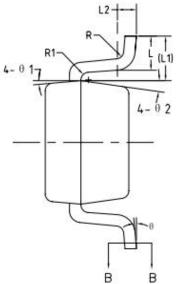


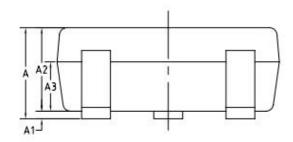
Normalized Thermal Transient Impedance, Junction-to-Foot

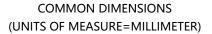


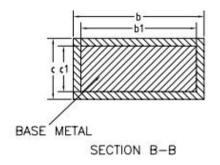
### SOT-23-3L PACKAGE OUTLINE











SYMBOL	MIN	TYP	MAX	
Α	ı	-	1.50	
A1	0.00	-	0.18	
A2	0.85	1.10	1.35	
A3	0.58	0.65	0.72	
b	0.23	-	0.53	
b1	0.20	0.40	0.50	
С	0.09	-	0.22	
c1	0.08	0.13	0.21	
D	2.78	2.95	3.10	
E	2.58	2.80	3.03	
E1	1.55	1.65	1.78	
е	0.83	0.95	1.07	
e1	1.78	1.90	2.02	
L	0.28	0.45	0.62	
L1	0.59REF			
L2	0.25BSC			
R	0.04	-	-	
R1	0.04	-	0.21	
θ	0°	-	8°	
θ 1	8°	10°	12°	
θ2	8°	10°	12°	





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