

N-Channel 250 V (D-S) MOSFET

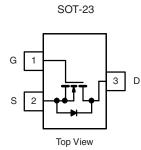
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
250	1.4 at V _{GS} = 10 V	0.6		
	1.5 at V _{GS} = 4.5 V	0.6		

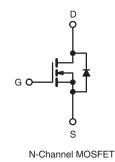
FEATURES

- DT-Trench Power MOSFET ٠
- 100 % R_g and UIS Tested Small package outline •
- ٠

APPLICATIONS

- · Load switch
- Power management for mobile computing ٠





ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	250	V	
Gate-Source Voltage		V _{GS}	± 20	7 V	
Continuous Drain Current (T _{.1} = 150 °C) ^a	T _A = 25 °C	1-	0.6		
Continuous Drain Current (1) = 150 C)	T _A = 70 °C		0.5	А	
Pulsed Drain Current ^b		I _{DM}	2.4		
Continuous Source Current (Diode Conduction) ^a		۱ _S	0.6	A	
Power Dissipation ^a	T _A = 25 °C	P _D	1.26	w	
	T _A = 70 °C	' D	0.81		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 5 s	R _{thJA}	75	100		
Maximum Sunction-to-Ambient	Steady State		125	170	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	40	55		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

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			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						-	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 1 mA$	250	250		v	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0		2.3	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zava Cata Valtaga Drain Current		$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}$	1				
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 200 V, V_{GS} = 0 V, T_{J} = 70 °C			75	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 15 \text{ V}, V_{GS} = 10 \text{ V}$	0.6			Α	
Drain-Source On-Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.3 \text{ A}$		1.4	4	Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 0.3 \text{ A}$		1.5		S	
Diode Forward Voltage	V _{SD}	I _S = 1 A, V _{GS} = 0 V		0.7	1.2	V	
Dynamic ^b						-	
Total Gate Charge	Qg			5.5	8	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 125 V, V_{GS} = 10 V, I_D = 0.3 A		0.9			
Gate-Drain Charge	Q _{gd}			1.35		1	
Gate Resistance	Rg			1.3		Ω	
Switching			·		·		
Input Capacitance	C _{iss}			165			
Output Capacitance	C _{oss}	$V_{GS} = 0 V, V_{DS} = 125 V, f = 1 MHz$		32		1	
Reverse Transfer Capacitance	C _{rss}			11		pF	
Turn-On Delay Time	t _{d(on)}			4.2			
Rise Time	t _r	V_{DD} = 125 V, R_L = 30 Ω		10		ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 0.3 \text{ Å}, V_{GEN} = 10 \text{ V}, \text{ R}_g = 6 \Omega$		19			
Fall Time	t _f			13			

Notes:

a. Pulse test: PW \leq 300 μ s, duty cycle \leq 2 %.

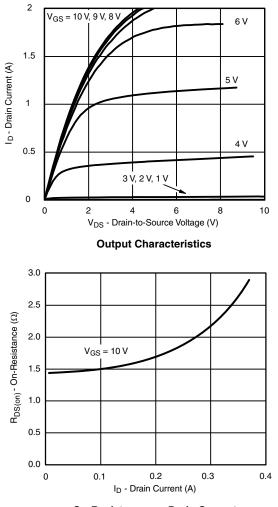
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

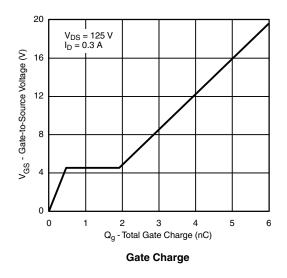


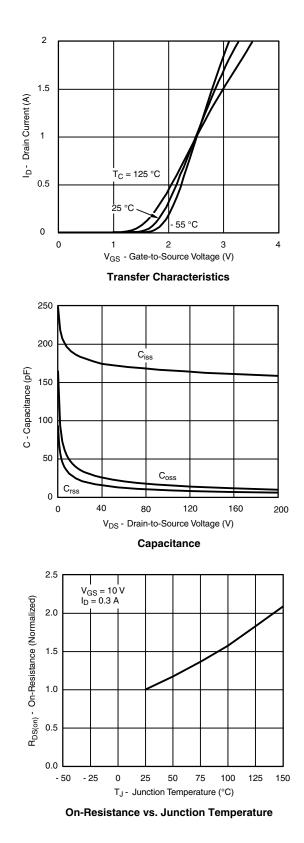
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

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On-Resistance vs. Drain Current

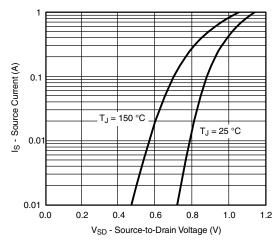




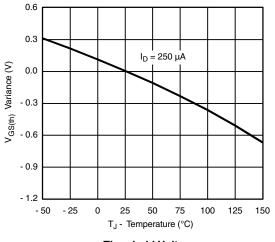


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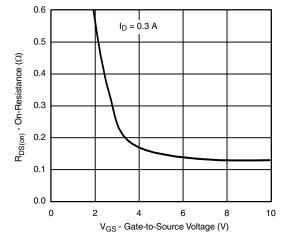
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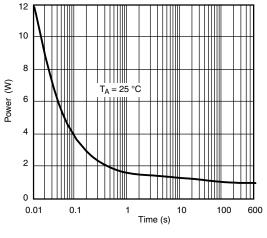
Source-Drain Diode Forward Voltage



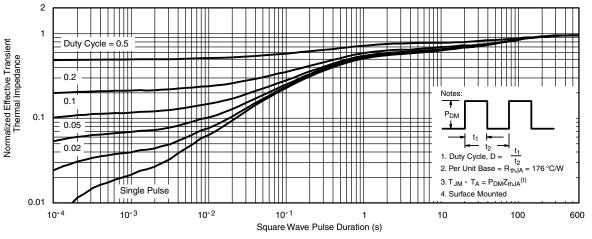
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power

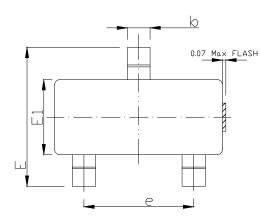


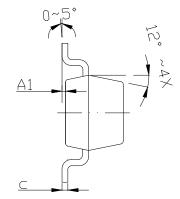
Normalized Thermal Transient Impedance, Junction-to-Ambient

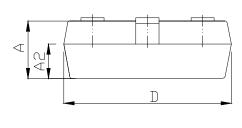


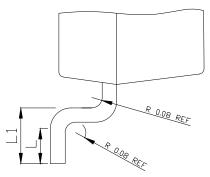
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SOT-23 PACKAGE OUTLINE









COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER				
	MIN	NOM	MAX		
А	0.80	1.00	1.30		
A1	0.00	0.05	0.15		
b	0.25	0.40	0.55		
С	0.11 BSC				
D	2 . 6 0	2.90	3.20		
Е	2.10	2.40	2.70		
E1	1.10	1.30	1.48		
е	1.90 BSC				
L	0.17	—	_		
L1	0.28	0.40	0.53		
A2	0.60 REF				



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