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P-Channel 30 V (D-S) MOSFET

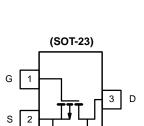
MOSFET PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω) (Typ.)	I _D (A) ^a	Q _g (Typ.)		
- 30	0.042 at V _{GS} = - 10 V	- 4.5	7.1 nC		
- 30	0.056 at V _{GS} = - 4.5 V	- 3.4	7.1110		

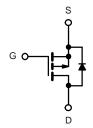
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested

APPLICATIONS

- · Load Switch
- Notebook Adaptor Switch
- DC/DC Converter
- Power Management





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (TA	$_{\lambda}$ = 25 °C, unless oth	nerwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	- 30	V	
Gate-Source Voltage		V_{GS}	± 20	v
	T _C = 25 °C		- 4.5	
Continuous Drain Current (T ₁ = 150 °C)	T _C = 70 °C	I _D	- 3.5	
Continuous Brain Current (1) = 100 °C)	T _A = 25 °C		- 4.2 ^{b,c}	
	T _A = 70 °C		- 3.5 ^{b,c}	Α
Pulsed Drain Current (t = 300 μs)		I _{DM}	- 22	
Continuous Source-Drain Diode Current	T _C = 25 °C	la	- 4.5	
Continuous Source-Diain Diode Current	T _A = 25 °C	I _S	- 0.85 ^{b,c}	
	T _C = 25 °C		2.0	
Maximum Power Dissipation	T _C = 70 °C	P _D	1.28	l w
iviaxiiiiuiii Fowei Dissipatioli	T _A = 25 °C	ט י	1.43 ^{b, c}	\ \v
	T _A = 70 °C		0.92 ^{b, c}	1
Operating Junction and Storage Temperature Range	T _J , T _{sta}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	90	120	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	55	70	C/VV	

Notes

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 175 °C/W.



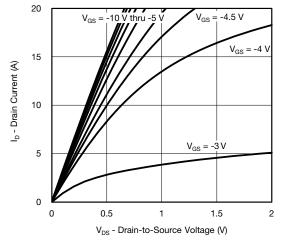
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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		- 25		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		3.9		miv/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.8		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valtana Busin Comment	I _{DSS}	V _{DS} = - 24 V, V _{GS} = 0 V		- 1			
Zero Gate Voltage Drain Current		V _{DS} = - 24 V, V _{GS} = 0 V, T _J = 55 °C			- 10	<u>μ</u> Α	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 10 V	- 4.5			Α	
	Б	V _{GS} = - 10 V, I _D = - 3.8 A		0.042	0.058	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 3 A		0.056	0.084		
Forward Transconductance ^a	g _{fs}	V _{DS} = - 5 V, I _D = - 3.8 A		10		S	
Dynamic ^b							
Input Capacitance	C _{iss}			715			
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		95		pF	
Reverse Transfer Capacitance	C _{rss}			70			
Total Gate Charge	Q _g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 4 A		14.5	22	22 10.4 nC	
				7.1	10.4		
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4 \text{ A}$		2.3			
Gate-Drain Charge	Q_{gd}			2.1			
Gate Resistance	R_g	f = 1 MHz	1.7	8.3	17	Ω	
Turn-On Delay Time	t _{d(on)}			6	12		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 5 Ω		6	12	ns	
Turn-Off Delay Time	t _{d(off)}	I_D = - 3 A, V_{GEN} = - 10 V, R_G = 1 Ω		19	29		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 5 Ω		9	18	ns	
Turn-Off Delay Time	t _{d(off)}	I_D = -3 A, V_{GEN} = -6 V, R_G = 1 Ω		18	27		
Fall Time	t _f			7	14		
Drain-Source Body Diode Characterist	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 4.5		
Pulse Diode Forward Current ^a	I _{SM}				- 22	A	
Body Diode Voltage	V_{SD}	I _S = - 3 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			13	20	ns	
ody Diode Reverse Recovery Charge 0				5	10	nC	
Reverse Recovery Fall Time	Ic = - 3 A. gl/dt = 100			8			
Reverse Recovery Rise Time	t _b			6		ns	

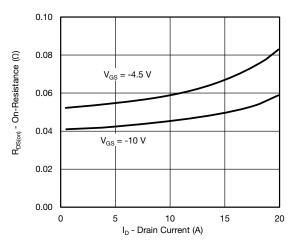
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Notes: a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

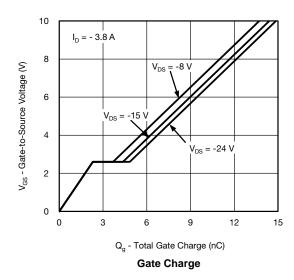


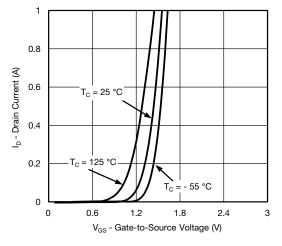


Output Characteristics

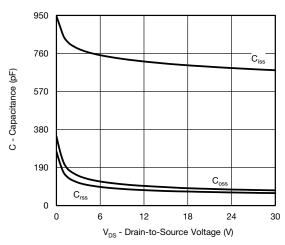


On-Resistance vs. Drain Current and Gate Voltage

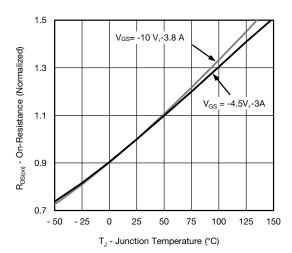




Transfer Characteristics

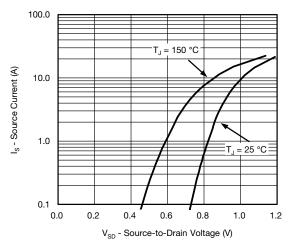


Capacitance

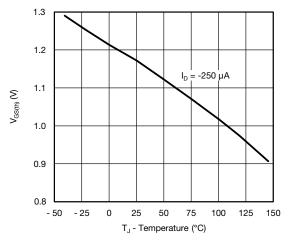


On-Resistance vs. Junction Temperature

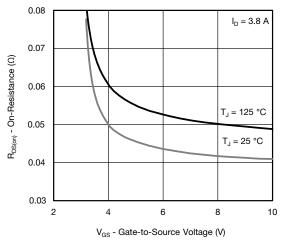




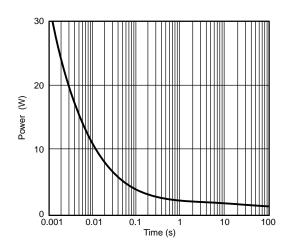
Source-Drain Diode Forward Voltage



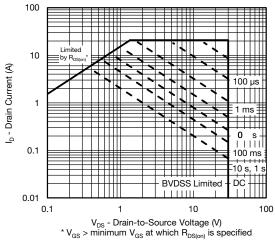
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

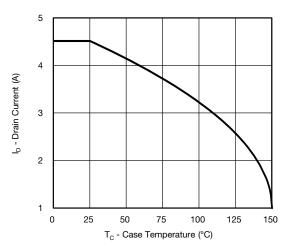


Single Pulse Power

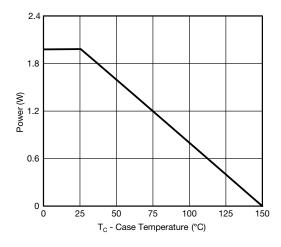


Safe Operating Area

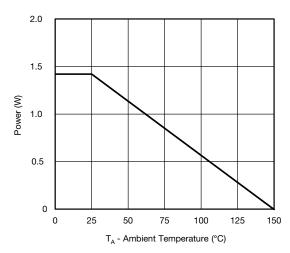




Current Derating*



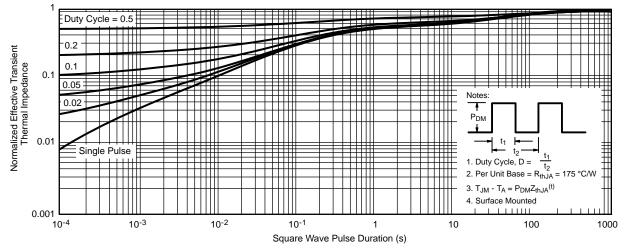




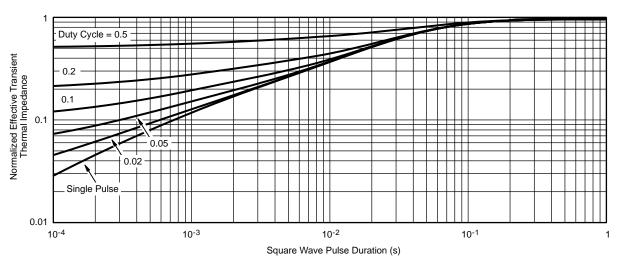
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





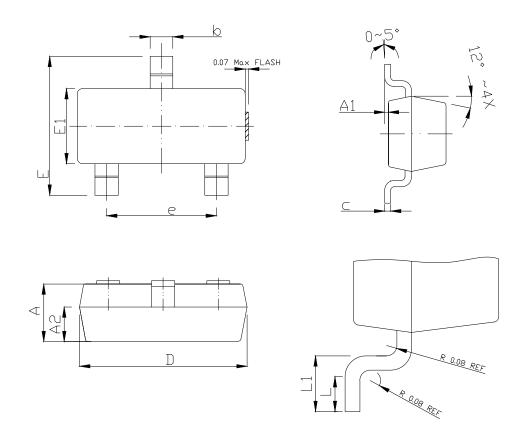
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-23 PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER			
	MIN	NOM	MAX	
A	0.80	1.00	1.30	
A1	0.00	0.05	0.15	
b	0. 25	0.40	0.55	
С	0.11 BSC			
D	2 .6 0	2.90	3 . 2 0	
Е	2.10	2.40	2.70	
E1	1. 10	1.30	1.48	
е	1.90 BSC			
L	0. 17	_	_	
L1	0. 28	0.40	0.53	
A2	0.60 REF			





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