

## P-Channel 30-V (D-S) MOSFET

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PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a, b</sup>	Q <sub>g</sub> (Typ.)			
- 30	0.088 at V <sub>GS</sub> = - 10 V	- 2.7	4.1 nC			
- 30	0.130 at V <sub>GS</sub> = - 4.5 V	- 2.2	4.1110			

#### **FEATURES**

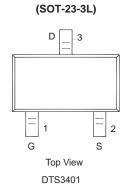
• DT-Trench Power MOSFET

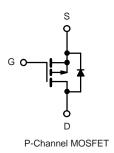


RoHS COMPLIANT

## 1 nC APPLICATIONS

· Load Switch for Portable Devices





Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		- 3.5		
0 11 0 17 17 17 10 10 10 h	T <sub>C</sub> = 70 °C		- 2.8		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 2.7 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		- 2.2 <sup>a, b</sup>	Α	
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	- 12		
a i a a a a a a a	T <sub>C</sub> = 25 °C	1	- 1.5		
Continuous Source-Drain Diode Current <sup>a, b</sup>	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 0.91 <sup>a, b</sup>		
	T <sub>C</sub> = 25 °C		1.8		
	T <sub>C</sub> = 70 °C	В	1.14	10/	
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.1 <sup>a, b</sup>	W	
	T <sub>A</sub> = 70 °C		0.7 <sup>a, b</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	00	
Soldering Recommendations (Peak Temperature)		260	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 5 s	R <sub>thJA</sub>	90	115	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	55	70	- C/VV	

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. t = 5 s

c. Maximum under Steady State conditions is 166 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•			•	•		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, } I_{D} = -250  \mu\text{A}$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 32		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η η η η η η η η η η η η η η η η η η η		4.5			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1		- 3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			- 100	nA	
Zoro Coto Voltogo Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, V_{GS} = -10 \text{ V}$	- 6			Α	
	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3.5 A		0.073	0.088	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -2.5 \text{ A}$		0.110	0.138	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 3.5 A		7		S	
Dynamic <sup>b</sup>	·			1	•		
Input Capacitance	C <sub>iss</sub>			340			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		67		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			51			
Total Gate Charge	Qg			4.1	6.2	nC	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2.5 A		1.3			
Gate-Drain Charge	Q <sub>gd</sub>			1.8			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		10		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			40	60		
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 15 \Omega$		40	60		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		20	40		
Fall Time	t <sub>f</sub>	]		17	30		
Turn-On Delay Time	t <sub>d(on)</sub>			5.5	10	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 15 \Omega$		13	25		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 Ω		17	30	1	
Fall Time	t <sub>f</sub>	]		7.7	15		
Drain-Source Body Diode Characterist	ics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 1.5	А	
Pulse Diode Forward Current	I <sub>SM</sub>				- 12		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 0.75 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time t <sub>rr</sub>				17	30	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$Q_{rr}$ $I_F = -2.5 \text{ A, dl/dt} = 100 \text{ A/µs, T}_J = 25 °C$		11	20	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	1		12		ns	
Reverse Recovery Rise Time	t <sub>b</sub>	]		5			

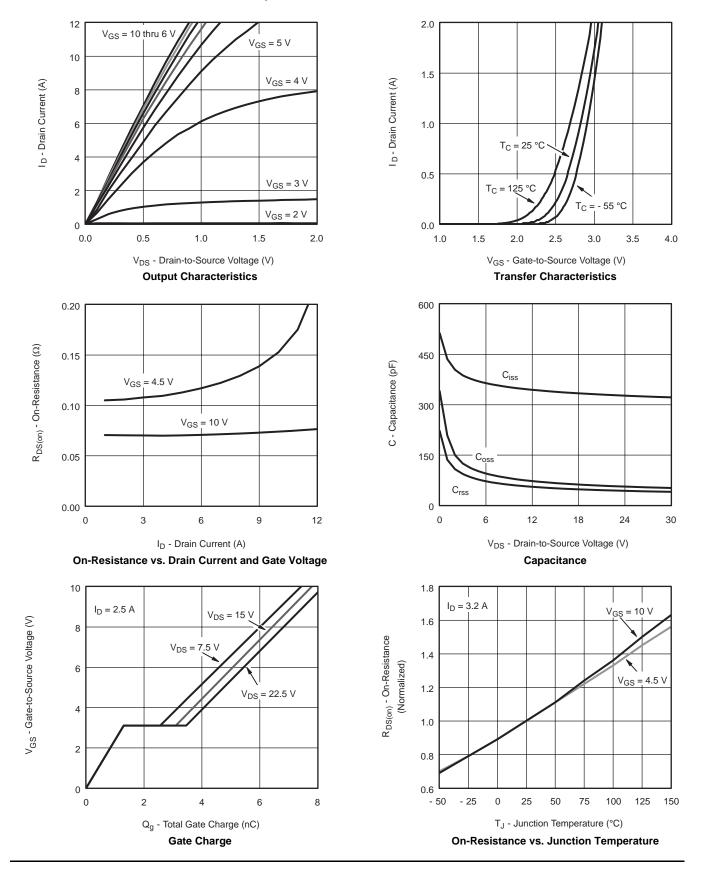
#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

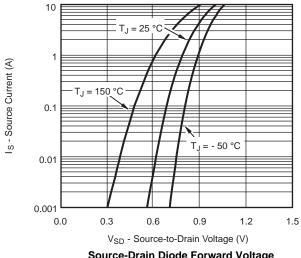


#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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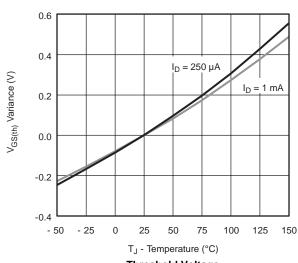
## $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - On-Resistance $(\Omega)$ 0.3 0.2 $T_J = 125 \, ^{\circ}C$ 0.1 T<sub>J</sub> = 25 °C 0.0 0 8 10

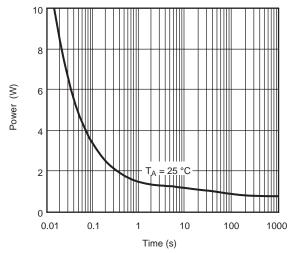
0.4

I<sub>D</sub> = 3.5 A

#### Source-Drain Diode Forward Voltage

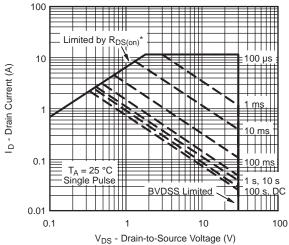






#### **Threshold Voltage**

Single Pulse Power, Junction-to-Ambient

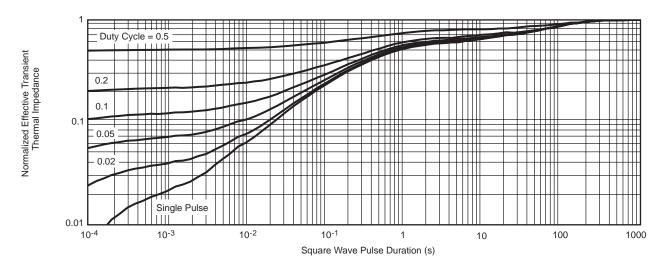


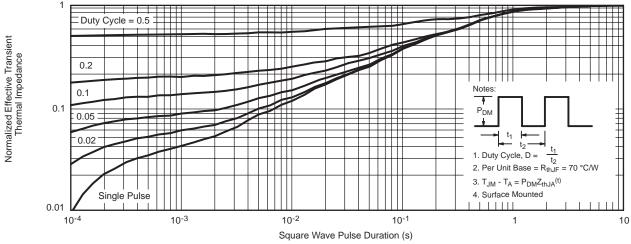
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Ambient



#### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)





Normalized Thermal Transient Impedance, Junction-to-Foot

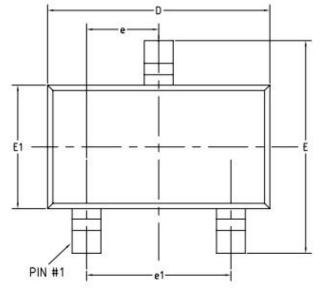
#### Note

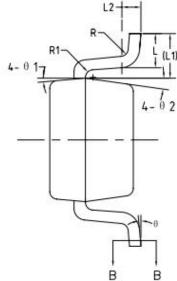
- The characteristics shown in the two graphs
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Foot (25  $^{\circ}\mathrm{C}$  )

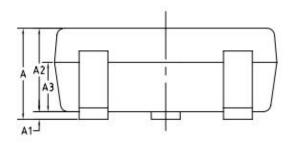
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

### SOT-23-3L PACKAGE OUTLINE

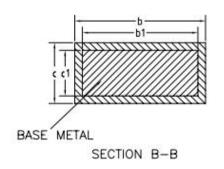
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# COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



SYMBOL	MIN	TYP	MAX
Α	ı	ı	1.50
A1	0.00	-	0.18
A2	0.85	1.10	1.35
A3	0.58	0.65	0.72
b	0.23	-	0.53
b1	0.20	0.40	0.50
С	0.09	-	0.22
c1	0.08	0.13	0.21
D	2.78	2.95	3.10
E	2.58	2.80	3.03
E1	1.55	1.65	1.78
е	0.83	0.95	1.07
e1	1.78	1.90	2.02
L	0.28	0.45	0.62
L1	0.59REF		
L2	0.25BSC		
R	0.04	-	-
R1	0.04	1	0.21
θ	0°	=	8°
θ1	8°	10°	12°
θ2	8°	10°	12°





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