

# P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ ) Max.	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
- 30	0.036 at V <sub>GS</sub> = - 10 V	- 5.6			
	0.039 at V <sub>GS</sub> = - 6 V	- 5	11.4 nC		
	0.044 at V <sub>GS</sub> = - 4.5 V	-4.5			

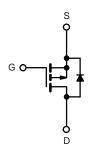
#### **FEATURES**

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested



#### **APPLICATIONS**

- For Mobile Computing
  - Load Switch
  - Notebook Adaptor Switch
  - DC/DC Converter



P-Channel MOSFET

(SOT-23-3L)					
D 3					
1 G					
Top View					

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		$V_{GS}$	± 20	v	
	T <sub>C</sub> = 25 °C		- 5.6		
Continuous Danis Compant /T 450 9C)	T <sub>C</sub> = 70 °C		- 5.1		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 5.4 <sup>b,c</sup>		
	T <sub>A</sub> = 70 °C		- 4.3 <sup>b,c</sup>	Α	
Pulsed Drain Current (t = 100 μs)		I <sub>DM</sub>	- 18		
0 11 0 0 0 1	T <sub>C</sub> = 25 °C		- 2.1		
Continous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	- 1 <sup>b,c</sup>		
	T <sub>C</sub> = 25 °C		2.5		
Mariana Paran Dissination	T <sub>C</sub> = 70 °C	<u></u>	1.6	10/	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.25 <sup>b,c</sup>	W	
	T <sub>A</sub> = 70 °C	1	0.8 <sup>b,c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b,d</sup>	t ≤ 5 s	R <sub>thJA</sub>	75	100	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	40	50	C/VV

#### Notes:

- a. Based on  $T_C$  = 25 °C. b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 166 °C/W.



<b>SPECIFICATIONS</b> ( $T_J = 25  ^{\circ}\text{C}$ , Parameter	Symbol	Test Conditions	Min.	Tvn	Max.	Unit	
Static	Зушьог	rest Conditions	WIII.	Тур.	wax.	Unit	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 30			V	
	ΔV <sub>DS</sub> /T <sub>J</sub>	VGS = 0 V, ID = 230 μA	- 30	- 19		V	
V <sub>DS</sub> Temperature Coefficient		I <sub>D</sub> = - 250 μA				mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	)/	4.0	4	0.5		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = -250 \mu\text{A}$	- 1.2		- 2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V				- 1 μΑ	
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 5	μ, ,	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 2.5			Α	
		$V_{GS} = -10 \text{ V}, I_D = -4.4 \text{ A}$		0.034	0.036		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =- 6 V, I <sub>D</sub> = - 4 A		0.038	0.039	Ω	
		$V_{GS} = -4.5 \text{ V}, I_D = -3.6 \text{ A}$		0.043	0.044		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.4 A		18		S	
Dynamic <sup>b</sup>						•	
Input Capacitance	C <sub>iss</sub>			1295			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		150		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			130			
·		V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5.4 A		24	36	nC	
Total Gate Charge	$Q_g$	23 / 63 / 5		11.4	17		
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.4 A		3.4			
Gate-Drain Charge	Q <sub>gd</sub>	103 10 1, 103 110 1, 10		3.8			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	1.5	7.7	15.4	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	20		
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V, R}_{1} = 3.5 \Omega$		4	8	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \simeq -4.3 \text{ A, V}_{GEN} = -10 \text{ V, R}_q = 1 \Omega$		38	57		
Fall Time		GEN , GEN		6	12		
Turn-On Delay Time				28	42	ns	
Rise Time	t <sub>d(on)</sub>	V 45 V D 25 O		16	24	-	
		$V_{DD} = -15 \text{ V}, R_L = 3.5 \Omega$ $I_D \cong -4.3 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_q = 1 \Omega$					
Turn-Off Delay Time	t <sub>d(off)</sub>	ID = -4.0  A,  VGEN = -4.0  V,  Ng = 1.32		30	45	_	
Fall Time	t <sub>f</sub>			10	20		
Drain-Source Body Diode Characteristic	_	T - 25 °C		l	0.4		
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.1	Α	
Pulse Diode Forward Current (t = 100 μs)	I <sub>SM</sub>	1 424 1			- 18		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 4.3 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			15	23	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 4.3 A, dl/dt = 100 A/μs, T <sub>.1</sub> = 25 °C		7	14	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	, , , , , , , , , , , , , , , , , , , ,		8		ns	
Reverse Recovery Rise Time	$t_b$			7			

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

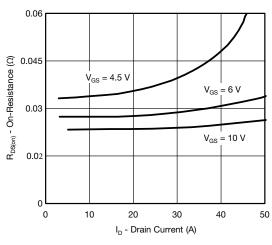
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

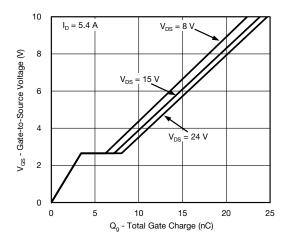




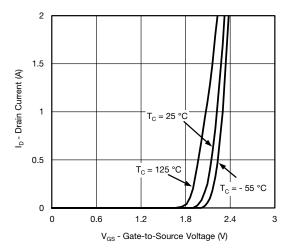
#### **Output Characteristics**



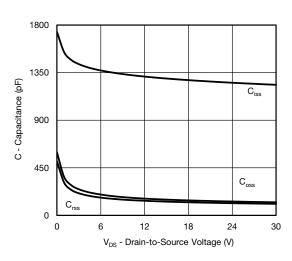
On-Resistance vs. Drain Current



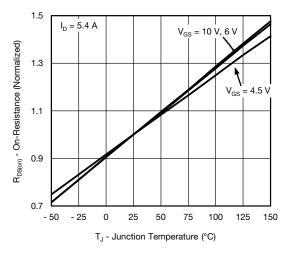
**Gate Charge** 



**Transfer Characteristics** 

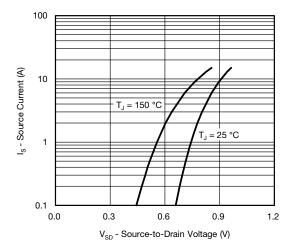


Capacitance

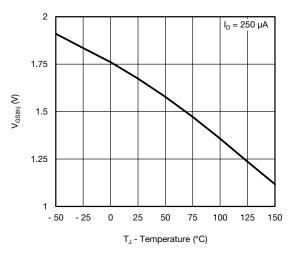


On-Resistance vs. Junction Temperature

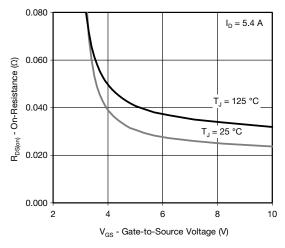




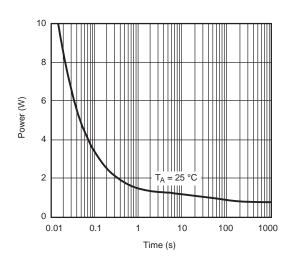
#### Source-Drain Diode Forward Voltage



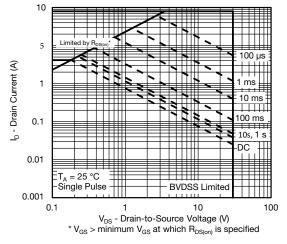
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage

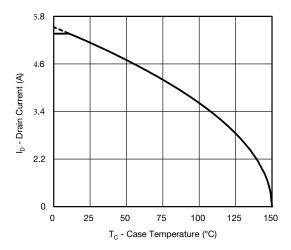


Single Pulse Power (Junction-to-Ambient)

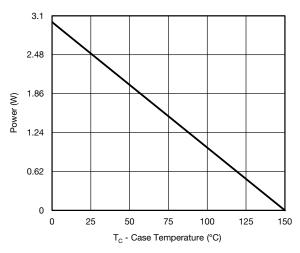


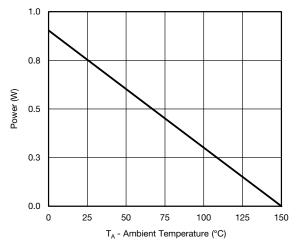
Safe Operating Area, Junction-to-Ambient





#### **Current Derating\***



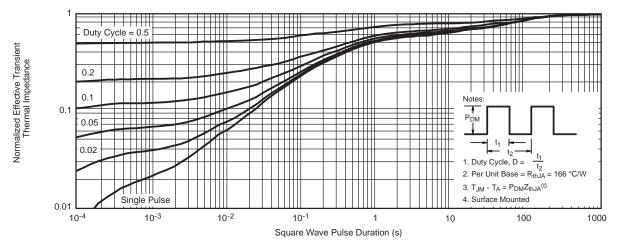


Power, Junction-to-Foot

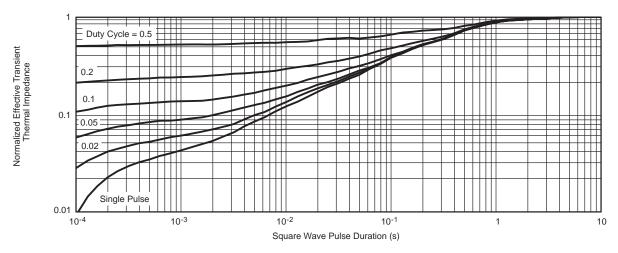
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max.)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





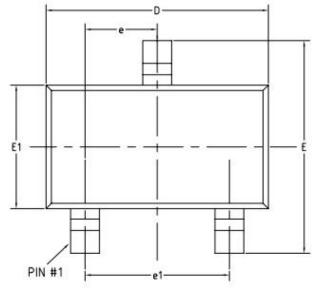
Normalized Thermal Transient Impedance, Junction-to-Ambient

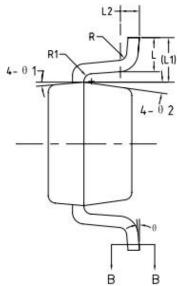


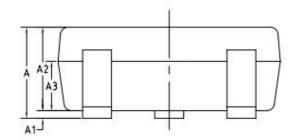
Normalized Thermal Transient Impedance, Junction-to-Foot

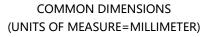


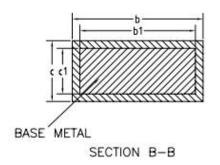
# SOT-23-3L PACKAGE OUTLINE











SYMBOL	MIN	TYP	MAX		
Α	1	1	1.50		
A1	0.00	-	0.18		
A2	0.85	1.10	1.35		
A3	0.58	0.65	0.72		
b	0.23	-	0.53		
b1	0.20	0.40	0.50		
С	0.09	=	0.22		
c1	0.08	0.13	0.21		
D	2.78	2.95	3.10		
E	2.58	2.80	3.03		
E1	1.55	1.65	1.78		
е	0.83	0.95	1.07		
e1	1.78	1.90	2.02		
L	0.28	0.45	0.62		
L1	0.59REF				
L2	0.25BSC				
R	0.04				
R1	0.04	-	0.21		
θ	0°	=	8°		
θ1	8°	10°	12°		
θ2	8°	10°	12°		





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