

# P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 20	38 at $V_{GS} = -4.5 \text{ V}$	- 5.5	8.5 nC			
	45 at V <sub>GS</sub> = - 2.5 V	- 5.5				

#### **FEATURES**

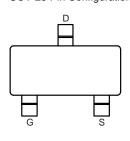
- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested



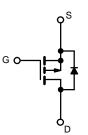
### **APPLICATIONS**

· Load Switch for Portable Devices

#### SOT-23 Pin Configuration



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub>	,	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V <sub>DS</sub>	- 20			
Gate-Source Voltage	$V_{GS}$	± 12	V		
Continuous Prain Current /T 150 °C\2	T <sub>C</sub> = 25 °C		- 5.5		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>C</sub> = 70 °C	l <sub>D</sub>	- 4.6		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	- 20	- A	
Single Pulse Avalanche Energy		E <sub>AS</sub>	5.8	mJ	
Maximum Daylor Dissination(	T <sub>A</sub> = 25 °C	Б	1.8	W	
Maximum Power Dissipation <sup>c</sup>	T <sub>A</sub> = 70 °C	P <sub>D</sub>	1.1		
Operating Junction and Storage Temperature Rang	e	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYPICAL	UNIT		
Junction-to-Ambient <sup>d</sup>	R <sub>thJA</sub>	59	°C/W		

#### Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R<sub>8JA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static		•				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0, I <sub>D</sub> = - 250 μA	- 20			V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.4		- 1.2	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	^
Zero Gate voltage Drain Current		V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 50	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -4.5 \text{ V}, V_{GS} = -5 \text{ V}$	- 5.8			Α
	D	V <sub>GS</sub> = - 4.5V, I <sub>D</sub> = - 3 A		38	45	mΩ
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 3 A		45	58	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 3 A		6.6		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			577		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		70		
Reverse Transfer Capacitance	C <sub>rss</sub>	1		56		
Total Gate Charge	Q <sub>g</sub>			8.5		nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3 \text{ A}$		1.3		
Gate-Drain Charge	Q <sub>gd</sub>	1		2		
Gate Resistance	R <sub>q</sub>	f = 1 MHz		40		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			15		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_1 = 1.5 \Omega$		16		- ns
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -3 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		25		
Fall Time	t <sub>f</sub>	-		11		
Drain-Source Body Diode Characterist	ics					
Continous Source-Drain Diode Current	I <sub>S</sub>	<u> </u>			- 5.5	A
Pulse Diode Forward Current	I <sub>SM</sub>				- 20	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1 A			- 1	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			15		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 3 A, dl/dt = 100 A/µs, T <sub>.I</sub> = 25 °C		5		nC
Reverse Recovery Fall Time t <sub>a</sub>		$\frac{1}{1} = \frac{1}{1} = \frac{1}$		9		ns
Reverse Recovery Rise Time	t <sub>b</sub>	7		15		113

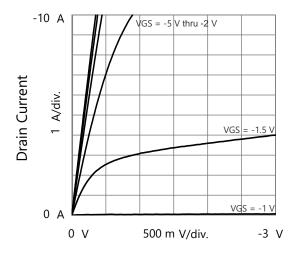
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

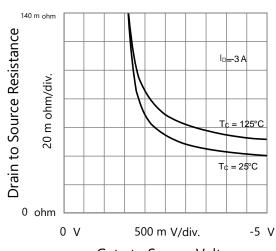
b. Guaranteed by design, not subject to production testing.



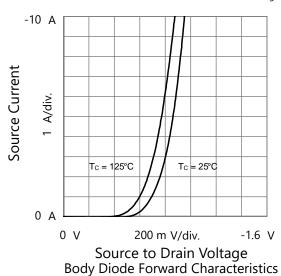
#### TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

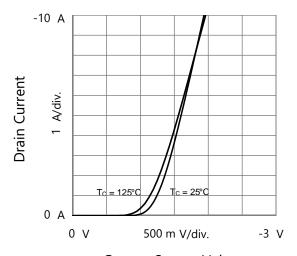


Drain to Source Voltage Output Characteristics

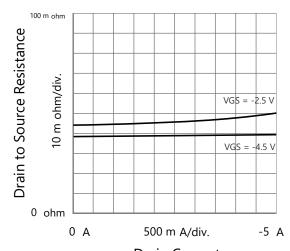


Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage

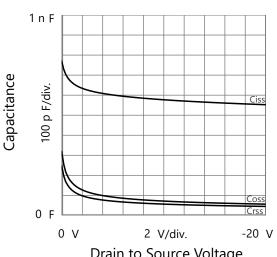




Gate to Source Voltage Transfer Characteristics



Drain Current
Drain to Source Resistance vs. Drain Current

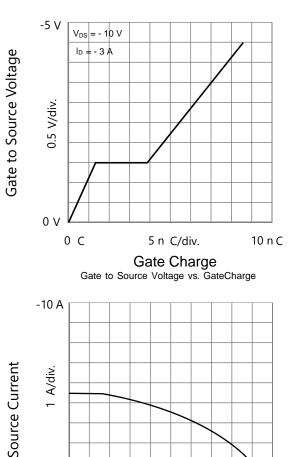


Drain to Source Voltage Capacitances

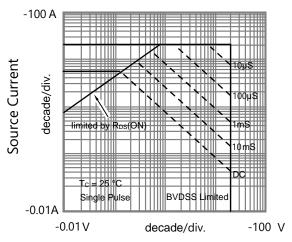


0 A

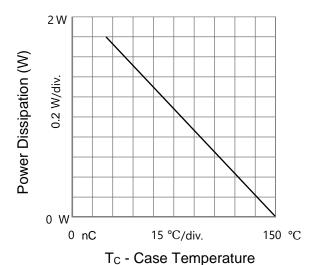
# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

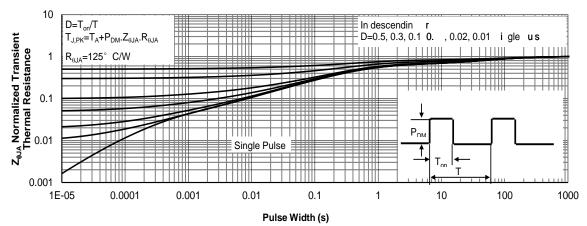






Source to Drain Voltage Safe Operating Area, Junction-to-Ambient

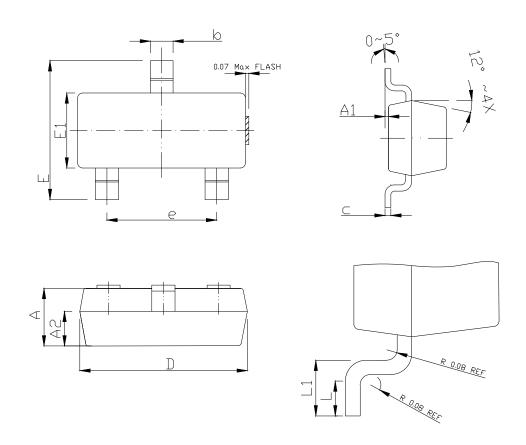




**Normalized Thermal Transient Impedance** 



# SOT-23 PACKAGE OUTLINE



# COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

CVMDOL	N	MILLIMETE	R
SYMBOL	MIN	NOM	MAX
A	0.80	1.00	1. 30
A1	0.00	0.05	0.15
b	0. 25	0.40	0.55
С	0.11 BSC		
D	2 <b>. 6</b> 0	2.90	3 <b>. 2</b> 0
Е	2.10	2.40	2.70
E1	1.10	1.30	1.48
е	1.90 BSC		
L	0.17	_	_
L1	0. 28	0.40	0.53
A2	0.60 REF		

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