

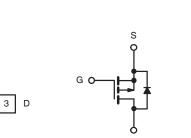
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# P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)			
- 20	0.39 at V <sub>GS</sub> = - 4.5 V	- 0.9	0.00.50			
- 20	0.53 at V <sub>GS</sub> = - 2.5 V	- 0.6	0.88 nC			

SOT-523

Top View



P-Channel MOSFET

**FEATURES** 

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

- · Load Switch for Portable Devices
- DC/DC Converter

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 20	٧		
Gate-Source Voltage		V <sub>GS</sub>	± 12	v		
	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	- 0.9			
Continuous Dunin Comment (T., 450 °C)	T <sub>C</sub> = 70 °C		- 0.8			
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C		- 0.7 <sup>a, b</sup>			
	T <sub>A</sub> = 70 °C		-0.4 <sup>a, b</sup>	Α		
Pulsed Drain Current (10 µs Pulse Width)	I <sub>DM</sub>	- 3				
	T <sub>C</sub> = 25 °C	1.	- 0.9			
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 0.8 <sup>a, b</sup>			
Maximum Power Dissipation	T <sub>C</sub> = 25 °C		0.78	W		
	T <sub>C</sub> = 70 °C	P <sub>D</sub>	0.49			
	T <sub>A</sub> = 25 °C		0.56 <sup>a, b</sup>			
	T <sub>A</sub> = 70 °C		0.35 <sup>a, b</sup>			
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 5 s	R <sub>thJA</sub>	90	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	50	65	]	

#### Notes:

- a. Surface Mounted on 1" x 1" FR4 board.
- b. t = 5 s.
- c. Maximum under Steady State conditions is 175 °C/W.
- d.  $T_C$  = 25 °C.



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 20		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	10 = -230 μΑ		- 2.5		IIIV/ C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.5		- 1.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	1	V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	0 μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 3			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 0.4 A		0.39	0.48		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 0.4 A		0.53	0.65	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 0.4 A		7.5		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			190			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		22		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			9			
Total Gate Charge	Qg	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 0.4 A		0.88			
Total Gate Charge	Qg			0.75		nC	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 0.4 A		0.62			
Gate-Drain Charge	Q <sub>gd</sub>			0.5			
Gate Resistance	$R_{g}$	f = 1 MHz	2	10	20	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			22	40		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 5 $\Omega$		20	40		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -0.4 \text{ A}, V_{GEN} = -0.5 \text{ V}, R_g = 1 \Omega$		35	72		
Fall Time	t <sub>f</sub>	]		10	20		
Turn-On Delay Time	t <sub>d(on)</sub>			8	16	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_L = 5 \Omega$		9	18		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -0.4 \text{ A}, V_{GEN} = -4 \text{ V}, R_g = 1 \Omega$		33	65		
Fall Time	t <sub>f</sub>	]		9	18		
Drain-Source Body Diode Characterist	ics			•			
Continuous Source-Drain Diode Current	I <sub>S</sub>	$T_C = 25  ^{\circ}C$			- 0.9	А	
Pulse Diode Forward Current	I <sub>SM</sub>				- 3	^	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 0.5 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time t <sub>rr</sub>				22	35	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	   I <sub>F</sub> = - 0.5 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		13	25	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	11F 0.3 A, αι/αι = 100 A/μ5, 1] = 25 0		9		ns	
Reverse Recovery Rise Time	t <sub>b</sub>	1		12			

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

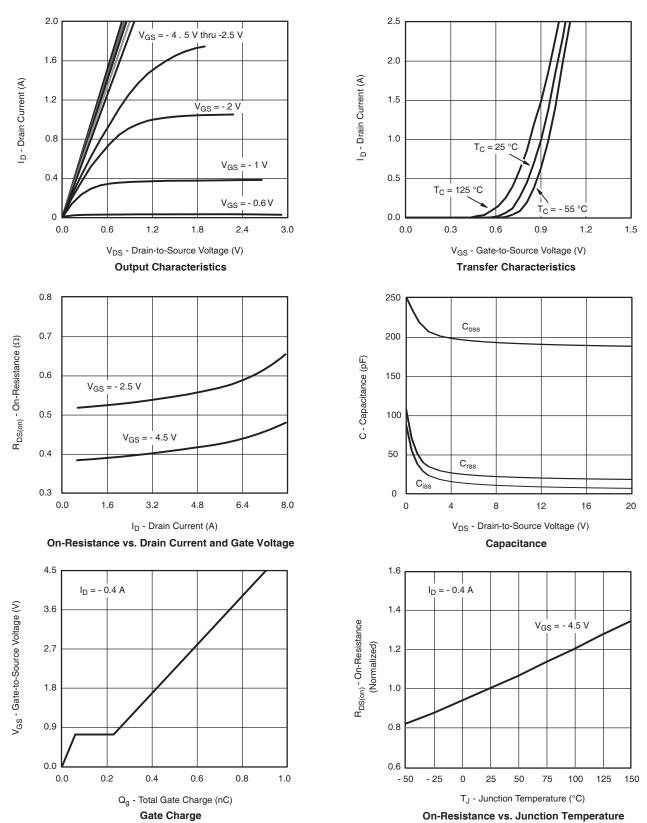
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.



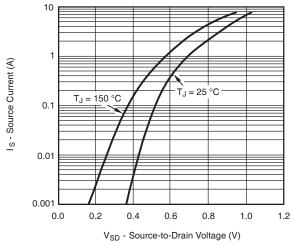


#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

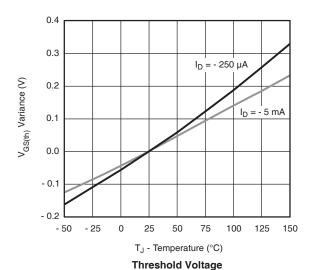


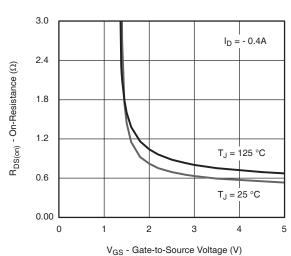


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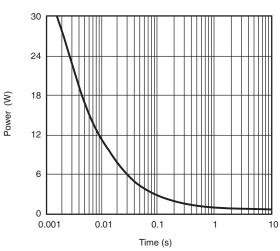


#### Source-Drain Diode Forward Voltage

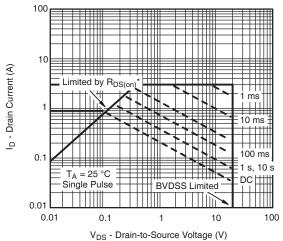




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

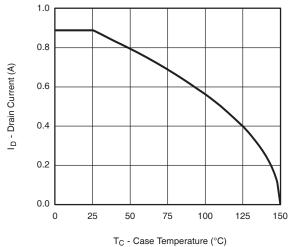


\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

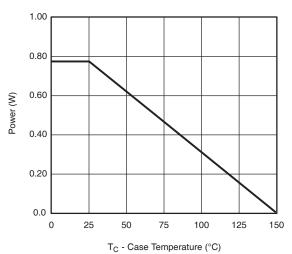
Safe Operating Area, Junction-to-Ambient

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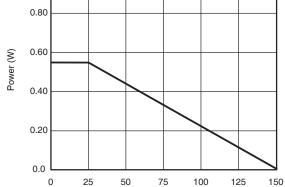


**Current Derating\*** 



Power Derating, Junction-to-Foot



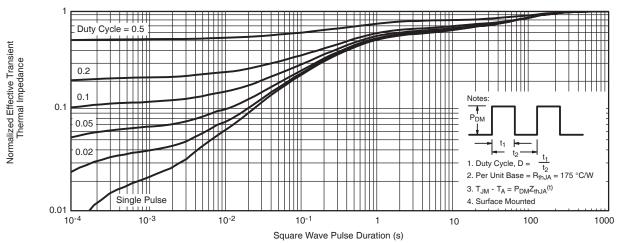


T<sub>A</sub> - Ambient Temperature (°C) Power, Junction-to-Ambient

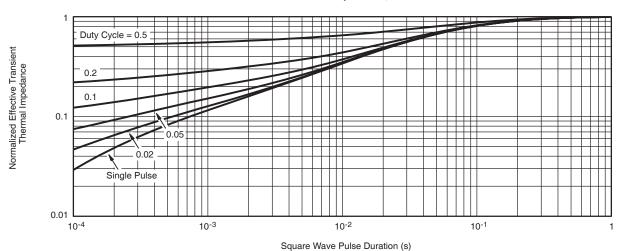
 $<sup>^*</sup>$  The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150  $^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



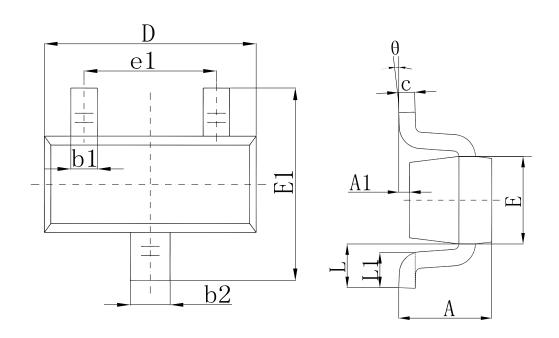
Normalized Thermal Transient Impedance, Junction-to-Ambient

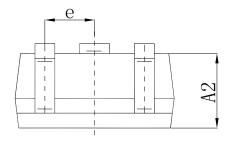


Normalized Thermal Transient Impedance, Junction-to-Foot



## SOT-523 PACKAGE OUTLINE





## **COMMON DIMENSIONS** (UNITS OF MEASURE=MILLIMETER)

C 1 1		Dim in m	ım	
Symbol	Min	Nor	Max	
A	0. 70	0.80	0. 90	
A1	0.00	0.05	0. 10	
A2	0. 70	0. 75	0.80	
b1	0. 15	0. 22	0. 29	
b2	0. 25	0.32	0. 39	
С	0. 10	0. 15	0. 20	
D	1.50	1.60	1. 70	
Е	1.45	1.60	1. 75	
E1	0. 70	0.80	0. 90	
е	0. 500TPY.			
e1	0. 90	1.00	1. 10	
L	0. 26	0.36	0. 46	
L1	0. 400REF.			
θ	0°	4°	8°	





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