

N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
60	0.078 at V _{GS} = 10 V	3.0	2.1 nC			
60	0.089 at V _{GS} = 4.5 V	2.1	2.1110			

FEATURES

- DT-Trench Power MOSFET
- 100 % R_q Tested
- 100 % UIS Tested
- Typical ESD Protection

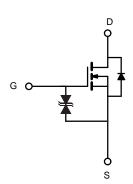
APPLICATIONS

- · Battery Switch
- DC/DC Converter





		SOT23-6		
D	1		6	D
D	2		5	D
G	3		4	S



Parameter		Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V	
Gate-Source Voltage	V _{GS}	± 20	v	
	T _C = 25 °C		3.0	
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C	L .	1.8	
Continuous Diain Current (1) = 150°C)	T _A = 25 °C	I _D	2.1 ^{b, c}	
	T _A = 70 °C		1.5 ^{b, c}	A
Pulsed Drain Current		I _{DM}	9	_ ^
Continuous Source-Drain Diode Current	T _C = 25 °C	I.	1.39	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	0.91 ^{b, c}	
Avalanche Current		I _{AS}	6	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	1.8	mJ
	T _C = 25 °C		1.66	
Maximum Dayor Dissination	T _C = 70 °C	В	1.06	w
Maximum Power Dissipation	T _A = 25 °C	P _D	1.09 ^{b, c}	- vv
	T _A = 70 °C		0.7 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	90	115	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75	C/VV	

- a. Based on T_C = 25 °C.
 b. Surface Mounted on 1" x 1" FR4 board.
- d. Maximum under Steady State conditions is 120 °C/W.



MOSFET SPECIFICATIONS $T_J = 25$ °C, ur Parameter Symbol		Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	60			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			55		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.5		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta da Dunia Comunit		V _{DS} = 60 V, V _{GS} = 0 V		1			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V, T _J = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	8			Α	
		V _{GS} = 10 V, I _D = 1.9 A		0.075	0.078	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 1.7 A		0.086	0.089		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15V, I _D = 1.9 A		5		S	
Dynamic ^b							
Input Capacitance	C _{iss}			180			
Output Capacitance	C _{oss}	V 00 V V 0 V (4 MHz		22		=	
Reverse Transfer Capacitance	C _{rss}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		13		pF	
Total Cata Charga	Qg	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 1.9 \text{ A}$	4.2	6.1			
Total Gate Charge				2.1	3.2	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 1.9 \text{ A}$		0.7			
Gate-Drain Charge	Q_{gd}			1			
Gate Resistance	R_g	f = 1 MHz	0.6	2.2	5.1	Ω	
Turn-On Delay Time	t _{d(on)}			4	6	- ns	
Rise Time	t _r	V_{DD} = 30 V, R_L = 20 Ω		10	15		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1.5 A, V_{GEN} = 10 V, R_G = 1 Ω		10	15		
Fall Time	t _f			7	10.5		
Turn-On Delay Time	t _{d(on)}			15	23	ns ns	
Rise Time	t _r	V_{DD} = 30 V, R_L = 20 Ω		16	24		
Turn-Off Delay Time	t _{d(off)}	I_D = 1.5 A, V_{GEN} = 4.5 V, R_G = 1 Ω		11	17		
Fall Time	t _f			11	17		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			2.19	^	
Pulse Diode Forward Current ^a	I _{SM}				9	A	
Body Diode Voltage	V _{SD}	I _S = 1.5 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			15	23	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 45 A 41/4 400 A/- T 05 00		10	15	nC	
Reverse Recovery Fall Time	t _a	$I_F = 1.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		12			
Reverse Recovery Rise Time	t _b			3		ns	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

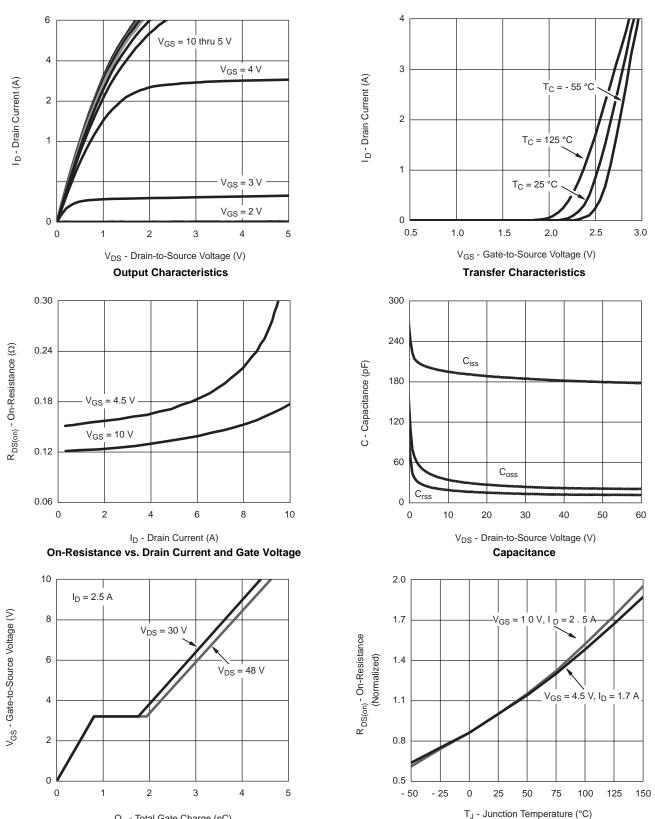
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Q_g - Total Gate Charge (nC)

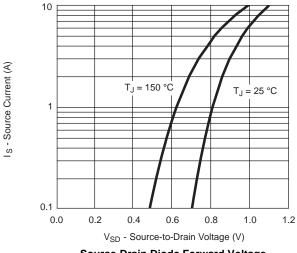
Gate Charge



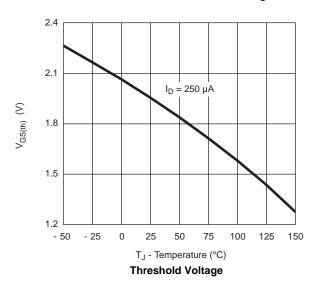
On-Resistance vs. Junction Temperature



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

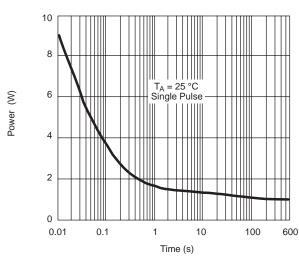


Source-Drain Diode Forward Voltage

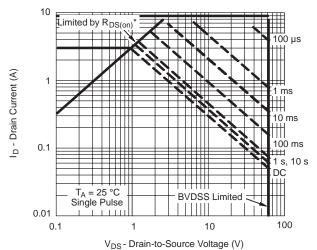


0.35 I_D = 2.5 A 0.25 $R_{DS(on)}$ - On-Resistance (Ω) T_J = 125 °C 0.20 0.15 T_J = 25 °C 0.10 3 6 7 8 10 V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power

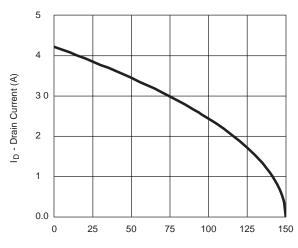


Safe Operating Area

^{*} V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

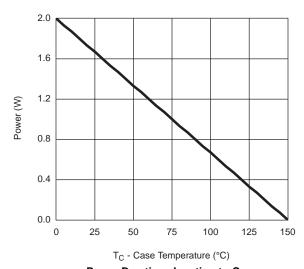


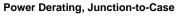
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

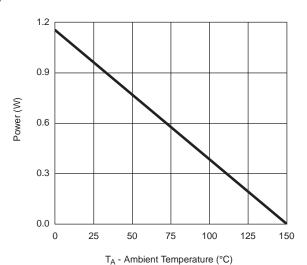


 $T_{\mbox{\scriptsize C}}$ - Case Temperature (°C)

Current Derating*





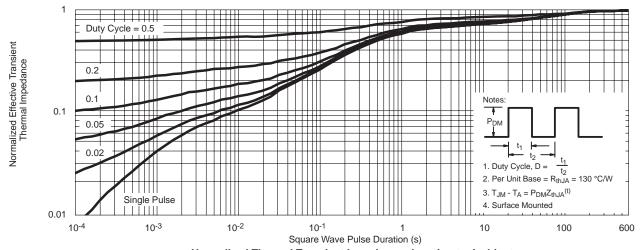


Power Derating, Junction-to-Ambient

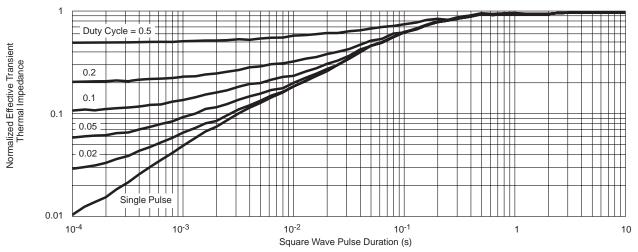
^{*} The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



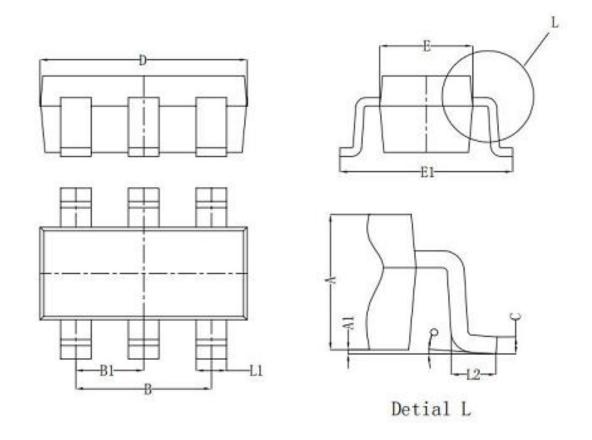
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-23-6L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	
А	0.95	1.10	1.35	
A1	0.00	0.05	0.12	
L1	0.25	0.40	0.55	
С	0.08	0.15	0.22	
D	2.77	2.92	3.12	
E	1.45	1.60	1.75	
E1	2.50	2.80	3.10	
В	1.75	1.90	2.10	
B1	0.95TYP			
L2	0.28	0.45	0.63	
0	0°	4°	8°	





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