

P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
60	0.225 at V _{GS} = - 10 V	- 3.0	00.50			
- 60	0.315 at V _{GS} = - 4.5 V	- 0.9	23 nC			

FEATURES

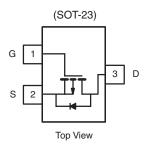
- DT-Trench Power MOSFET
- 100 % R_g Tested

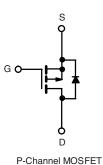
APPLICATIONS

- · Load Switch
- DC/DC Converter









ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V_{DS}	- 60	V		
Gate-Source Voltage		V_{GS}	± 20			
	T _C = 25 °C		- 3.0			
Continuous Drain Current /T 150 °C)	T _C = 70 °C	,	- 2.0			
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	- I _D	- 1.8 ^{b, c}	1		
	T _A = 70 °C		- 1.0 ^{b, c}	Α		
Pulsed Drain Current		I _{DM}	- 9			
0 11 0 0 11 11 11 11 11	T _C = 25 °C		- 3.0			
Continous Source-Drain Diode Current	T _A = 25 °C	I _S	- 1.2 ^{b, c}			
	T _C = 25 °C		1.2			
Maximum Dawar Dissination	T _C = 70 °C		0.8	l w		
Maximum Power Dissipation	T _A = 25 °C	- P _D	0.65 ^{b, c}	7 vv		
	T _A = 70 °C		0.4 ^{b, c}	1		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	85	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	45	55	- C/VV	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 166 $^{\circ}\text{C/W}.$



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 40		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η η η η η η η η η η η η η η η η η η η		4.8		mv/·C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = -250 \mu A$	- 2.0		- 4.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	I _{DSS}	V _{DS} = - 48 V, V _{GS} = 0 V	-1		- 1		
Zero Gate Voltage Drain Current		$V_{DS} = -48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 3.0			Α	
Durin Course Co. Olate Besidence	В	V _{GS} = - 10 V, I _D = - 2.0 A		225	280	_	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.0 A		315	390	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 48 V, I _D = - 2.0 A		10		S	
Dynamic ^b					•		
Input Capacitance	C _{iss}			609		pF	
Output Capacitance	C _{oss}	V _{DS} = - 48 V, V _{GS} = 0 V, f = 1 MHz		112			
Reverse Transfer Capacitance	C _{rss}	1		68			
Total Cata Chausa	0	V _{DS} = -48 V, V _{GS} = -10 V, I _D = -2.0 A		23		nC	
Total Gate Charge	Qg			12			
Gate-Source Charge	Q_{gs}	$V_{DS} = -48 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -1.0 \text{ A}$		2.5			
Gate-Drain Charge	Q_{gd}			3.2			
Gate Resistance	R_g	f = 1 MHz		4.3		Ω	
Turn-On Delay Time	t _{d(on)}			18			
Rise Time	t _r	$V_{DD} = -48 \text{ V}, R_L = 8 \Omega$		10			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1.0 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		41			
Fall Time	t _f]		13			
Turn-On Delay Time	t _{d(on)}			10		ns	
Rise Time	t _r	$V_{DD} = -48 \text{ V}, R_{L} = 8 \Omega$		6			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.0 A, V_{GEN} = - 10 V, R_g = 1 Ω		33			
Fall Time	t _f]		9			
Drain-Source Body Diode Characteristi	cs				•		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 3.0	٨	
Pulse Diode Forward Current	I _{SM}				- 9	A	
Body Diode Voltage	V_{SD}	I _S = - 2.0 A, V _{GS} = 0 V		- 0.9	- 1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			25		ns	
Body Diode Reverse Recovery Charge	Q_{rr}	Q_{rr} $I_F = -2.0 \text{ A, dl/dt} = 100 \text{ A/µs, T}_J = 25 °C$		32		nC	
Reverse Recovery Fall Time	t _a			10		ns	
Reverse Recovery Rise Time	t _b			7			

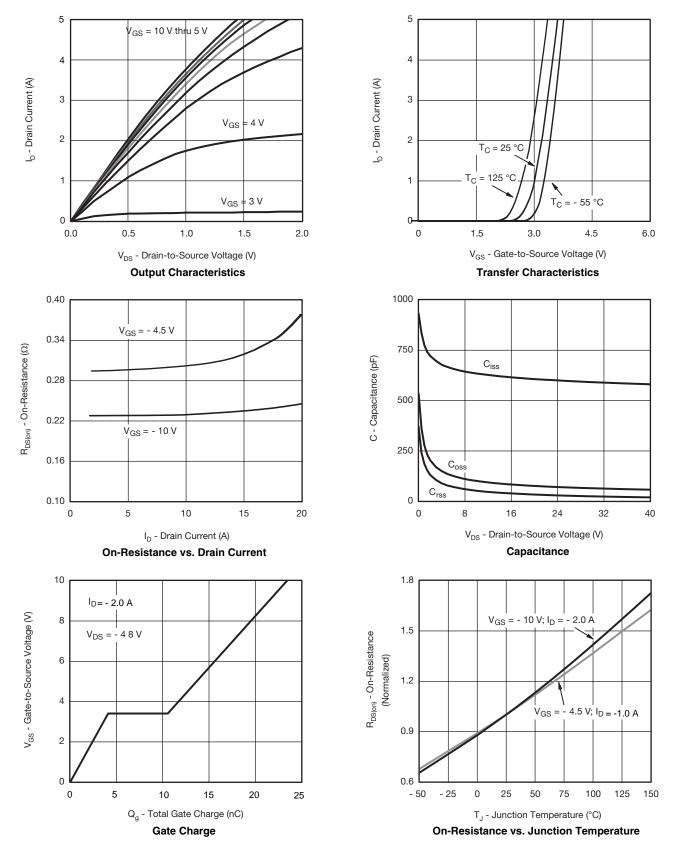
Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

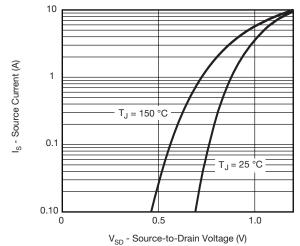
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

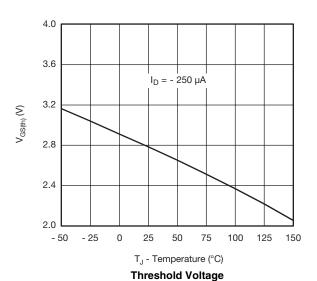




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

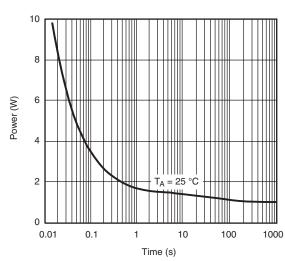


Source-Drain Diode Forward Voltage

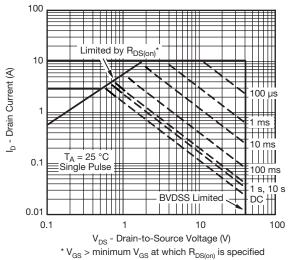


0.60 $I_D = -2.0 \text{ A}$ 0.50 $I_D = -2.0 \text{ A}$ 0.40 $I_J = 125 \text{ °C}$ 0.20 $I_J = 25 \text{ °C}$

On-Resistance vs. Gate-to-Source Voltage

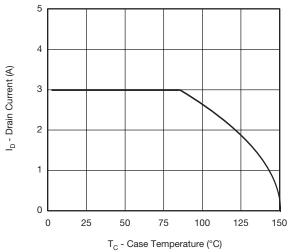


Single Pulse Power (Junction-to-Ambient)

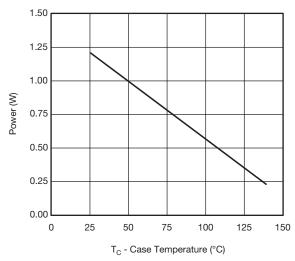


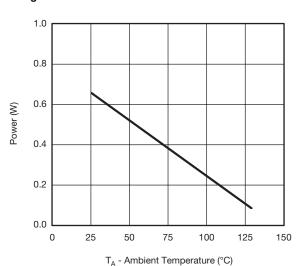
Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*



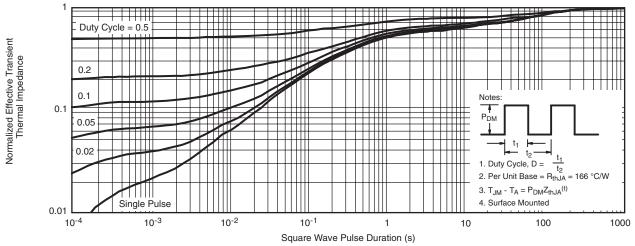


Power, Junction-to-Foot

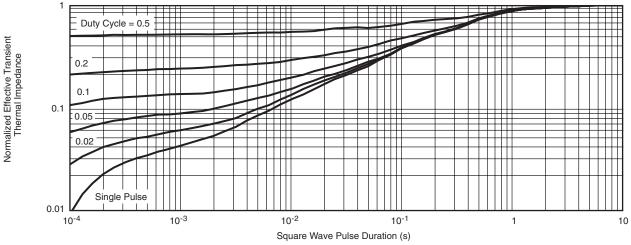
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



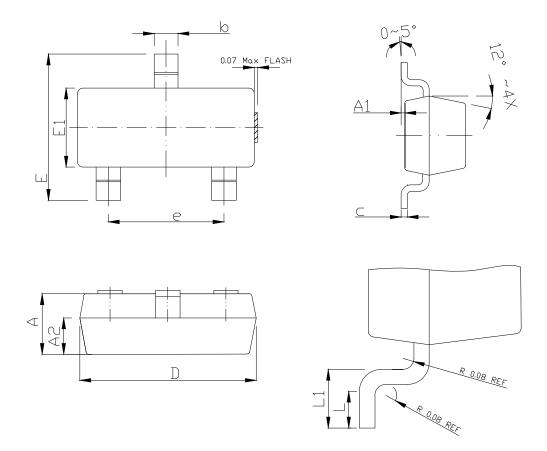
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

SOT-23 PACKAGE OUTLINE

www.din-tek.jp



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MILLIMETER			
	MIN	NOM	MAX	
A	0.80	1.00	1.30	
A1	0.00	0.05	0.15	
b	0.25	0.40	0.55	
С	0.11 BSC			
D	2 .6 0	2.90	3 . 2 0	
Е	2.10	2.40	2.70	
E1	1.10	1.30	1.48	
е	1.90 BSC			
L	0.17	_	_	
L1	0. 28	0.40	0.53	
A2	0.60 REF			





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