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# N- and P-Channel 60 V (D-S) MOSFET

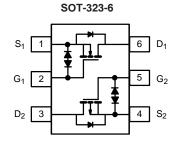
PRODUCT SUMMARY					
	V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)		
N-Channel	60	1.6 at V <sub>GS</sub> = 10 V	0.45		
		1.8 at V <sub>GS</sub> = 4.5 V	0.4		
P-Channel	- 60	4.5 at V <sub>GS</sub> = - 10 V	- 0.25		
		6.3 at V <sub>GS</sub> = - 4.5 V	- 0.2		

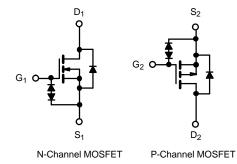
#### **FEATURES**

- DT-Trench Power MOSFET
- Very Small Footprint
- 100 % Rg and UIS Tested
- Gate-Source ESD Protected

#### APPLICATIONS

- LED Inverter Circuits
- DC/DC Conversion Circuits
- Motor drives





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage		V <sub>DS</sub>	60	- 60	v	
Gate-Source Voltage		V <sub>GS</sub>	± 20		V	
Continuous Drain Current $(T_J = 150 \text{ °C})^a$	T <sub>A</sub> = 25 °C	I <sub>D</sub>	0.45	- 0.25		
	T <sub>A</sub> = 85 °C		0.36	- 0.19		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	1.8	- 1	A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	0.45	- 0.25		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	0.33	0.3	w	
	T <sub>A</sub> = 85 °C	'D	0.17	0.16	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 t	o 150	°C	

Notes:

a. Surface mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.





Parameter	IONS (T <sub>J</sub> = 25 °C, unless otherwise noted)   Symbol   Test Conditions			Min.	Тур.	Max.	Unit
Static							
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 10 μA	N-Ch	60			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -10 \mu A$	P-Ch	- 60			l
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1		3.0	- V
		$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	P-Ch	- 1		- 3.0	
Gate-Body Leakage		$V_{DS} = 0 V, V_{GS} = \pm 5 V$	N-Ch			± 50	
		$v_{\rm DS} = 0$ v, $v_{\rm GS} = \pm 5$ v	P-Ch			± 100	
	I <sub>GSS</sub>		N-Ch			± 150	
		$V_{DS} = 0 V, V_{GS} = \pm 10 V$	P-Ch			± 200	- ^
		$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	N-Ch			10	nA
		$V_{DS} = -50 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch			- 25	_
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	N-Ch			100	
		$V_{DS} = -50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 85 ^{\circ}\text{C}$	P-Ch			- 250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	N-Ch	1.5			
On-State Drain Current	'D(on)	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 4.5 V	P-Ch	- 0.85			A
Drain-Source On-State		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.25 \text{ A}$	N-Ch		1.8	3.9	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 0.16 A	P-Ch		6.3	9.2	
	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.3 A	N-Ch		1.6	2.2	Ω
Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 0.2 A	P-Ch		4.5	5.3	
Forward Transconductance <sup>a</sup>	<b>a</b> .	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.2 A	N-Ch		12		
	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 0.1A	P-Ch		10		ms
	V <sub>SD</sub>	I <sub>S</sub> = 0.2 A, V <sub>GS</sub> = 0 V	N-Ch			1.4	v
Diode Forward Voltage <sup>a</sup>	♥ SD	I <sub>S</sub> = - 0.2 A, V <sub>GS</sub> = 0 V	P-Ch			- 1.4	v
Dynamic <sup>b</sup>							
Tatal Cata Charge	0		N-Ch		1.2		
Total Gate Charge	Qg	N-Channel V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.25 A	P-Ch		1.2		nC
Gate-Source Charge	Q <sub>gs</sub>		N-Ch		0.2		
Cale Cource Charge	⊶gs	P-Channel	P-Ch		0.4		
Gate-Drain Charge	Q <sub>gd</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -0.2 \text{ A}$	N-Ch		0.8		
eare Drain enange	gu		P-Ch		0.5		
Input Capacitance	C <sub>iss</sub>	N-Channel	N-Ch		60		4
	100	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	P-Ch		83		
Output Capacitance	C <sub>oss</sub>	I IN	N-Ch		18		pF
Reverse Transfer Capacitance		P-Channel	P-Ch		15		
	C <sub>rss</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	N-Ch		8		-
-		N-Channel	P-Ch		9		+
Turn-On Time <sup>c</sup>	t <sub>ON</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 150 \Omega$	N-Ch		5		
	SON	$I_D \cong 0.2 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, \text{R}_{\text{g}} = 10 \Omega$	P-Ch		8		ns
Turn-Off Time <sup>c</sup>	te	P-Channel V <sub>DD</sub> = - 25 V, R <sub>L</sub> = 150 Ω	N-Ch		15		115
	t <sub>OFF</sub>	$V_{DD} = -2.5 \text{ V}, \text{ H}_{L} = -100 \Omega_{2}$ $I_{D} \cong -0.2 \text{ A}, \text{ V}_{\text{GEN}} = -10 \text{ V}, \text{ H}_{\text{g}} = 10 \Omega_{2}$	P-Ch		13		1

Notes:

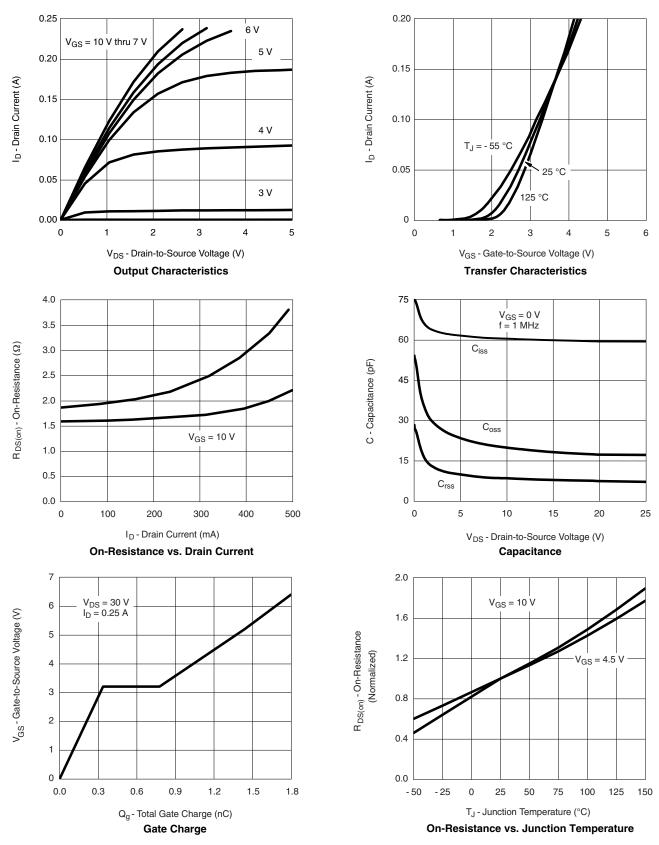
a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

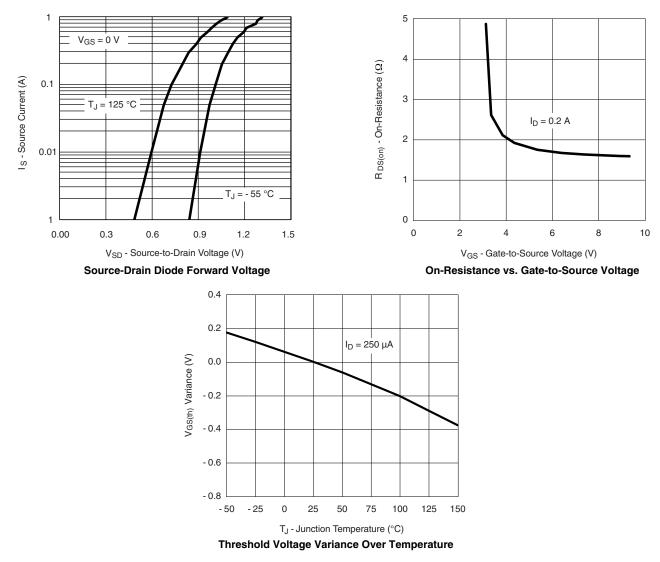






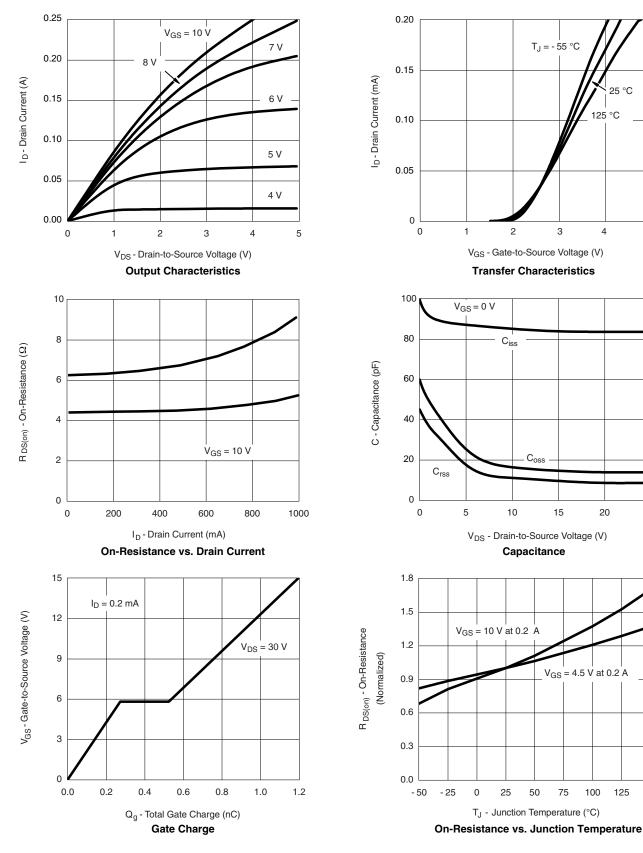


## **N-CHANNEL TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



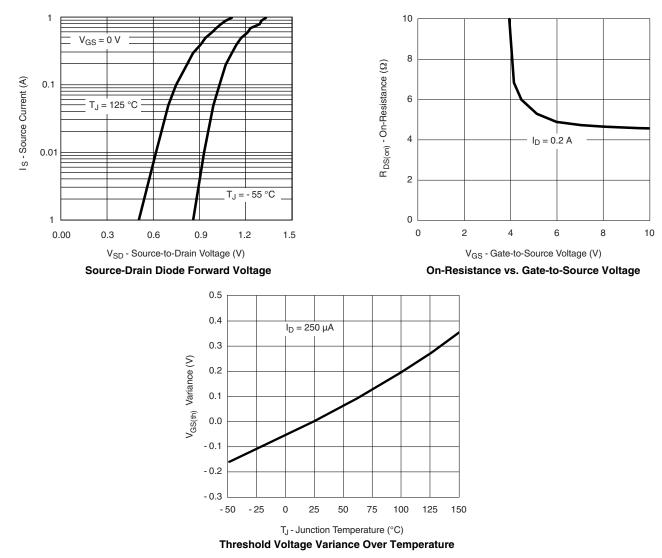


### **P-CHANNEL TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

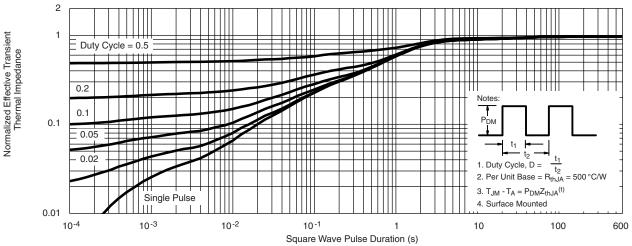




## **P-CHANNEL TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)





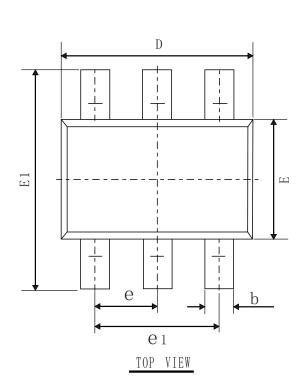


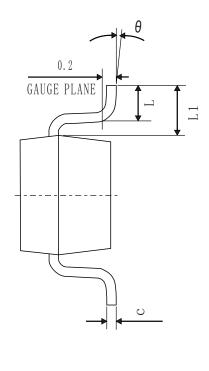
### N- OR P-CHANNEL TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



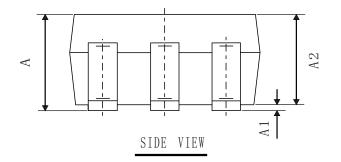


## SOT-323-6L PACKAGE OUTLINE





SIDE VIEW



#### COMMON DIMENSIONS (UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX	
А	0.90	1.00	1.10	
A 1	0.00	0.05	0.10	
A2	0.90	0.95	1.00	
b	0.20	0.25	0.30	
С	0.08	0.10	0.15	
e 1	1.20	1.30	1.40	
D	2.00	2.10	2.20	
Е	1.15	1.25	1.35	
E 1	2.15	2.30	2.45	
L	0.26	0.36	0.46	
θ	0°	4°	8°	
L1	0.525 REF			
е	0.65 TYP			



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