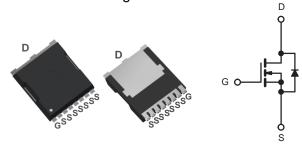


DTT300N06 www.din-tek.jp

N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (mΩ) TYP.	Q _g (TYP.)			
60	1.3 at V _{GS} = 10 V	300	103 nC		

TOLL Pin Configuration



N-Channel MOSFET

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS tested

APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power suppliesLighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Battery management

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \text{ °C}$, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	60	v			
Gate-Source Voltage	V _{GS}	V _{GS} ± 20				
	T _C = 25 °C		300			
Continuous Drain Current (T _J = 150 °C)	T _C = 100 °C	– I _D –	191			
Pulsed Drain Current (t = 100 µs)	I _{DM}	1200	A			
Avalanche Current ^{a,d}	L = 0.1 mH	I _{AS}	65			
Single Avalanche Energy ^{a,d}	L = 0.1 MH	E _{AS}	239	mJ		
Maximum Power Dissipation ^a	T _C = 25 °C	р	363 ^b	W		
	T _C = 100 °C	- P _D -	178 ^b	v		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	LIMIT	UNIT				
Junction-to-Ambient (PCB Mount) ^c	t ≤ 10 s	R _{thJA}	22	°C/W			
Junction-to-Case (Drain)	Steady State	R _{thJC}	0.5	C/W			

Notes

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).
- d. This single-pulse measurement was taken under the following condition [L = 0.1mH, V_{GS} = 10V, V_{DS} = 48V] while its value is limited by T_{J_Max} = 150°C.



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.		MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	60	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\;\mu A$	1	-	3	v	
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 100	nA	
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 48$ V, $V_{GS} = 0$ V, $T_J = 85\ ^\circ C$	-	-	30	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \! \geq \! 10 \ V, V_{GS} \! = \! 10 \ V$	300	-	-	Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	1.3	1.6	mΩ	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 5 V, I_{D} = 30 A$	-	95	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	13060	-	pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 30 V, f = 1 MHz	-	4118	-		
Reverse Transfer Capacitance	C _{rss}		-	102	-		
Total Gate Charge ^c	Qg		-	103	-	nC	
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 30 V, V_{GS} = 10 V, I_{D} = 30 A	-	23	-		
Gate-Drain Charge ^c	Q _{gd}		-	11	-		
Gate Resistance	Rg	f = 1 MHz	-	3.0	-	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	22	-		
Rise Time ^c	t _r	V_{DD} = 30 V, I_D = 30 A, R_g = 6 Ω	-	67	-	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$V_{GEN} = 10 V$	-	95	-		
Fall Time ^c	t _f		-	70	-		
Drain-Source Body Diode Ratings and	Characterist	ics ^b (T _C = 25 °C)					
Continuous Source-Drain Diode Current	ا _S	T _C = 25 °C	-	-	300	А	
Pulsed Current (t = 100 µs)	I _{SM}		-	-	1200	А	
Forward Voltage ^a	V _{SD}	$I_F = 1 \text{ A}, V_{GS} = 0 \text{ V}$	-	-	1	V	
Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/µs	-	102	-	ns	
Reverse Recovery Charge	Q _{rr}	$r_F = 20 \text{ A}, \text{ avat} = 100 \text{ A/}\mu\text{s}$	-	70	-	nC	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

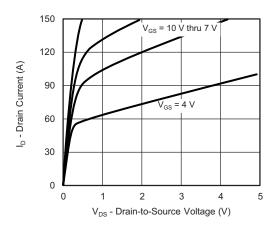
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

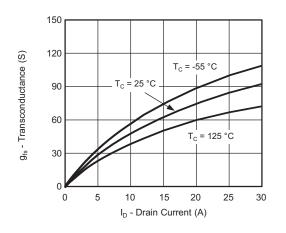
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



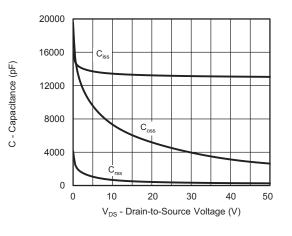
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



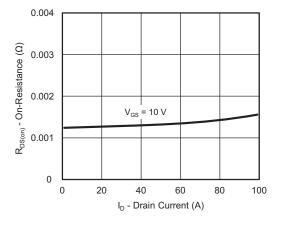
Output Characteristics



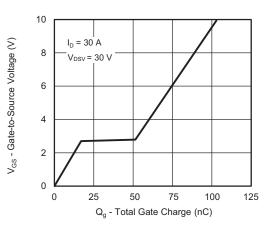
Transconductance



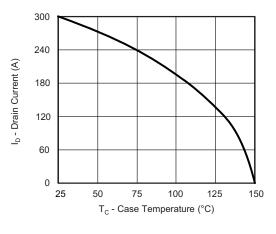
Capacitance



On-Resistance vs. Drain Current



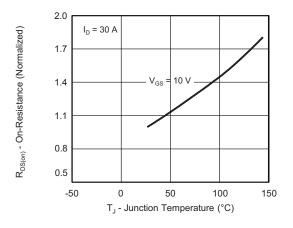
Gate Charge



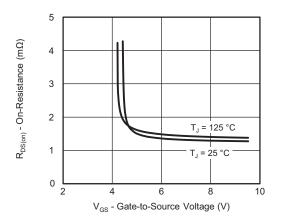
Current De-Rating



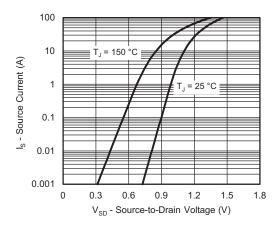
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



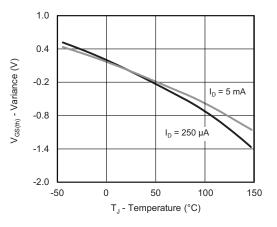
On-Resistance vs. Junction Temperature



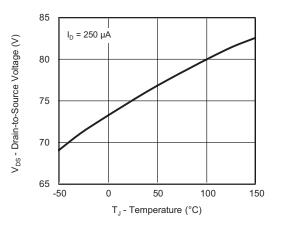
On-Resistance vs. Gate-to-Source Voltage



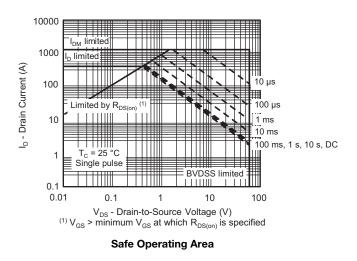
Source Drain Diode Forward Voltage



Threshold Voltage

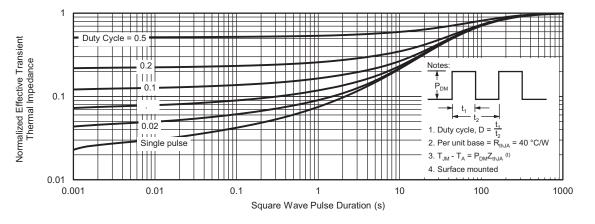


Drain Source Breakdown vs. Junction Temperature





THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

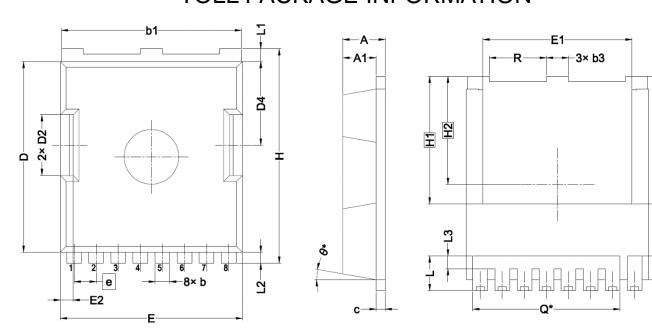


Normalized Thermal Transient Impedance, Junction-to-Ambient

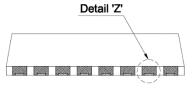
Din-Tek SEMICONDUCTOR

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TOLL PACKAGE INFORMATION



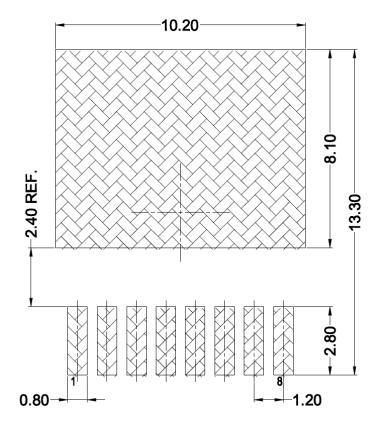


Detail 'Z'

SVMDOL	mm			mm			
SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
Α	2.20	2.30	2.40	Н	11.58	11.68	11.78
A1	1.70	1.80	1.90	H1	6.95 BSC		
b	0.70	0.80	0.90	H2	5.89 BSC		
b1	9.70	9.80	9.90	i	0.10 REF		
b3	1.10	1.20	1.30	j	0.46 REF		
С	0.40	0.50	0.60	K	2.80 REF		
D	10.28	10.38	10.48	L	1.40 1.90 2.10		2.10
D1	10.98	11.08	11.18	L1	0.60	0.70	0.80
D2	3.20	3.30	3.40	L2	0.50	0.60	0.70
D4	4.45	4.55	4.65	L3	0.30	0.70	0.80
E	9.80	9.90	10.00	N	8		
E1	8.00	8.10	8.20	Q	8.00 REF		
E2	0.60	0.70	0.80	R	3.00	3.10	3.20
е	e 1.20 BSC			θ	10º REF		



TOLL RECOMMENDED LAND PATTERN



unit : mm



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