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N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I _D (A) ^a	Q _g (Typ.)			
30	2.2 at V _{GS} = 10 V	150	68.8 nC			
30	3.6 at $V_{GS} = 4.5 \text{ V}$	130				

FEATURES

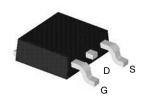
- DT-Trench MOSFET
- 100 % R_g and UIS Tested
- High Current Capability

RoHS

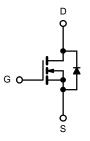
APPLICATIONS

- DC/DC Converters
- · Ideal for high-frequency switching

TO-252 Pin Configuration



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	30	V			
Gate-Source Voltage	V _{GS}	± 20	v			
Continuous Drain Current (T _{.I} = 150 °C) ^a	T _C = 25 °C	1_	150			
Continuous Diam Current (1) = 150 °C)	T _C = 100 °C	I _D	51	Α		
Pulsed Drain Current ^b	I _{DM}	450				
Single Avalanche Energy	E _{AS}	402	mJ			
Maximum Power Discinstians	T _C = 25 °C	В	105	w		
Maximum Power Dissipation ^c	T _C = 100 °C	P _D	42			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to +150	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	62	°C/W		
Junction-to-Case (Drain)	R _{thJC}	1.2	_ C/VV		

Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R_{8JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



Parameter	Symbol	I Test Conditions		Min. Typ.		Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0, I_D = 250 \mu\text{A}$	30			V	
Gate-Source Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	l	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V, T _J = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	150			Α	
Desir Course On Chata Basistanas	Res()	V _{GS} = 10 V, I _D = 30 A		2.2	2.6		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 20 A		3.6	4.5	mΩ	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 5 V,I _D = 30 A		75		S	
Dynamic ^b							
Input Capacitance	C _{iss}			3350		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		478			
Reverse Transfer Capacitance	C _{rss}]		449			
Total Gate Charge	Q _g			68.8		nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 30 \text{ A}$		5.6			
Gate-Drain Charge	Q _{gd}			14			
Gate Resistance	R_{g}	-		1.7		Ω	
Turn-On Delay Time	t _{d(on)}			19			
Rise Time	t _r	$V_{DD} = 15 \text{ V}, I_D = 30 \text{ A}, R_q = 3 \Omega$		10		ns	
Turn-Off DelayTime	t _{d(off)}	V _{GS} = 10 V		61			
Fall Time	t _f			10			
Drain-Source Body Diode Characterist	ics		<u> </u>				
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			150	А	
Pulse Diode Forward Current (100 μs)	I _{SM}				450		
Body Diode Voltage	V_{SD}	I _S = 1 A			1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	$I_F = 30 \text{ A, dI/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 ^{\circ}\text{C}$		48		ns	
Body Diode Reverse Recovery Charge	Q_{rr}	1 - 00 Λ, αι/αι - 100 Λ/μ3, 1 - 25 0		30		nC	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

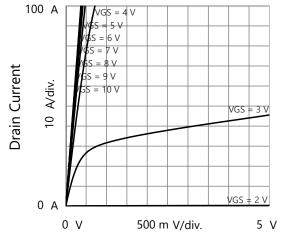
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

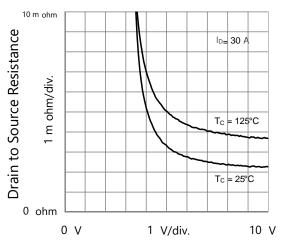




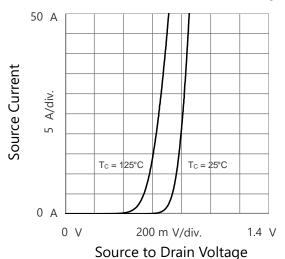
TYPICAL CHARACTERISTICS (25°C, unless otherwise noted)



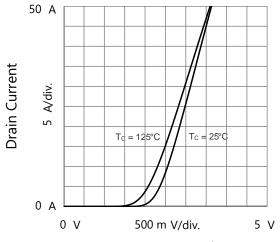
Drain to Source Voltage Output Characteristics



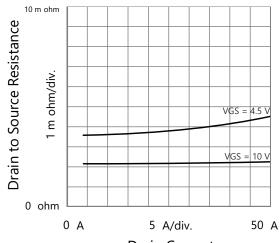
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



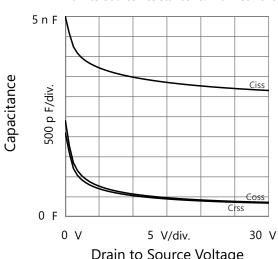
Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



Drain Current
Drain to Source Resistance vs. Drain Current



Drain to Source Voltage Capacitances

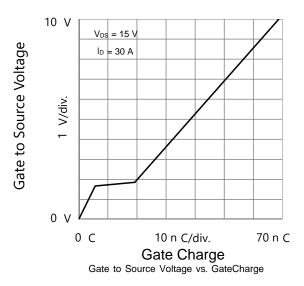


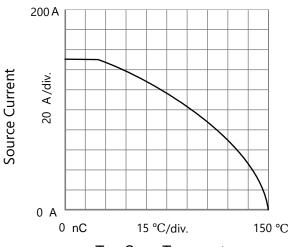
100 V

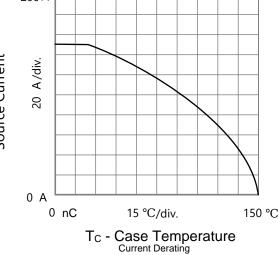
150 °C

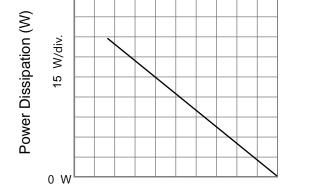


TYPICAL CHARACTERISTICS (25°C, unless otherwise noted)









decade/div.

Source to Drain Voltage

Safe Operating Area, Junction-to-Ambient

1000 A

decade/div.

0.1 A

150 W

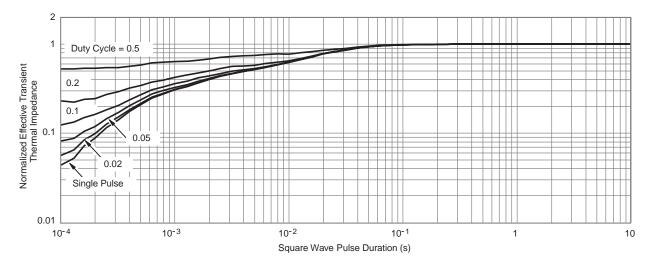
0.01 V

0 nC

Source Current

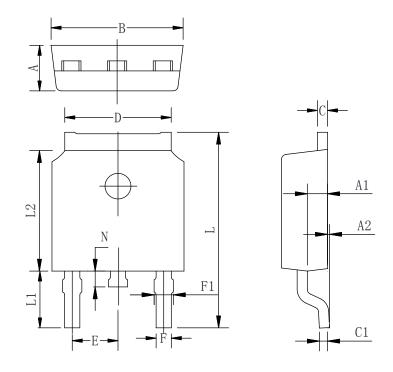


15 °C/div.



Normalized Thermal Transient Impedance, Junction-to-Case

TO-252-2L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
A	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
В	6.40	6.60	6.80
C	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
Е	2.286 TYP		
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
L	9.30	9.90	10.80
L1	2.4	2.8	3.6
L2	5.90	6.10	6.55
N	0.57	0.80	1.05



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