N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY								
V _{DS} (V)	$R_{DS(on)}(m\Omega)(Typ.)$	I _D (A) ^a	Q _g (Typ.)					
40	1.9 at V _{GS} = 10 V	145	43 nC					

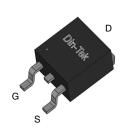
TO-252 Pin Configuration Top View

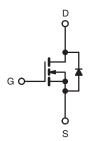
FEATURES

- DT-SGT Power MOSFET
- Very Low On-resistance
- Excellent FOM(Figure of Merit)
- 100% UIS and Rg Tested

APPLICATIONS

- · Load Switching
- Motor Driver
- · High Frequency Switching, Synchronous Rectification





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V _{DS}	40	V		
Gate-Source Voltage	V_{GS}	± 20	V		
Continuous Proin Current /T = 150 °C\a	T _C = 25 °C	1	145	A	
Continuous Drain Current (T _J = 150 °C) ^a	T _C = 100 °C	- I _D	91		
Pulsed Drain Current ^b	I _{DM}	561			
Single Avalanche Energy	E _{AS}	462	mJ		
Maximum Power Dissipations	T _C = 25 °C	В	78	W	
Maximum Power Dissipation ^c	T _C = 100 °C	- P _D	31		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^d	R_{thJA}	38	°C/W		
Junction-to-Case (Drain)	R _{thJC}	1.6	- C/VV		

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R_{0JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS} $V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		40	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		-	4.0	
Gate-Body Leakage	I _{GSS}			-	± 100	nA
Zana Oata Wallana Basia Oamaat	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	-	-	1 .	
Zero Gate Voltage Drain Current		V _{DS} = 32 V, V _{GS} = 0 V, T _J = 125 °C	-	-	100	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	145	-	-	Α
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A	-	1.9	2.3	mΩ
25(44)		V _{DS} = 5 V, I _D = 20 A	-	44	-	S
Dynamic ^b				<u>. </u>	<u> </u>	
Input Capacitance	C _{iss}		-	3014	-	pF
Output Capacitance	Coss	V _{GS} = 0 V, V _{DS} = 20 V, f = 1 MHz	-	1544	-	
Reverse Transfer Capacitance	C _{rss}		-	71	-	
Total Gate Charge ^c	Qg		-	43	-	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	12.7	-	
Gate-Drain Charge ^c	Q_{gd}		-	8.4	-	
Gate Resistance	R _g	f = 1 MHz	-	1.2	-	Ω
Turn-On Delay Time ^c	t _{d(on)}		-	7.5	-	
Rise Time ^c	t _r	$V_{DD} = 20 \text{ V}, \text{ R}_{GEN} = 3 \Omega, \text{ I}_{D} = 20 \text{ A},$	-	17	-	
Turn-Off Delay Time ^c	t _{d(off)}	VGS = 10 V	-	24	-	ns -
Fall Time ^c	t _f		-	11	-	
Drain-Source Body Diode Ratings and	Characterist	ics b (T _C = 25 °C)				
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	105	Α
Pulsed Current	I _{SM}		-	-	561	Α
Forward Voltage ^a	V _{SD}	I _F = 2 A, V _{GS} = 0 V	-	0.7	1.2	V
Reverse Recovery Time t _{rr}		1 20 A di/dt = 100 A/:	-	46	-	ns
Reverse Recovery Charge	Q _{rr}	I _F = 20 A, di/dt = 100 A/μs	_	50	-	nC

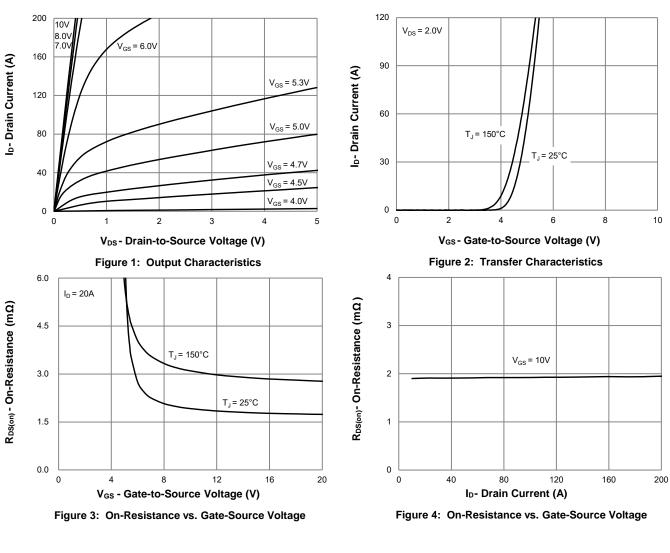
Notes

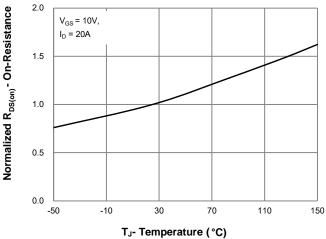
- a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

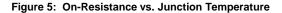
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25°C unless otherwise noted)







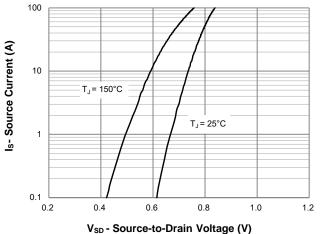


Figure 6: Source-Drain Diode Forward Voltage



TYPICAL CHARACTERISTICS (25°C unless otherwise noted)

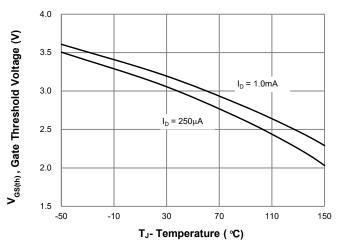


Figure 7: Gate Threshold Variation vs. Junction Temperature

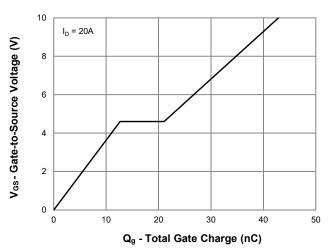


Figure 8: Gate Charge Characteristics

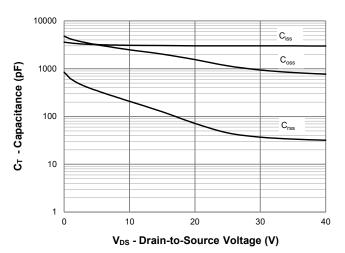


Figure 9: Capacitance Characteristics

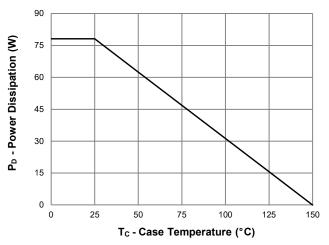


Figure 10: Power Derating

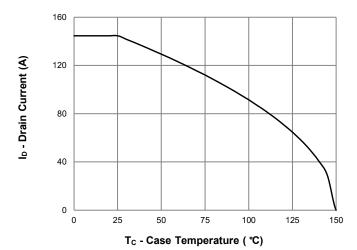
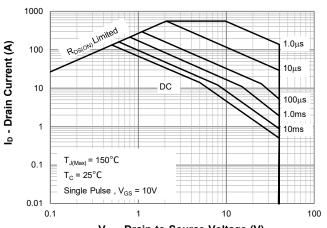


Figure 11: Current Derating



 $\ensuremath{V_{\text{DS}}}$ - Drain-to-Source Voltage (V)

Figure 12: Safe Operating Area



TYPICAL CHARACTERISTICS (25°C unless otherwise noted)

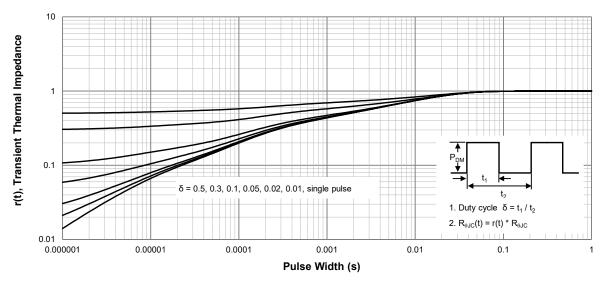
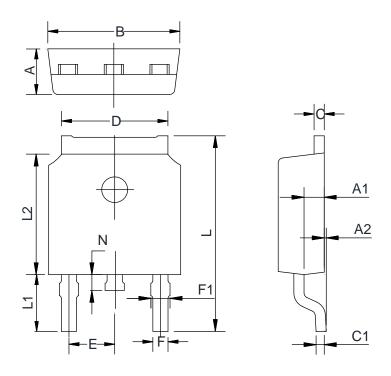


Figure 13: Normalized Maximum Transient Thermal Impedance



TO-252-2L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
Α	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
В	6.40	6.60	6.80
С	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
E	2.286 TYP		
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
L	9.30	9.90	10.80
L1	2.4	2.8	3.6
L2	5.90	6.10	6.55
N	0.57	0.80	1.05





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