

## N-Channel 80 V (D-S) Super Junction Power MOSFET

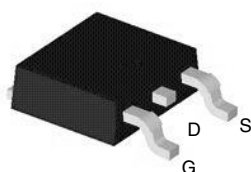
PRODUCT SUMMARY			
$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>c</sup>	$Q_g$ (Typ.)
80	0.0029 at $V_{GS} = 10$ V	140	90 nC
	0.0042 at $V_{GS} = 4.5$ V	90	

### FEATURES

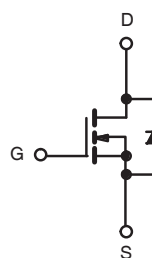
- DT-Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested



TO-252 Pin Configuration



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)				
Parameter		Symbol	Limit	Unit
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_J = 175$ °C) <sup>b</sup>	$T_C = 25$ °C	$I_D$	140	A
	$T_C = 100$ °C		90 <sup>a</sup>	
Pulsed Drain Current		$I_{DM}$	560	
Continuous Source Current (Diode Conduction)		$I_S$	135	
Avalanche Current		$I_{AS}$	140	
Single Avalanche Energy (Duty Cycle $\leq 1$ %)	$L = 0.1$ mH	$E_{AS}$	300	mJ
Maximum Power Dissipation	$T_C = 25$ °C	$P_D$	425	W
	$T_C = 125$ °C		150	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ sec	$R_{thJA}$	10	15	°C/W
	Steady State		20	35	
Maximum Junction-to-Case		$R_{thJC}$	0.75	1.0	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c.  $t \leq 10$  s.

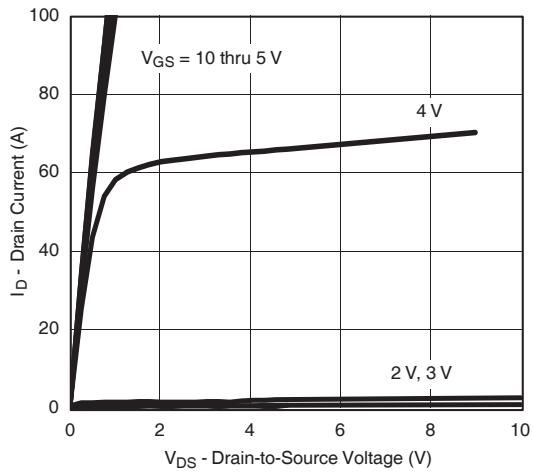
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	80			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 64\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 64\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			10	
		$V_{DS} = 64\text{ V}, V_{GS} = 0\text{ V}, T_J = 175\text{ }^\circ\text{C}$			150	
On-State Drain Current <sup>b</sup>	$I_{D(on)}$	$V_{DS} = 10\text{ V}, V_{GS} = 10\text{ V}$	140			A
Drain-Source On-State Resistance <sup>b</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 30\text{ A}$		0.0029	0.0035	$\Omega$
		$V_{GS} = 10\text{ V}, I_D = 20\text{ A}, T_J = 125\text{ }^\circ\text{C}$		0.0034	0.0040	
		$V_{GS} = 4.5\text{ V}, I_D = 20\text{ A}$		0.0042	0.0055	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 64\text{ V}, I_D = 30\text{ A}$		85		S
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 64\text{ V}, f = 1\text{ MHz}$		10050		$\text{pF}$
Output Capacitance	$C_{oss}$			3340		
Reverse Transfer Capacitance	$C_{rss}$			460		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 64\text{ V}, V_{GS} = 10\text{ V}, I_D = 30\text{ A}$		98	120	$\text{nC}$
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			20		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			30		
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 64\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong 30\text{ A}, V_{GEN} = 10\text{ V}, R_g = 2.5\text{ }\Omega$		10	20	ns
Rise Time <sup>c</sup>	$t_r$			18	25	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			55	80	
Fall Time <sup>c</sup>	$t_f$			15	22	
<b>Source-Drain Diode Ratings and Characteristics</b> ( $T_C = 25\text{ }^\circ\text{C}$ )						
Pulsed Current	$I_{SM}$				560	A
Diode Forward Voltage	$V_{SD}$	$I_F = 20\text{ A}, V_{GS} = 0\text{ V}$		0.8	1.5	V
Reverse Recovery Time	$t_{rr}$	$I_F = 30\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		125	196	ns

Notes:

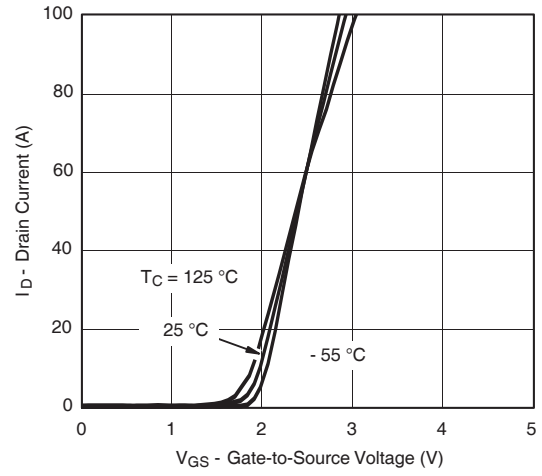
- For design aid only; not subject to production testing.
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

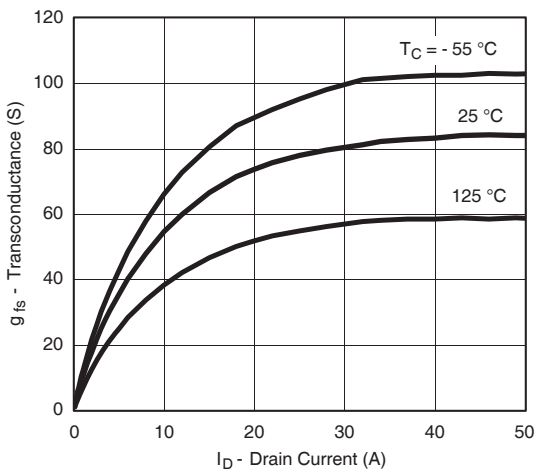
**TYPICAL CHARACTERISTICS** (25 °C unless noted)



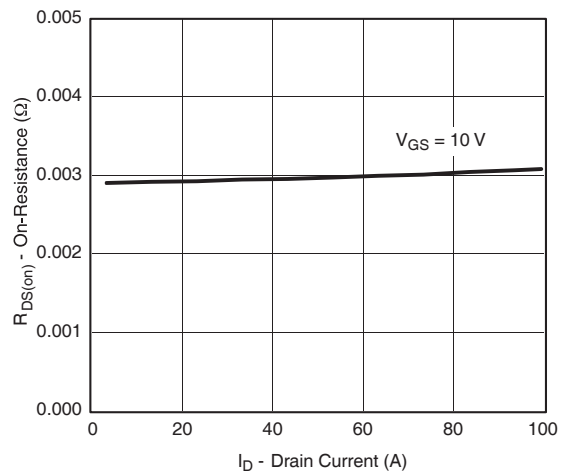
**Output Characteristics**



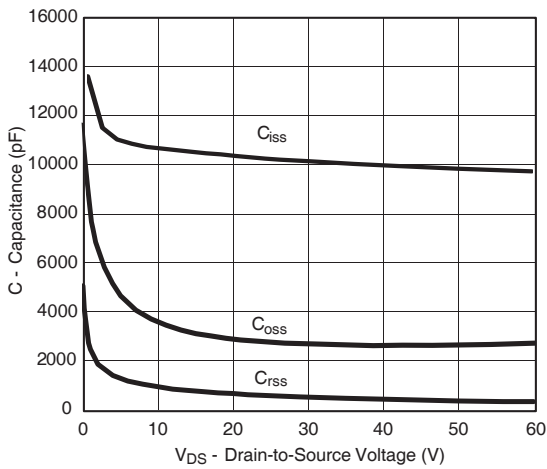
**Transfer Characteristics**



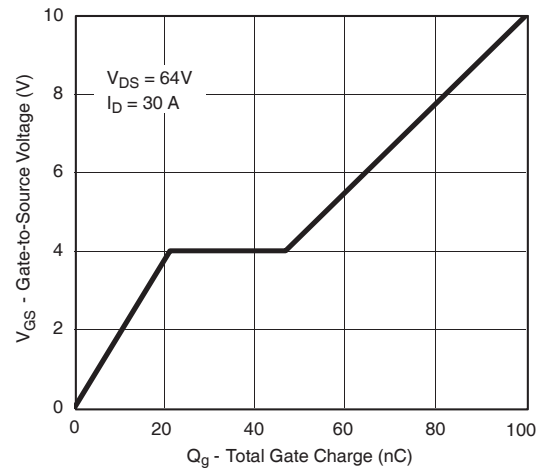
**Transconductance**



**On-Resistance vs. Drain Current**

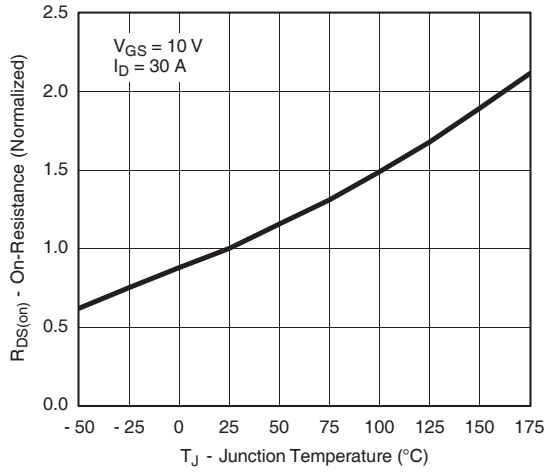


**Capacitance**

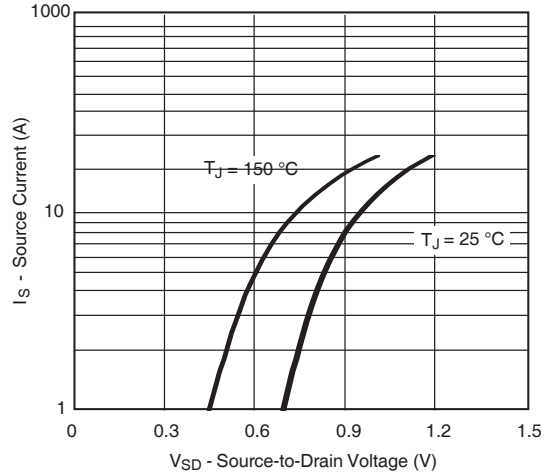


**Gate Charge**

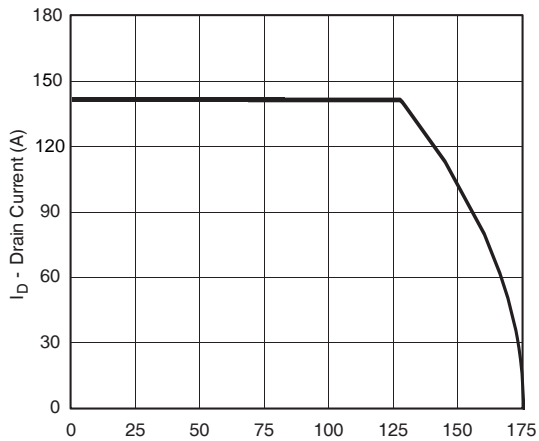
**TYPICAL CHARACTERISTICS** (25 °C unless noted)



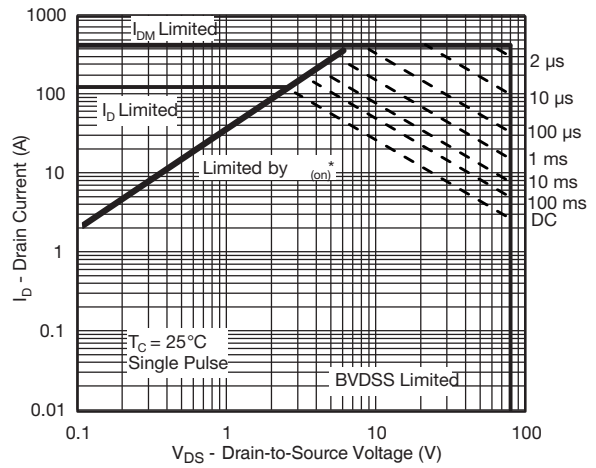
**On-Resistance vs. Junction Temperature**



**Source-Drain Diode Forward Voltage**



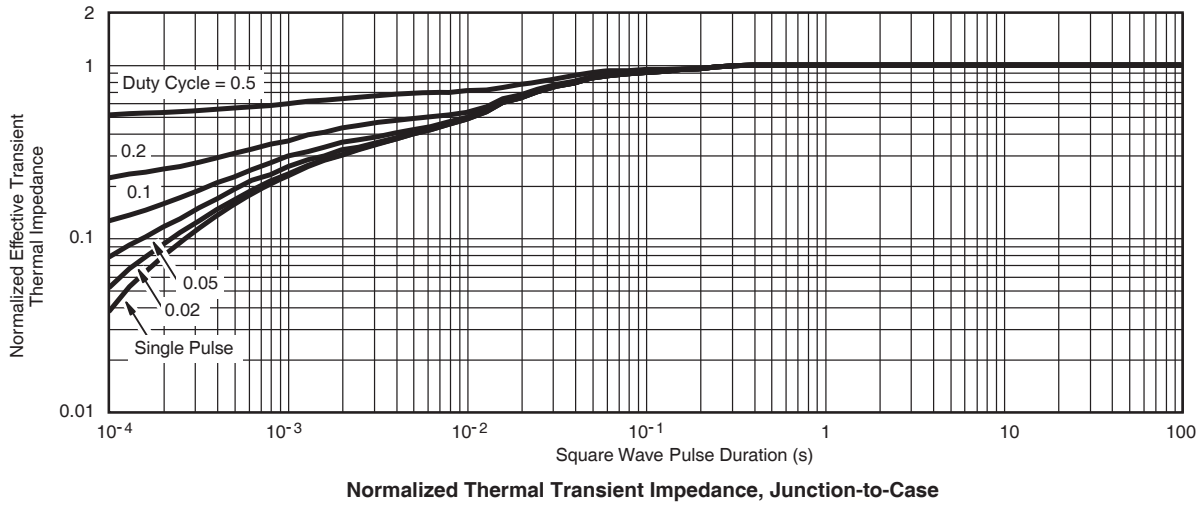
**Maximum Drain Current vs. Ambient Temperature**



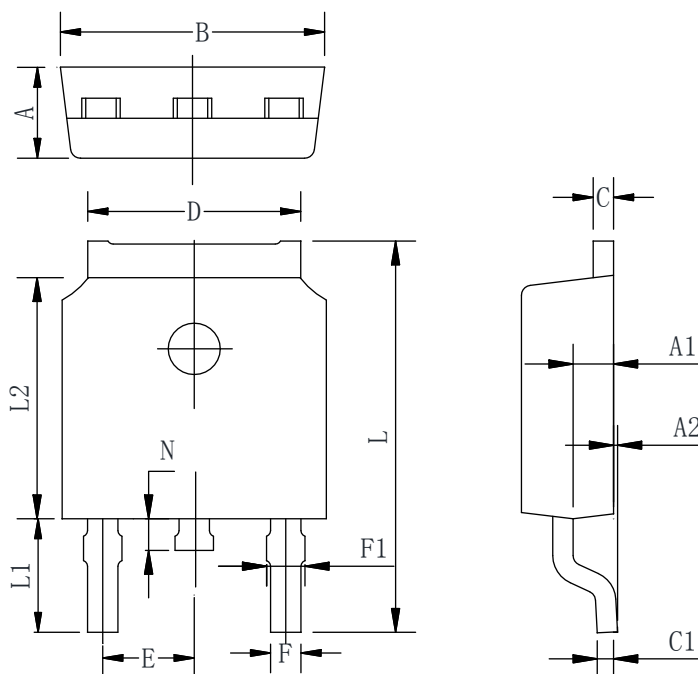
\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area**

**THERMAL RATINGS**



## TO-252-2L PACKAGE OUTLINE



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Typ	Max
A	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
B	6.40	6.60	6.80
C	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
E	2.286 TYP		
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
L	9.30	9.90	10.80
L1	2.4	2.8	3.6
L2	5.90	6.10	6.55
N	0.57	0.80	1.05

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