

N-Channel 250 V (D-S) Super Junction MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
250	235 at V _{GS} = 10 V	8	25 nC
	240 at V _{GS} = 4.5 V		

FEATURES

- DT-SJ Power MOSFET
- 100 % R_g and UIS Tested
- Low On Resistance
- Low Gate Charge

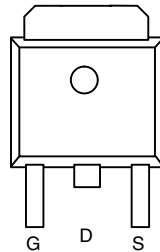
APPLICATIONS

- DC/DC Converters
- DC/AC Inverters
- Motor Drives

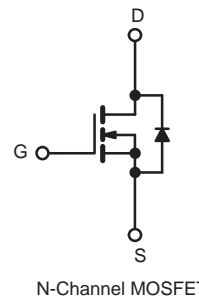


RoHS
COMPLIANT

TO-252 Pin Configuration



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	250	V	
Gate-Source Voltage	V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	8	A
		T _C = 100 °C	5	
Pulsed Drain Current	I _{DM}	32		
Single Pulse Avalanche Energy	E _{AS}	3	mJ	
Maximum Power Dissipation	P _D	T _C = 25 °C	62.5	W
		T _C = 100 °C	25	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	R _{thJA}	-	35	°C/W	
Maximum Junction-to-Case (Drain)	R _{thJC}	-	2		

Notes:

a. Based on T_C = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	250			V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		2.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 200\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 200\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			100	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 10\text{ V}, V_{GS} = 10\text{ V}$	8			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 3\text{ A}$		235	305	m Ω
		$V_{GS} = 4.5\text{ V}, I_D = 3\text{ A}$		240	336	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5\text{ V}, I_D = 3\text{ A}$		12		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 125\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		894		pF
Output Capacitance	C_{oss}			36		
Reverse Transfer Capacitance	C_{rss}			22		
Total Gate Charge	Q_g	$V_{DS} = 125\text{ V}, V_{GS} = 10\text{ V}, I_D = 3\text{ A}$		25		nC
Gate-Source Charge	Q_{gs}			2.6		
Gate-Drain Charge	Q_{gd}			6.4		
Gate Resistance	R_g	$f = 1\text{ MHz}$		3.2		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 125\text{ V},$ $I_D \cong 3\text{ A}, V_{GEN} = 10\text{ V}, R_g = 6\text{ }\Omega$		10		ns
Rise Time	t_r			18		
Turn-Off Delay Time	$t_{d(off)}$			49		
Fall Time	t_f			96		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			8	A
Pulse Diode Forward Current (100 μs)	I_{SM}				32	
Body Diode Voltage	V_{SD}	$I_S = 1\text{ A}$			1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 3\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		68		ns
Body Diode Reverse Recovery Charge	Q_{rr}				158	

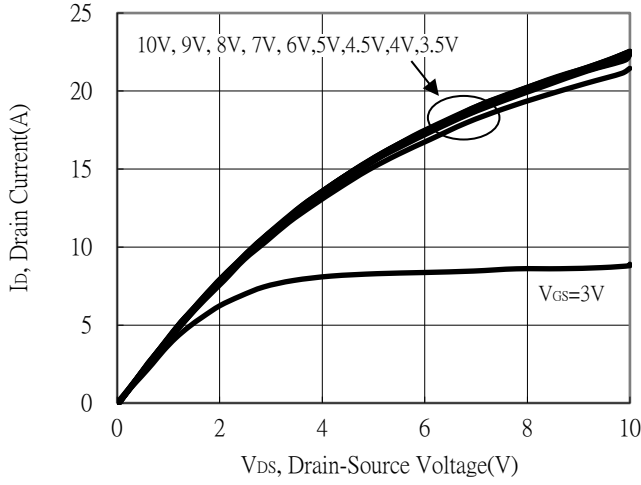
Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

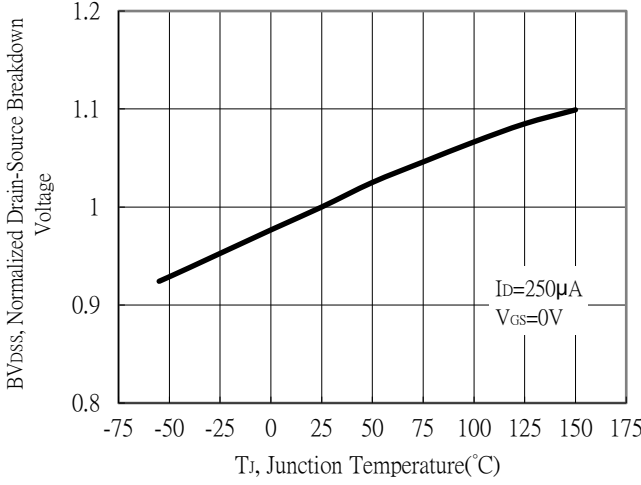
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

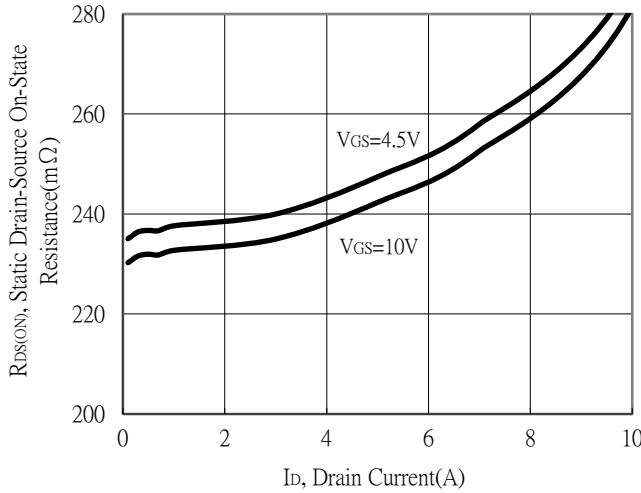
Typical Output Characteristics



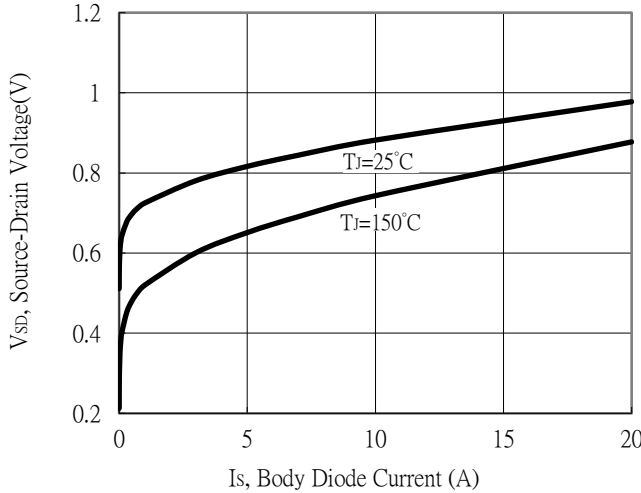
Breakdown Voltage vs Ambient Temperature



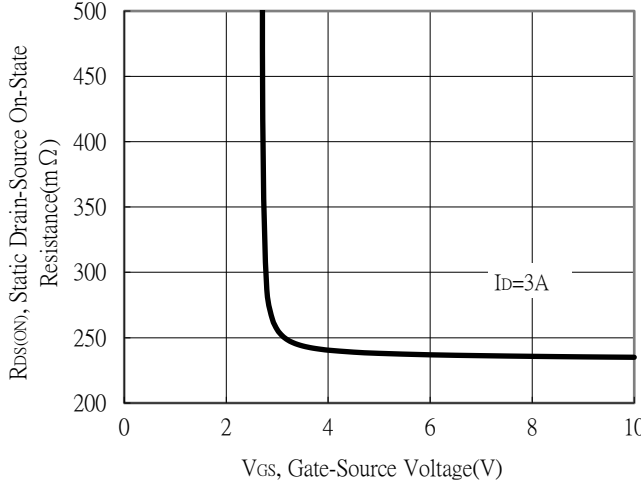
Static Drain-Source On-State resistance vs Drain Current



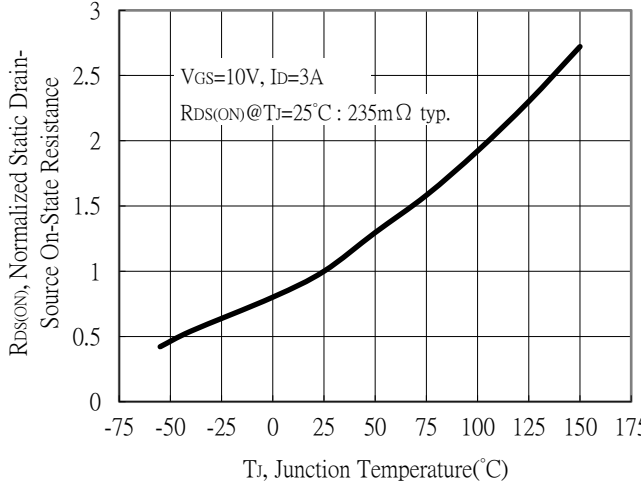
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

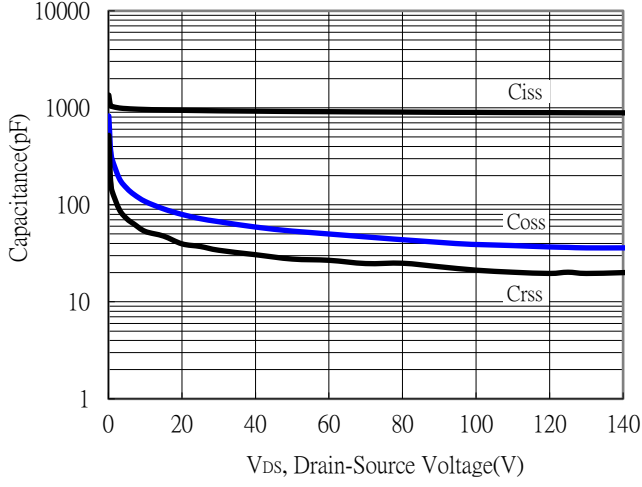


Drain-Source On-State Resistance vs Junction Temperature

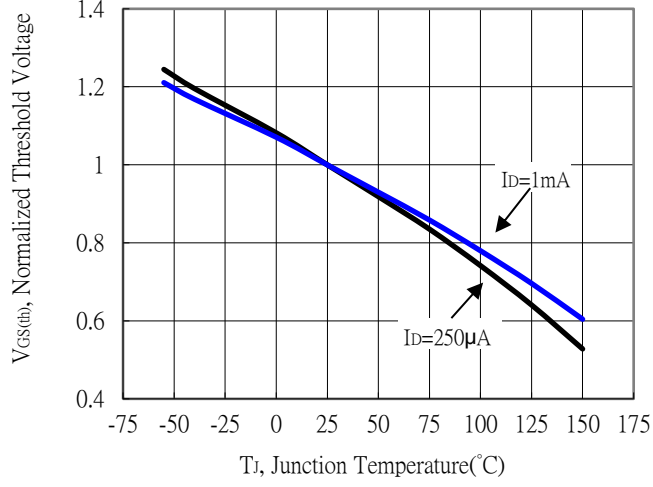


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

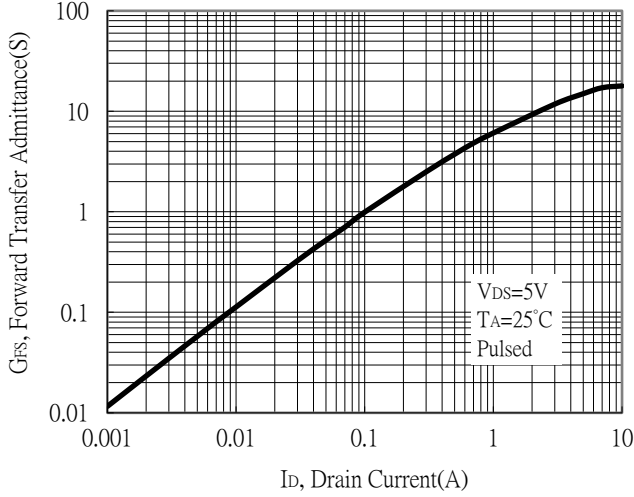
Capacitance vs Drain-to-Source Voltage



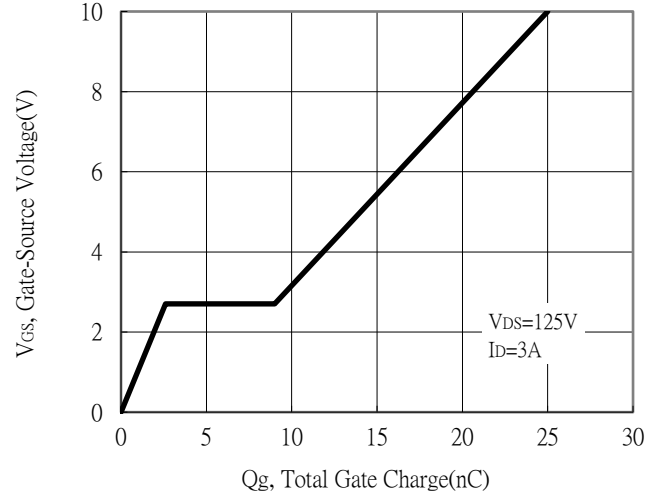
Threshold Voltage vs Junction Temperature



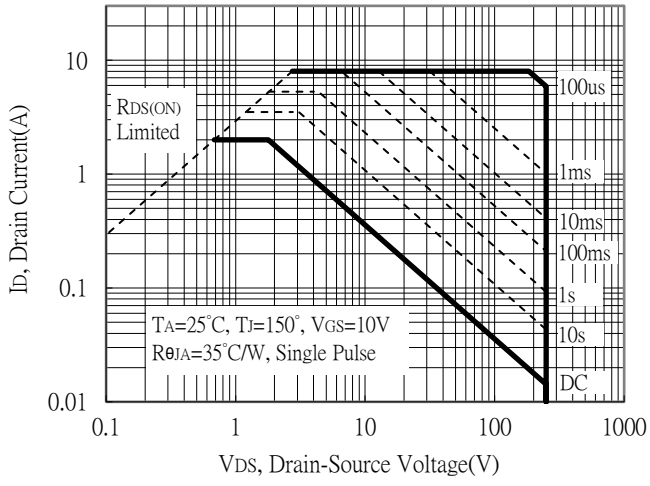
Forward Transfer Admittance vs Drain Current



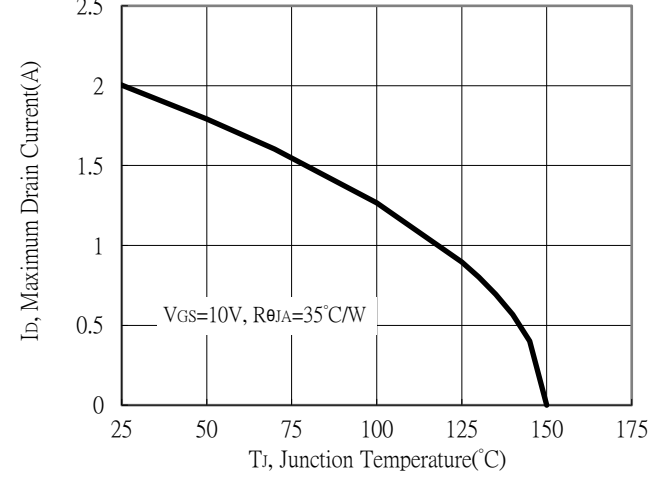
Gate Charge Characteristics



Maximum Safe Operating Area

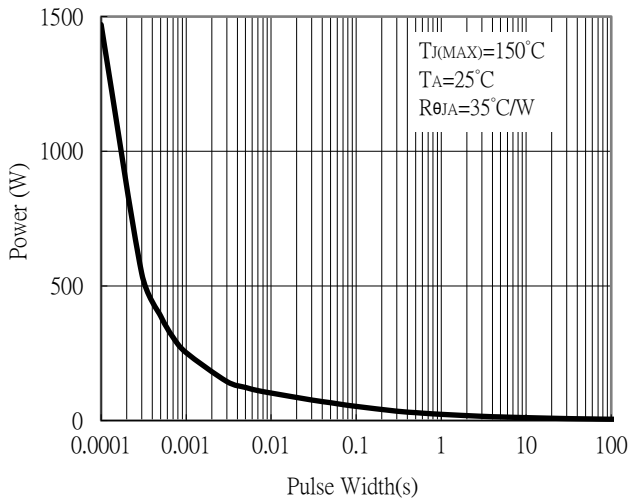


Maximum Drain Current vs Junction Temperature

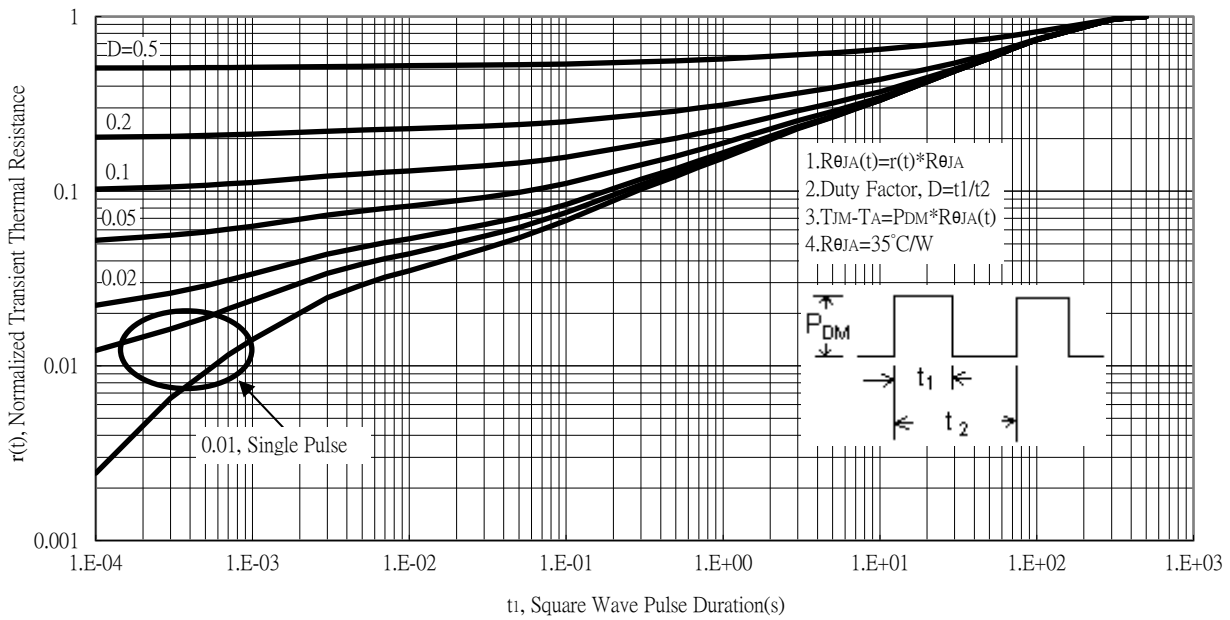


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves



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