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# P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(Typ.)	I <sub>D</sub> (A)		
- 60	70 at V <sub>GS</sub> = - 10 V	- 35		
	85 at V <sub>GS</sub> = - 4.5 V	- 25		

#### **FEATURES**

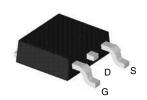
- DT-Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested



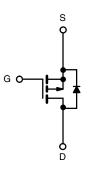
### **APPLICATIONS**

Load Switch

## TO-252 Pin Configuration



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage		V <sub>DS</sub>	- 60	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	7			
Continuous Drain Current (T <sub>.I</sub> = 150 °C)	T <sub>C</sub> = 25 °C	I_	- 35	٨		
Continuous Drain Current (1) = 130 C)	T <sub>C</sub> = 100 °C	l <sub>D</sub>	- 26			
Pulsed Drain Current	I <sub>DM</sub>	- 125	Α			
Avalanche Current	I <sub>AS</sub>	- 30	1			
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	116	mJ		
Dougs Dissingtion	T <sub>C</sub> = 25 °C	P <sub>D</sub>	133 <sup>c</sup>	W		
Power Dissipation	T <sub>A</sub> = 25 °C	r D	2.9°			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Junction-to-Ambient <sup>b</sup>	t ≤ 10 s	R <sub>thJA</sub>	10	15	°C/W	
Junction-to-Ambient	Steady State	' ¹thJA	30	45		
Junction-to-Case		$R_{thJC}$	0.7	1.1		

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. When mounted on 1" square PCB (FR-4 material).
- c. See SOA curve for voltage derating.



Parameter Symb		bol Test Conditions		Min. Typ.		Unit	
Static	•			•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50	μΑ	
		V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			- 100		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 35			Α	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10A		70	85	mΩ	
	D	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A, T <sub>J</sub> = 125 °C			96		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A, T <sub>J</sub> = 150 °C			115		
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5 A		85	105		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 10A		15		S	
Dynamic <sup>b</sup>	•				·		
Input Capacitance	C <sub>iss</sub>			860		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = - 30V,= 1 MHz		51			
Reverse Transfer Capacitance	C <sub>rss</sub>			40			
Total Gate Charge <sup>c</sup>	Qg			25			
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A}$		14		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Gate Resistance	$R_g$	f = 1 MHz		19		Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			11			
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = - 30 V, $R_L$ = 0.6 $\Omega$		17			
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN} =$ - 10 V, $R_G =$ 6 Ω		33		ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	1		15			
Source-Drain Diode Ratings and Cha	aracteristics	T <sub>C</sub> = 25 °C <sup>b</sup>					
Continuous Current	I <sub>S</sub>				- 35	^	
Pulsed Current	I <sub>SM</sub>				- 125	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 30 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.5	V	
		I <sub>F</sub> = - 10 A, dl/dt = 100 A/μs		26		ns	

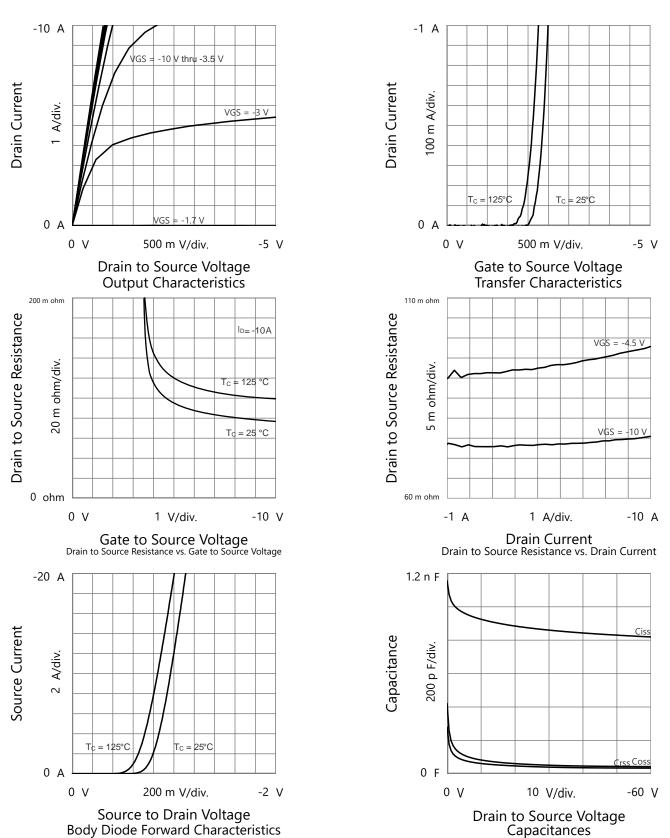
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

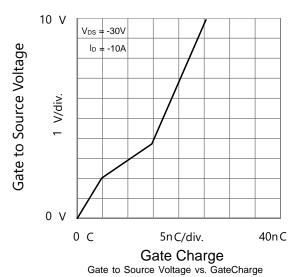
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

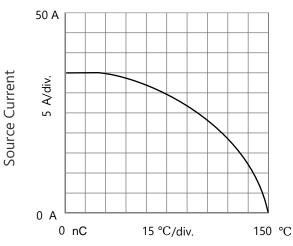


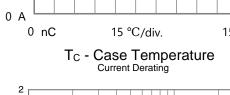
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

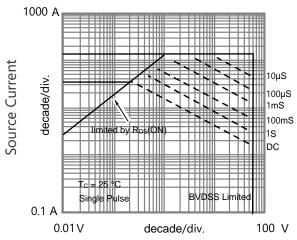


#### **TYPICAL CHARACTERISTICS**

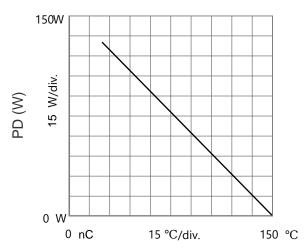




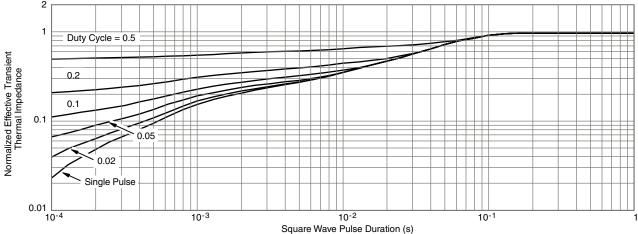




Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



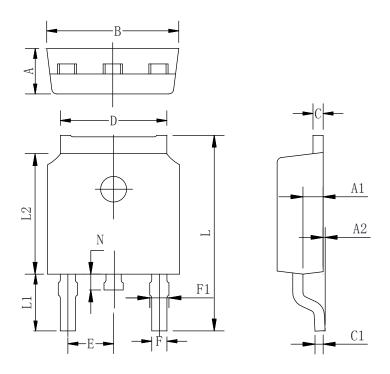
 $T_{C}$  - Case Temperature  $_{\text{Power Derating}}$ 



Normalized Thermal Transient Impedance, Junction-to-Case

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# **TO-252-2L PACKAGE OUTLINE**



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max	
A	2.10	2.30	2.50	
A1	0.88	1.01	1.16	
A2	0.00	0.15	0.28	
В	6.40	6.60	6.80	
С	0.42	0.50	0.63	
C1	0.42	0.50	0.63	
D	5.08	5.32	5.65	
Е	2.286 TYP			
F	0.63	0.76	0.89	
F1	0.64	0.86	1.08	
L	9.30	9.90	10.80	
L1	2.4	2.8	3.6	
L2	5.90	6.10	6.55	
N	0.57	0.80	1.05	



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