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P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY V_{DS} (V) $R_{DS(on)}$ (mΩ)(Typ.) I_D (A)a Q_g (Typ.) - 60 $\frac{16 \text{ at V}_{GS} = -10 \text{ V}}{20 \text{ at V}_{GS} = -4.5 \text{ V}}$ - 50 65 nC

FEATURES

- DT-Trench Power MOSFET
- 100 % R_q and UIS Tested
- Low Rdson
- Excellent package for good heat dissipation



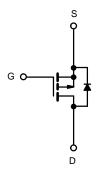
APPLICATIONS

· Load switch

TO-252 Pin Configuration

Top View





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	- 60	V		
Gate-Source Voltage		V _{GS}	± 20	1 '	
Continuous Drain Current /T 450° C)3	T _C = 25 °C	1	- 50		
Continuous Drain Current (T _J = 150° C) ^a	T _C = 100 °C	l _D	- 37	Α	
Pulsed Drain Current ^b		I _{DM}	- 200		
Single Avalanche Energy		E _{AS}	245	mJ	
Mariana Bana Birahatian	T _C = 25 °C	P _D	160	W	
Maximum Power Dissipation ^c	T _C = 100 °C	64	٧٧		
Operating Junction and Storage Temperature R	Range	T _J , T _{stg}	- 55 to + 150	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	50	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.78	C/VV	

Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. $\,$ Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R_{6JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 60	-	- V		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1 -		- 3	7 v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$	-	-	± 100	nA	
Zero Gate Voltage Drain Current	1	V _{DS} = -60 V, V _{GS} = 0 V	-	-	- 1	μΑ	
Zero Gate Voltage Drain Guirent	I _{DSS}	V_{DS} =-48V, V_{GS} = 0 V, T_{J} = 55 °C	-	-	- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 50	-	-	Α	
Drain-Source On-State Resistance ^a	D- ac	V _{GS} = - 10 V, I _D = - 20 A	-	16	22	~0	
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 15 A	-	20	28	mΩ	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 5 V, I _D = - 20 A	-	27	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	7590	-		
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = -30 \text{ V}, f = 1 \text{ MHz}$	-	315	-	pF	
Reverse Transfer Capacitance	C _{rss}		-	329	-		
Total Gate Charge ^c	Qg		-	65	-		
Gate-Source Charge ^c	Q_{gs}	V _{DS} = - 30 V, V _{GS} = - 10 V, I _D = - 20 A	-	9.5	-	nC	
Gate-Drain Charge ^c	Q_{gd}		-	17	-		
Gate Resistance	R_g	f = 1 MHz	=	3.2	-	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	18.5	-		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_{q} = 3 \Omega,$	-	21	-		
Turn-Off Delay Time ^c	t _{d(off)}	I _D = - 20 A , V _G s = - 10 V	-	55	-	ns	
Fall Time ^c	t _f		-	36	-		
Drain-Source Body Diode Ratings and	Characterist	ics ^b (T _C = 25 °C)					
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	- 50	Α	
Pulsed Current	I _{SM}		-	-	- 200	Α	
Forward Voltage ^a	V _{SD}	I _F = - 1 A, V _{GS} = 0 V	-	-	-1.2	V	
Reverse Recovery Time	t _{rr}	I _F = - 20 A, di/dt = 100 A/µs	-	51	-	ns	
Reverse Recovery Charge	Q _{rr}	1 _F = - 20 A, αι/αι - 100 Α/μ5	-	75	-	nC	

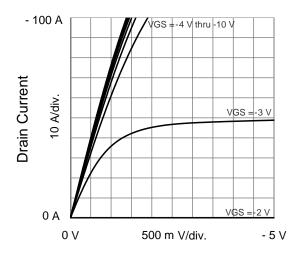
Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

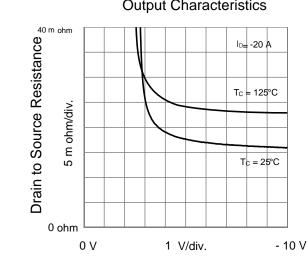
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended pe riods may affect device reliability.



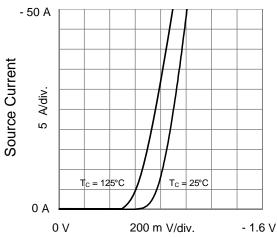
TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)



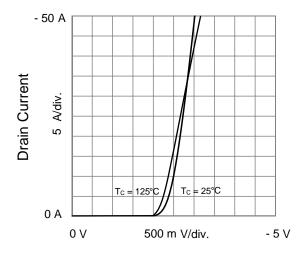
Drain to Source Voltage Output Characteristics



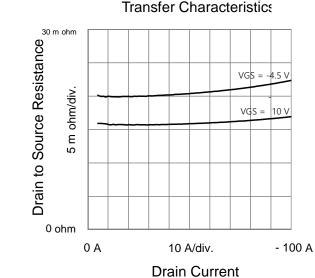
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



O F Ciss

Capacitance

Ciss

Ciss

Ciss

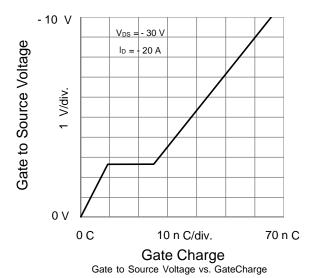
Crss

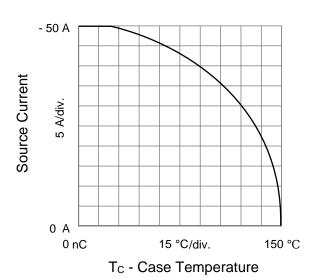
O V 10 V/div. - 60 V

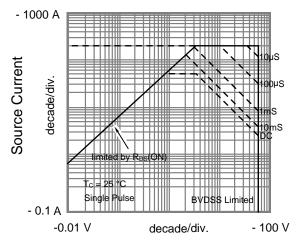
Drain to Source Voltage Capacitances



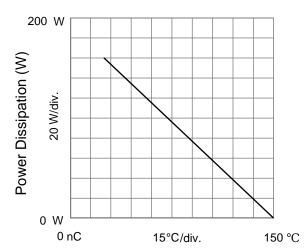
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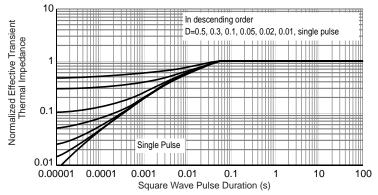




Source to Drain Voltage Safe Operating Area, Junction-to-Case



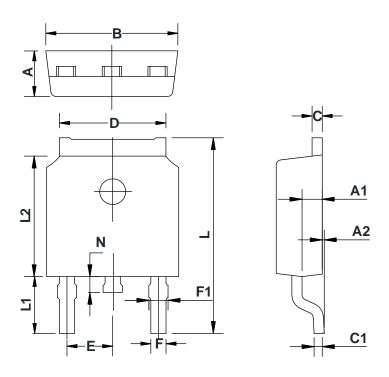
T_C - Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Case



TO-252-2L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEA SURE=MILLIMETER)

Symbol	Min	Тур	Max
А	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
В	6.40	6.60	6.80
С	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
Е	2.286 TYP		
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
Г	9.30	9.90	10.80
L1	2.40	2.80	3.60
L2	5.90	6.10	6.55
N	0.57	0.80	1.05





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