

**DTU60P02** www.din-tek.jp

# P-Channel 20 V (D-S) MOSFET

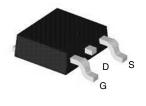
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>		
- 20	0.007 at V <sub>GS</sub> = - 4.5 V	- 60		
	0.009 at $V_{GS}$ = - 2.5 V	- 53		

#### **FEATURES**

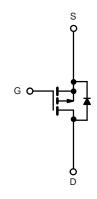
- DT-Trench Power MOSFET 100 % R<sub>g</sub> Tested 100 % UIS Tested
- •



**TO-252** Pin Configuration



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Gate-Source Voltage	V <sub>GS</sub>	± 12	V			
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C		- 60 <sup>a</sup>	А		
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	- 45			
Pulsed Drain Current	I <sub>DM</sub>	- 220	A			
Avalanche Current	I <sub>AR</sub>	- 60				
Repetitive Avalanche Energy <sup>b</sup>	L = 0.1 mH	E <sub>AR</sub>	130	mJ		
Power Dissipation	T <sub>C</sub> = 25 °C (TO-220AB and TO-263)	Р	127 <sup>d</sup>	W		
	T <sub>A</sub> = 25 °C (TO-263) <sup>c</sup>	P <sub>D</sub>	3.15			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Limit	Unit		
Junction-to-Ambient	PCB Mount (TO-263) <sup>c</sup>	Р	35	°C/W		
Junction-to-Ambient	Free Air (TO-220AB)	– R <sub>thJA</sub>	55			
Junction-to-Case		R <sub>thJC</sub>	0.8			

Notes:

a. Package limited.

b. Duty cycle  $\leq$  1 %.

c. When mounted on 1" square PCB (FR-4 material).

d. See SOA curve for voltage derating.

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

Parameter Symbol		Test Conditions Mir		Min. Typ.		Unit	
Static							
Drain-Source Breakdown Voltage		$V_{GS} = 0 V, I_{D} = -250 \mu A$	- 20			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.5		- 2.5	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 16 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			- 50	μΑ	
		$V_{DS}$ = - 16 V, $V_{GS}$ = 0 V, $T_{J}$ = 175 °C			- 250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -10 V$	- 60			А	
		$V_{GS}$ = - 4.5 V, I <sub>D</sub> = - 30 A		0.007	0.0085		
Drain-Source On-State Resistance <sup>a</sup>	Б	$V_{GS}$ = - 4.5 V, I <sub>D</sub> = - 30 A, T <sub>J</sub> = 125 °C			0.010	Ω	
Diam-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS}$ = - 4.5 V, I <sub>D</sub> = - 30 A, T <sub>J</sub> = 175 °C			0.013		
		$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -20 \text{ A}$		0.009	0.0125		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 16 V, I <sub>D</sub> = - 30 A	20			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			8400		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$ , $V_{DS} = -25 V$ , f = 1 MHz		1505			
Reversen Transfer Capacitance	C <sub>rss</sub>			710			
Total Gate Charge <sup>c</sup>	Qg			160	240		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = - 15 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 30 A		32		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			30			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			27			
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, \text{ R}_{L} = 0.2 \Omega$		220			
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 30 Å, $V_{GEN}$ = - 4.5 V, $R_g$ = 2.5 $\Omega$		140		ns	
Fall Time <sup>c</sup>	t <sub>f</sub>			215		I	
Source-Drain Diode Ratings and Cha	racteristics <sup>b</sup>	(T <sub>C</sub> = 25 °C)					
Continuous Current	۱ <sub>S</sub>				- 60	А	
Pulsed Current	I <sub>SM</sub>				- 220	А	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 30 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Reverse Recovery Time	t <sub>rr</sub>			55	100	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = - 30 A, dl/dt = 100 A/μs		2.5	5	А	
Reverse Recovery Charge	Q <sub>rr</sub>	1		0.07	0.25	μC	

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

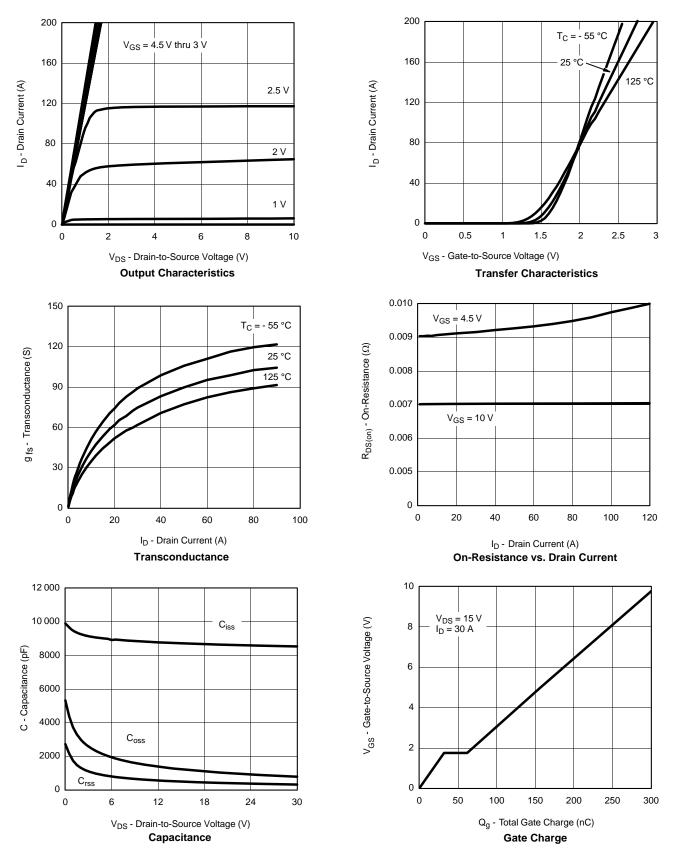
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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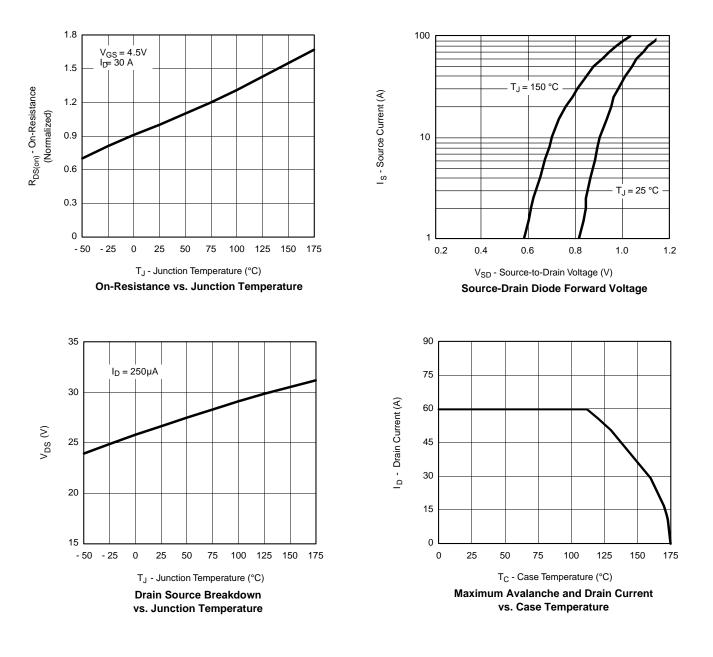
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





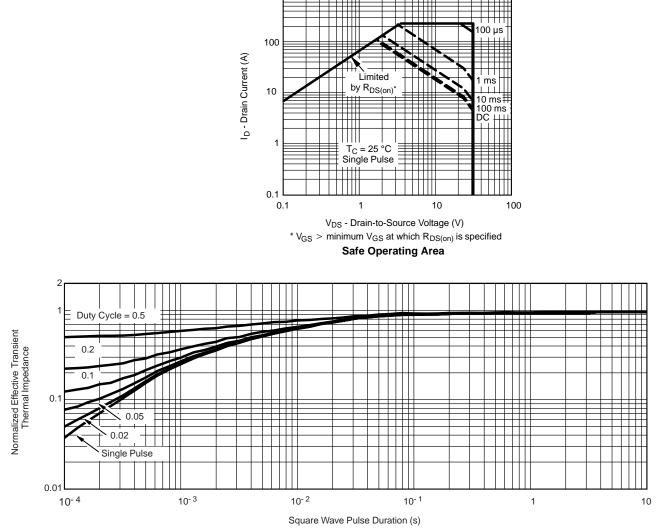
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#### THERMAL RATINGS

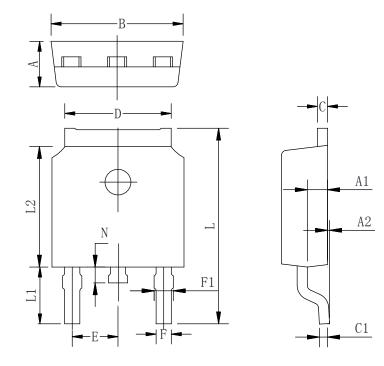


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Normalized Thermal Transient Impedance, Junction-to-Case



# TO-252-2L PACKAGE OUTLINE



#### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
А	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
В	6.40	6.60	6.80
С	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
Е	2.286 TYP		
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
L	9.30	9.90	10.80
L1	2.4	2.8	3.6
L2	5.90	6.10	6.55
N	0.57	0.80	1.05

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