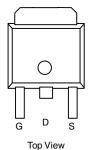


RoHS COMPLIANT

# N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
60	3.8 at $V_{GS}$ = 10 V	80	95.7 nC		



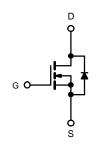


#### **FEATURES**

- 175 °C Junction Temperature
- DT-Trench Power MOSFET

#### **APPLICATIONS**

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T	<sub>A</sub> = 25 °C, unle	ess otherwise	noted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v	
Continuous Drain Current ( $T_{I} = 175 \text{ °C}$ )	T <sub>C</sub> = 25 °C	L	80 <sup>b, c</sup>		
Continuous Drain Current (1j = 175 C)	T <sub>C</sub> = 70 °C	l <sub>D</sub>	70 <sup>b, c</sup>	А	
Pulsed Drain Current		I <sub>DM</sub>	240	A	
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	85		
Single Pulse Avalanche Energy	L = 0.1 min	E <sub>AS</sub>	250	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	۱ <sub>S</sub>	80 <sup>a</sup>	А	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	PD	180 <sup>b, c</sup>	W	
	T <sub>C</sub> = 70 °C	- U	93 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b</sup>	$t \le 10 \text{ s}$	R <sub>thJA</sub>	11	15	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.75	1.0		

Notes:

a. Based on  $T_C = 25 \text{ °C}$ . b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Static		•				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = 250 \mu A$	60			V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		3	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	100			А
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		3.8	4.9	mΩ
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		80		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			5010		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, f = 1 MHz		398		
Reverse Transfer Capacitance	C <sub>rss</sub>			337		
Total Gate Charge	Qg			95.7		nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		17		
Gate-Drain Charge	Q <sub>gd</sub>			24.5		
Gate Resistance	Rg	f = 1 MHz		1.37		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			18		ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 30 V, $R_L$ = 0.6 $\Omega$		20		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		25		
Fall Time	t <sub>f</sub>			10		
Drain-Source Body Diode Characteristics	5					
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			80	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				240	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1 A		0.6	1	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			30		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$I_{F} = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_{J} = 25 \text{ °C}$		80		nC

Notes:

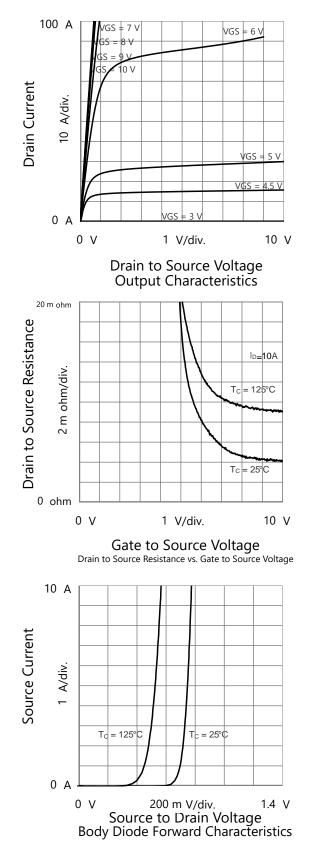
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

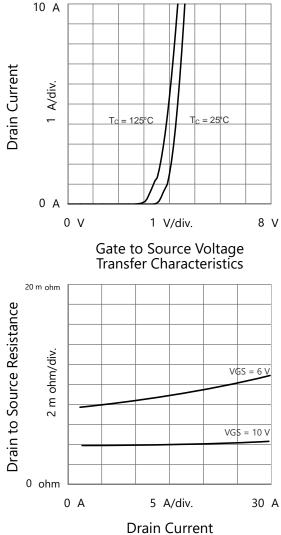
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

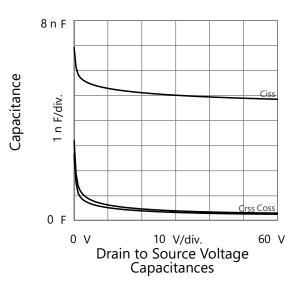


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



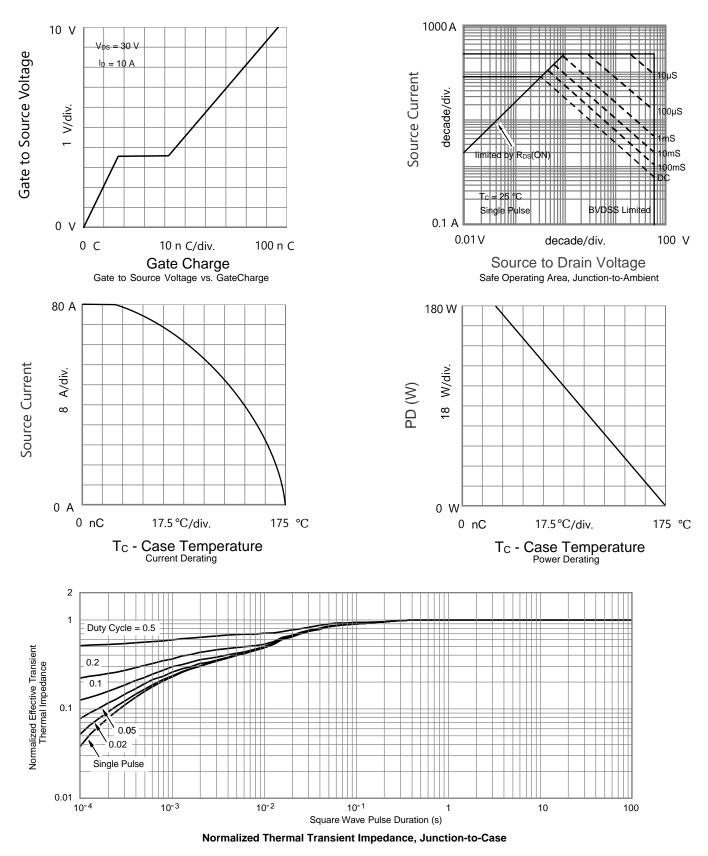


Drain to Source Resistance vs. Drain Current





#### TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)





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